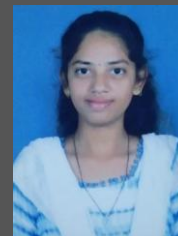


SARAVAKOTA NIROSHA

Email : saravakotanirosha@gmail.com
Contact : +91 7569762351



OBJECTIVE

Seeking a challenging position with a reputable organization where I can acquire new skills, broaden my knowledge, and use what I've learned. Possesses excellent problem-solving skills and is able to work independently or as part of a team. Committed to delivering high-quality results while continuously learning new technologies.

ACADEMIC PROFILE

Course	College/School	University/Board	Pass Out Year	Percentage /CGPA
B.Tech. (ECE)	Aditya Institute of Technology and Management, Tekkali	JNTUGV, Vizianagaram	2025	9.22 (CGPA)
Intermediate (MPC)	Gayatri Junior College, Srikakulam	Board of Intermediate Education, Andhra Pradesh	2022	95%
SSC	Sri Sai School, Tekkalipatnam	Board of Secondary Education, Andhra Pradesh	2020	9.5

TECHNICAL SKILLS

Programming : Basics of Python, C++, C, Basics of Java
Tools : HTML, MATLAB, Vivado
Database : MySQL
Operating Systems : Windows, Android

PROJECT EXPERIENCE

Project Title: Movie Magic - Smart Movie Ticket Booking System

Tools & Technologies: AWS (S3, Lambda, DynamoDB, API Gateway), ReactJS, Node.js, Cognito

Description:

Designed and developed a cloud-based movie ticket booking system to simplify the user experience by integrating real-time seat availability, personalized movie suggestions, and a secure booking flow. The platform eliminates fragmented booking systems by providing a unified, fast, and transparent interface for users. Movie Magic is a fully cloud-hosted movie ticket booking system that simplifies and enhances the process of selecting movies, checking real-time seat availability, and completing secure bookings. The platform is designed to be lightweight, scalable, and mobile-friendly

INTERNSHIP EXPERIENCE

Title: VLSI soc design using Verilog HDL

Organization: Maven silicon

Duration: 2 months

VLSI SoC design leverages Verilog HDL to model complex integrated circuits on a single chip, where multiple functional blocks like processors, memory, and peripherals are interconnected and described using modular code, enabling efficient simulation, synthesis, and layout for high-performance system-on-

chip development. It is widely used for Soc design due to its efficiency in describing hardware behavior, supporting Complex designs, and compatibility with simulation.

CERTIFICATIONS

- Completed NPTEL Certification on “**Internet of Things**” issued by Indian Institute of Technology Kharagpur.
- Completed NPTEL Certification on “**System Design Through Verilog**” issued by Indian Institute of Technology Guwahati.
- Certified in “**PCB design**” issued by APSSDC.
- Certified in “**Internet of Things**” issued by APSSDC.

EXTRA CURRICULAR ACTIVITIES

- Won medal in Kho-Kho in my school
- An active member of NSS as a NSS Volunteer

PERSONAL SKILLS

- friendly nature
- Good communication skills.

PERSONAL DETAILS

Father's Name : Saravakota Tirupathi Rao

Date of Birth : 23/06/2005

Address : D/no:1-1801, Tekkalipatnam(Village) palasa(mandal)
Srikakulam(district), Andhra Pradesh(state).
Pincode:532220

Nationality : Indian

Marital status : unmarried

Hobbies/Interests : Listening to music, Cooking

Languages Known :

Language	Speak	Read	Write
English	✓	✓	✓
Telugu	✓	✓	✓
Hindi	_____	✓	✓

DECLARATION

I hereby declare that the details furnished above are true and correct.

Date: 20/07/2025

Place: Tekkali

(SARAVAKOTA NIROSHA)

