DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

7th January, 2015

CIRCULAR

SUB: CONDUCTION OF ASSIGNMENT EXAMINATIONS

All the Course Coordinators handling classes for III/IV and IV/IV B.Tech are hereby informed that the Assignment examinations will be conducted as per the Academic Calendar from **19th January, 2015** as per the Time Table given below, in this regard they are requested to follow format given from DEAN-ACADEMICS office and submit the copies of the question papers by **09.01.2015**.

TIMETABLE FOR CONDUCTION OF ASSIGNMENT EXAMINATION

S. No.	Date of Exam.	Year	Time.	COURSE CODE & TITLE
1	19.01.2015	III	09:05 TO 09:55 A.M.	Automata and Formal Languages
		IV	07:20 TO 08:10 A.M.	Distributed Systems
3	20.01.2015	III	09:05 TO 09:55 A.M.	Object Oriented Analysis and Design
		IV	07:20 TO 08:10 A.M.	Complier Design
3	21.01.2015	III	09:05 TO 09:55 A.M.	Parallel Processing
		IV	07:20 TO 08:10 A.M.	**Professional Elective- IV**
4	22.01.2015	III	09:05 TO 09:55 A.M.	**Professional Elective-II**
		IV	07:20 TO 08:10 A.M.	**Professional Elective-V**
5	23.01.2015	III	09:05 TO 09:55 A.M.	**Professional Elective-III**
		IV	07:20 TO 08:10 A.M.	**Open Elective-I**
6	25.01.2015	III	09:05 TO 09:55 A.M.	**HS Elective**
		IV	07:20 TO 08:10 A.M.	**Open Elective-II**

T.PAVAN KUMAR   **DR. THIRUPATHI RAO**
Exam-Section In-charge-CSE   **HOD –CSE**

Copy to: 3rd and 4th Year Academic Coordinators/ All Course Coordinator.