```
PITController< ChID >
+ Actual period
+ Req period
+ Clk frea
+ LoadVal
+ PIT MAX COUNTER
+ PITController()
+ start()
+ stop()
+ set gate time()
+ period error us()
+ tick period us()
+ interrupt()
+ no interrupt()
+ clear interrupt flag()
+ sel FBUS clock()
+ sel 24MHz clock()
+ enable PITs()
+ disable PITs()
+ pause resume PITs()
+ get xbar in pin()
+ set interrupt()
```