

# Demo 01: Icarus Verilog

Eddy Yau

April 29, 2018

## Verilog

Verilog and VHDL are the most commonly used **HDL**(Hardware Description Language) in the world. On the other hand, high-level synthesis will utilize SystemC instead. Comparing with verilog and VHDL, verilog is more easily to study, since verilog is similar to C.

Here is some example code for a NAND gate in verilog:

```
1 module NAND {
2     input  A,
3     input  B,
4     output Z
5 };
6 // Assign nand-gate logic to Z
7 assign Z=~A&B;
8 endmodule
```