

January \*\*\*

## CURRICULUM VITAE

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### Personal Information

Date of Birth: \*\*\*

Address:

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Postal Code: \*\*\*

Phone: \*\*\*

Cellphone: \*\*\*

E-mail: \*\*\*

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### Objectives:

Seeking a scholarship, Research assistantship or grant towards my M.S. and Ph.D. programs in the field of Electrical Engineering with special interests in Communications systems, Wireless Communication, Information and Coding Theory, Random Signal Processing, Network Analysis.

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### Education:

- BS in Electrical Engineering, Communications, University of Tehran, Tehran, IRAN, \*\*\*  
Expected graduation date: \*\*\*

Total GPA: \*\*\*/20.0 (up to now)

GPA of 1<sup>st</sup> semester: \*\*\*

GPA of 2<sup>nd</sup> semester: \*\*\*

GPA of 3<sup>rd</sup> semester: \*\*\*

GPA of 4<sup>th</sup> semester: \*\*\*

GPA of 5<sup>th</sup> semester: \*\*\*

GPA of 6<sup>th</sup> semester: \*\*\*

GPA of 7<sup>th</sup> semester: \*\*\*

- Pre university degree, \*\*\* , Tehran, Iran,\*\*\*;  
Total GPA: \*\*\*/20

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### Honors:

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## Accomplished Projects:

- Design and implementation of a very precise current source (mA and nA), Microprocessor project, spring \*\*\*.
- Speech processing and Image processing, Discrete-time Signal Processing , spring \*\*\*.
- Design and implementation of a Rotary Joints using HFSS in K band(18 to 27 GHz), Microwave, \*\*\*.
- Implementing a digital coding system using MATLAB, Communication II. \*\*\*.
- Finding the most economical way for doing a specific project using MS project, Operations Research ,\*\*\*
- Design and implementation of a low noise amplifier, Electronics II laboratory. fall \*\*\*
- Design and implementation of a robot, capable of tracking paths , General workshop, fall \*\*\*
- Design and implementation of Combinational Multiplier with Binary-BCD converter, Digital logic Laboratory, Summer \*\*\*
- Design and implementation of Function Generator, Digital logic Laboratory, Summer \*\*\*
- Design and implementation of An intelligent house light turner , Digital logic Laboratory, Summer \*\*\*

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## Computer Skills:

- Programming skills in C++, Assembly of 80x86 and Verilog language.
  - Practical experience with application packages such as SPICE, MATLAB, HFSS, PROTEL, Altium, Modelsim, Quartus, MS office, ORCAD.
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## Work Experience:

- Working at \*\*\*.

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## Language Skills:

- Persian: native
- English: fluent
- Arabic: fair

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## Extracurricular Activities:

- Reading
- Watching movie
- Swimming
- Painting
- Hiking

## References:

- **Professor \*\*\*:**  
\*\*\* Laboratory, University of Tehran

Email: \*\*\*

- **Professor \*\*\*:**  
\*\*\* Laboratories, University of Tehran

Email: \*\*\*

- **Professor \*:**

Email: \*\*\*

- **Professor \*\*\*:**  
\*\*\* Laboratory, University of Tehran

Email: \*\*\*