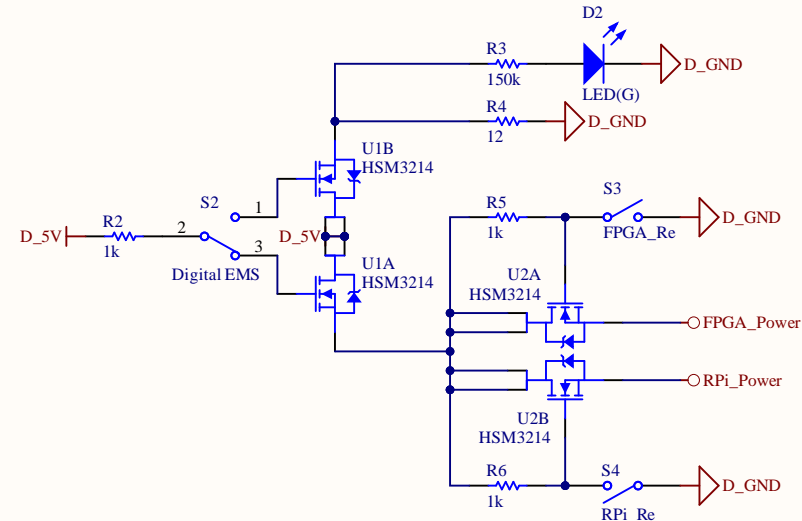
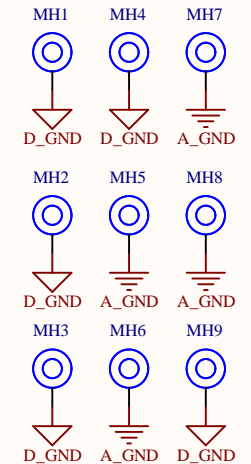
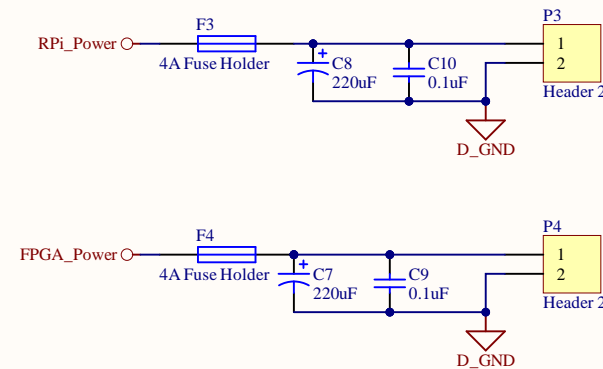
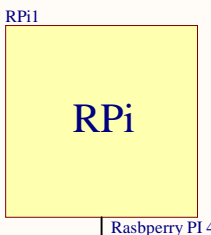
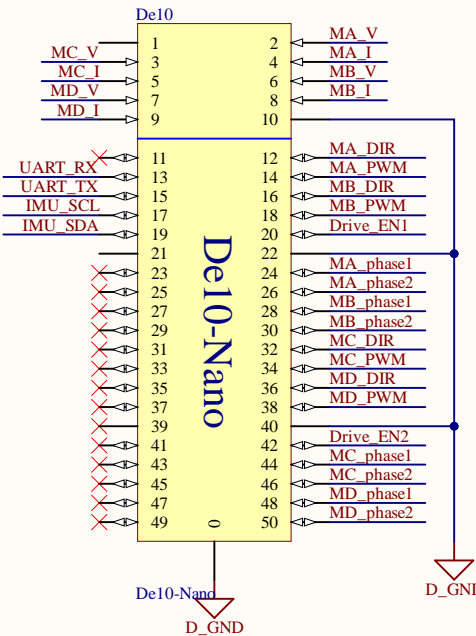
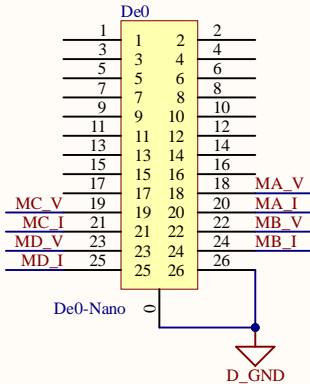
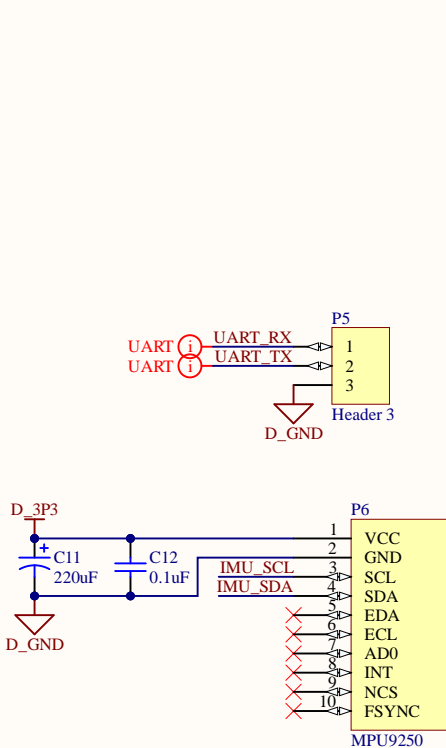


Reset & Discharge

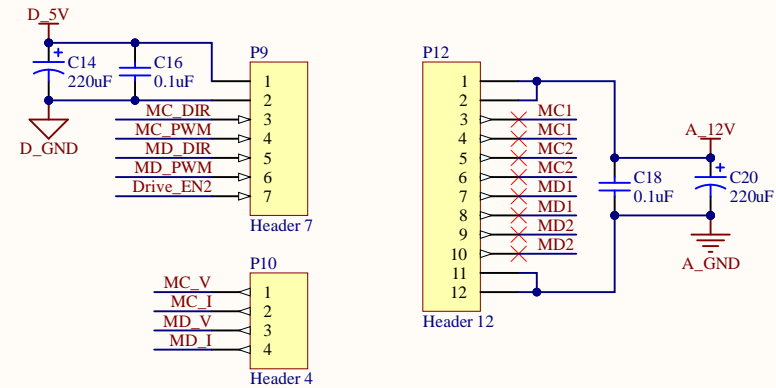
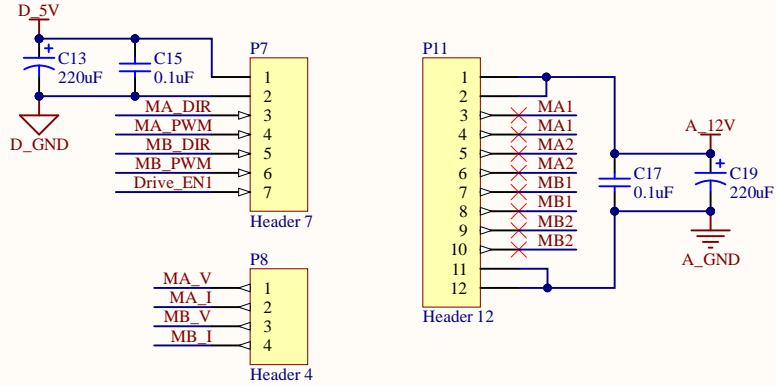


FPGA & RPi Power Input





Motor Driver



Motor Output

