

October 2011

# 2N3904 / MMBT3904 / PZT3904 NPN General Purpose Amplifier

### **Features**

- This device is designed as a general purpose amplifier and switch.
- The useful dynamic range extends to 100 mA as a switch and to 100 MHz as an amplifier.



# **Absolute Maximum Ratings\*** T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CEO</sub>	Collector-Emitter Voltage	40	V	
$V_{CBO}$	Collector-Base Voltage	60	V	
V <sub>EBO</sub>	Emitter-Base Voltage	6.0	V	
I <sub>C</sub>	Collector Current - Continuous	200	mA	
T <sub>J,</sub> T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C	

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

## Thermal Characteristics T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter		Units		
		2N3904	*MMBT3904	**PZT3904	Units
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	1,000 8.0	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3			°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	125	°C/W

 $<sup>^{\</sup>ast}$  Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06".

<sup>1)</sup> These ratings are based on a maximum junction temperature of 150 degrees C.

<sup>2)</sup> These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

<sup>\*\*</sup> Device mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead min. 6 cm $^2$ .

# **Electrical Characteristics** $T_a = 25$ °C unless otherwise noted

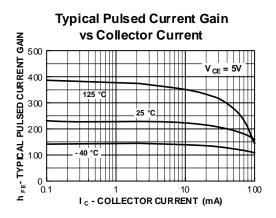
Symbol	Parameter Test Condition		Min.	Max.	Units	
OFF CHARAC	TERISTICS				•	
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage   I <sub>C</sub> = 1.0mA, I <sub>B</sub> = 0		40		V	
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 10\mu A, I_E = 0$	60		V	
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10\mu A, I_C = 0$	6.0		V	
I <sub>BL</sub>	Base Cutoff Current	$V_{CE} = 30V, V_{EB} = 3V$		50	nA	
I <sub>CEX</sub>	Collector Cutoff Current	$V_{CE} = 30V, V_{EB} = 3V$		50	nA	
ON CHARACT	TERISTICS*				•	
h <sub>FE</sub>	DC Current Gain	$ \begin{aligned} & I_{C} = 0.1 \text{mA},  V_{CE} = 1.0 \text{V} \\ & I_{C} = 1.0 \text{mA},  V_{CE} = 1.0 \text{V} \\ & I_{C} = 10 \text{mA},  V_{CE} = 1.0 \text{V} \\ & I_{C} = 50 \text{mA},  V_{CE} = 1.0 \text{V} \\ & I_{C} = 100 \text{mA},  V_{CE} = 1.0 \text{V} \end{aligned} $	40 70 100 60 30	300		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_C = 10$ mA, $I_B = 1.0$ mA $I_C = 50$ mA, $I_B = 5.0$ mA		0.2 0.3	V V	
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	$I_C = 10$ mA, $I_B = 1.0$ mA $I_C = 50$ mA, $I_B = 5.0$ mA	0.65	0.85 0.95	V V	
SMALL SIGNA	AL CHARACTERISTICS					
f <sub>T</sub>	Current Gain - Bandwidth Product $I_C = 10\text{mA}, V_{CE} = 20\text{V}, f = 100\text{MHz}$		300		MHz	
$C_{obo}$	Output Capacitance	$V_{CB} = 5.0V, I_{E} = 0,$ f = 1.0MHz		4.0	pF	
C <sub>ibo</sub>	Input Capacitance	$V_{EB} = 0.5V, I_{C} = 0,$ f = 1.0MHz		8.0	pF	
NF	Noise Figure	$I_{C} = 100\mu\text{A}, V_{CE} = 5.0\text{V},$ $R_{S} = 1.0k\Omega,$ f = 10Hz to 15.7kHz		5.0	dB	
SWITCHING (	CHARACTERISTICS			•	•	
t <sub>d</sub>	Delay Time $V_{CC} = 3.0V, V_{BE} = 0.5V$			35	ns	
t <sub>r</sub>	Rise Time	I <sub>C</sub> = 10mA, I <sub>B1</sub> = 1.0mA		35	ns	
t <sub>s</sub>	Storage Time	$V_{CC} = 3.0V, I_{C} = 10mA,$		200	ns	
t <sub>f</sub>	Fall Time	$I_{B1} = I_{B2} = 1.0 \text{mA}$		50	ns	

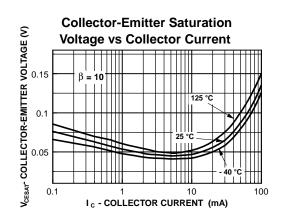
<sup>\*</sup> Pulse Test: Pulse Width  $\leq 300 \mu s$ , Duty Cycle  $\leq 2.0\%$ 

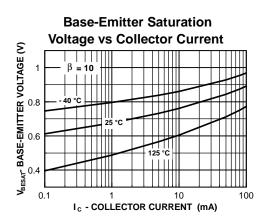
# **Ordering Information**

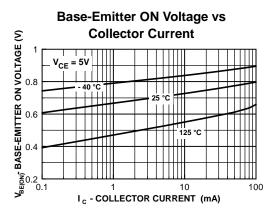
Part Number	Marking	Package	Packing Method	Pack Qty
2N3904BU	2N3904	TO-92	BULK	10000
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2N3904TAR	2N3904	TO-92	AMMO	2000
2N3904TF	2N3904	TO-92	TAPE REEL	2000
2N3904TFR	2N3904	TO-92	TAPE REEL	2000
MMBT3904	1A	SOT-23	TAPE REEL	3000
MMBT3904_D87Z	1A	SOT-23	TAPE REEL	10000
PZT3904	3904	SOT-223	TAPE REEL	2500

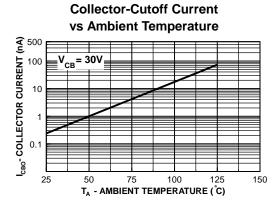
# **Typical Performance Characteristics**

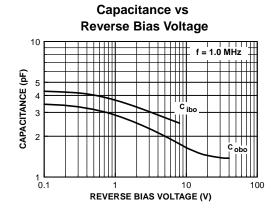




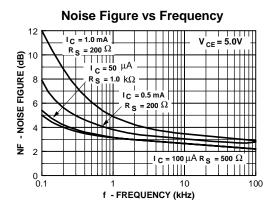


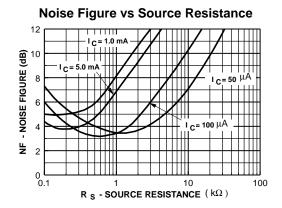


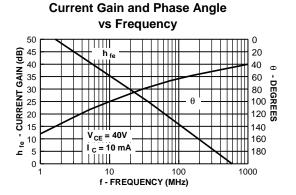


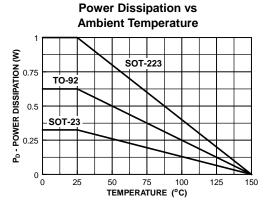


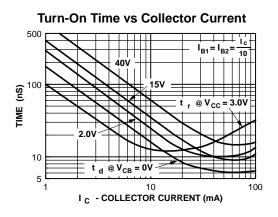
## Typical Performance Characteristics (continued)

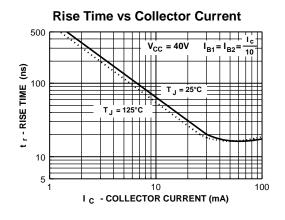




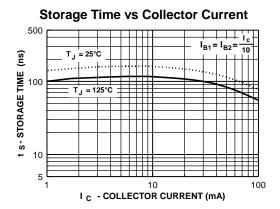


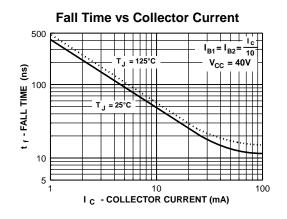


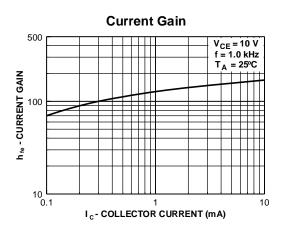


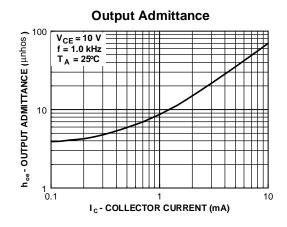


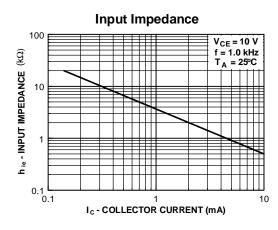
# **Typical Performance Characteristics** (continued)

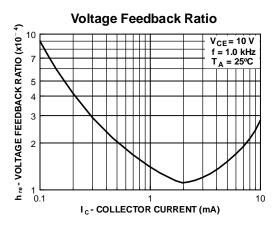












## **Test Circuits**

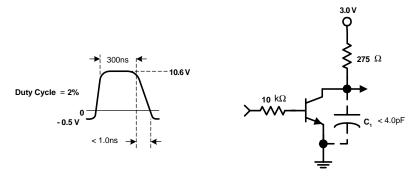


FIGURE 1: Delay and Rise Time Equivalent Test Circuit

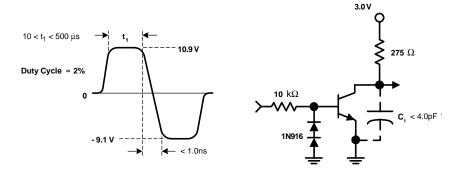


FIGURE 2: Storage and Fall Time Equivalent Test Circuit





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