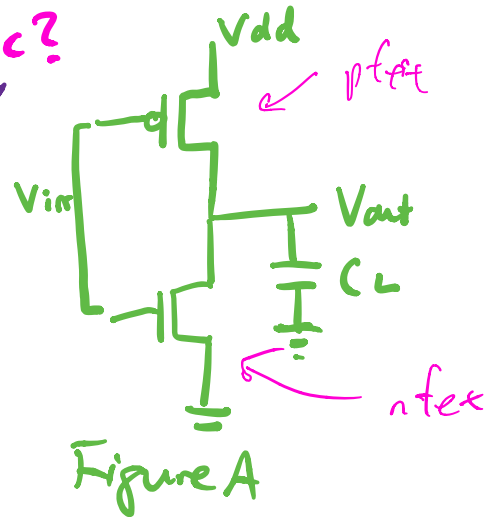
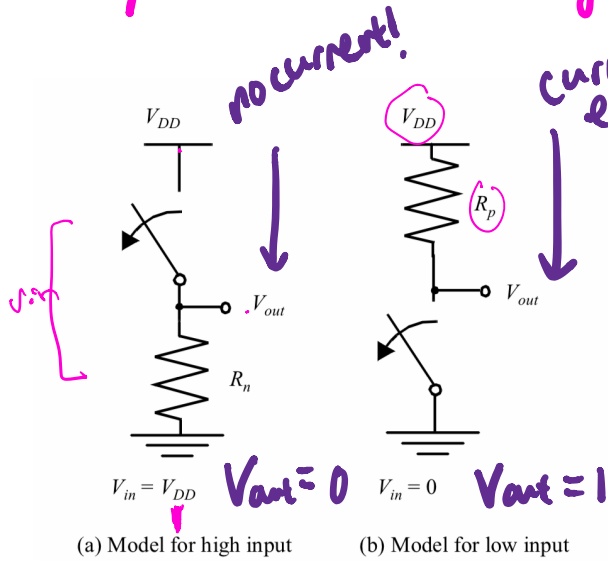
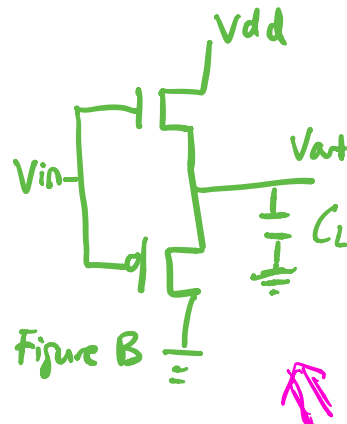
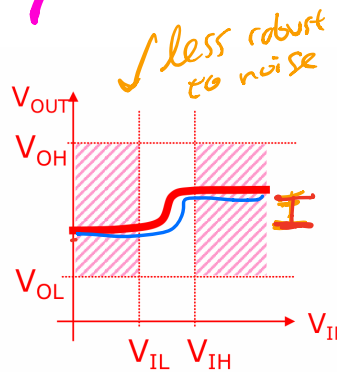
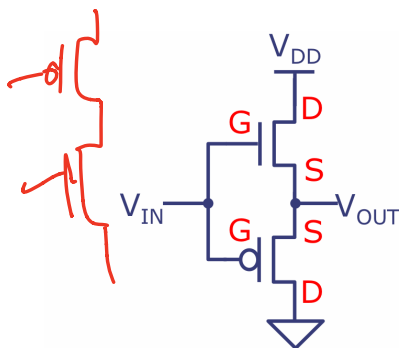


Why is CMOS inverting logic?



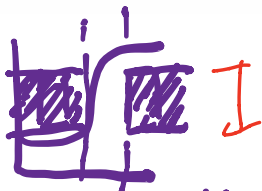
remember, NMOS & PMOS are like electrically controlled switches!!!

Great, but why can't we do this instead?



pFET doesn't pull down
 V_{OUT} below V_{TH}
 nFET doesn't pull up
 V_{OUT} above $V_{DD} - V_{TH}$

Note here V_{out} passes through the forbidden zones!



This is the VTC graph for figure A. (clearly better. (more separation between 1 & 0))