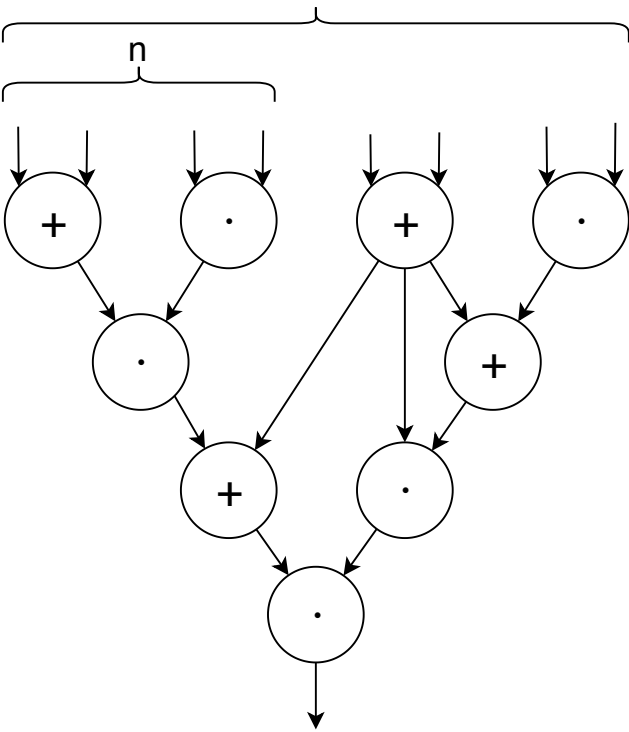
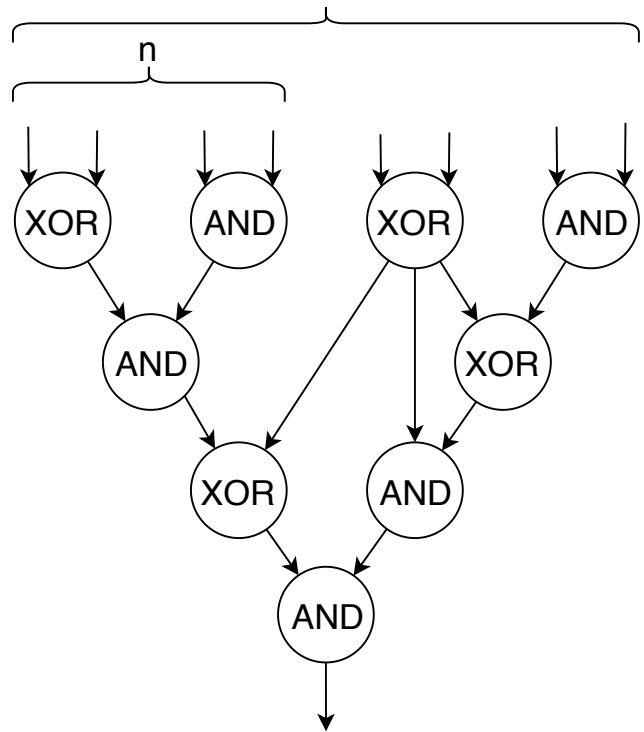


m input wires



Output wire

m input wires



Output wire