

RK3288 Tablet REF V1.5

01.Index
02.Change List
03.Block Diagram
04.Power Tree-RC5T620
05.Power Tree-RK808-B
06.Power Tree-RK818-1
10.RK3288 Power
11.RK3288 PMU Controler
12.RK3288 DDR Controler
13.RK3288 Flash Controler
14.RK3288 USB/HSIC Controler
15.RK3288 SARADC/Key Board
16.RK3288 DVP Interface
17.RK3288 Display Interface
18.RK3288 GPIO
20.USB Port
21.Power-RT5C620_1CELL (option)
22.Power-RK808-B_2CELL (option)
23.Power-RK818-1_1CELL (option)
30.RAM-DDR3 4x16bit
32.RAM-DDR3 2x32bit (option)
33.RAM-LPDDR2(168P) (option)
34.RAM-LPDDR2(216P) (option)
35.RAM-LPDDR2(220P) (option)
36.RAM-LPDDR3(178P) (option)
40.Memory-eMMC
41.Memory-Nand FLASH (option)
42.Memory-tSD (option)
45.DVP power and Flash LED
46.Camera-MIPI CSI
47.Camera-CIF (option)
50.LCM-eDP Panel
51.LCM-MIPI Panel (option)
52.LCM-Dual MIPI Panel (option)
53.LCM-LVDS Panel (option)
60.WIFI/BT-AP6210/AP6212/XZ3538
61.WIFI ac/BT-AP6335 (option)
62.WIFI/BT/GPS-AP6476 (option)
65.3G-UNA
66.3G-UNA LITE (option)
70.Audio Codec-ES8323
71.Audio Codec-ALC5631 (option)
72.Audio Codec-ALC5640 (option)
73.Audio Codec-ES8316 (option)
75.TP COF
76.TP COB-CT363 (option)
77.TP COB-FT5506 (option)
78.TP COB-GSL3680 (option)
80.Sensor/VIB
81.TF Card
82.HDMI Port
84.eFUSE (option)

I2C address(7bit):

1.I2C0 Power:

RK808 0x1b
CW2013 0x62
RK818 0x1C
RT5C620 0x32
SYR837 0x40
SYR838 0x41

2.I2C1 Sensor:

CM3218 0x10,0x0c
LSM330TR G:0x6a A:0x1e
MMA8452Q 0X1d
MPU6500 0x34
LIS3DH 0X19
LSM303D 0X1d

3.I2C2 Audio Codec:

ALC5640 0x19
ALC5623 0x1a
ALC5631 0x1a
ES8323 0x10
ES8316 0x10

4.I2C3 Camera:

OV2659 0x30
OV8825 0x36

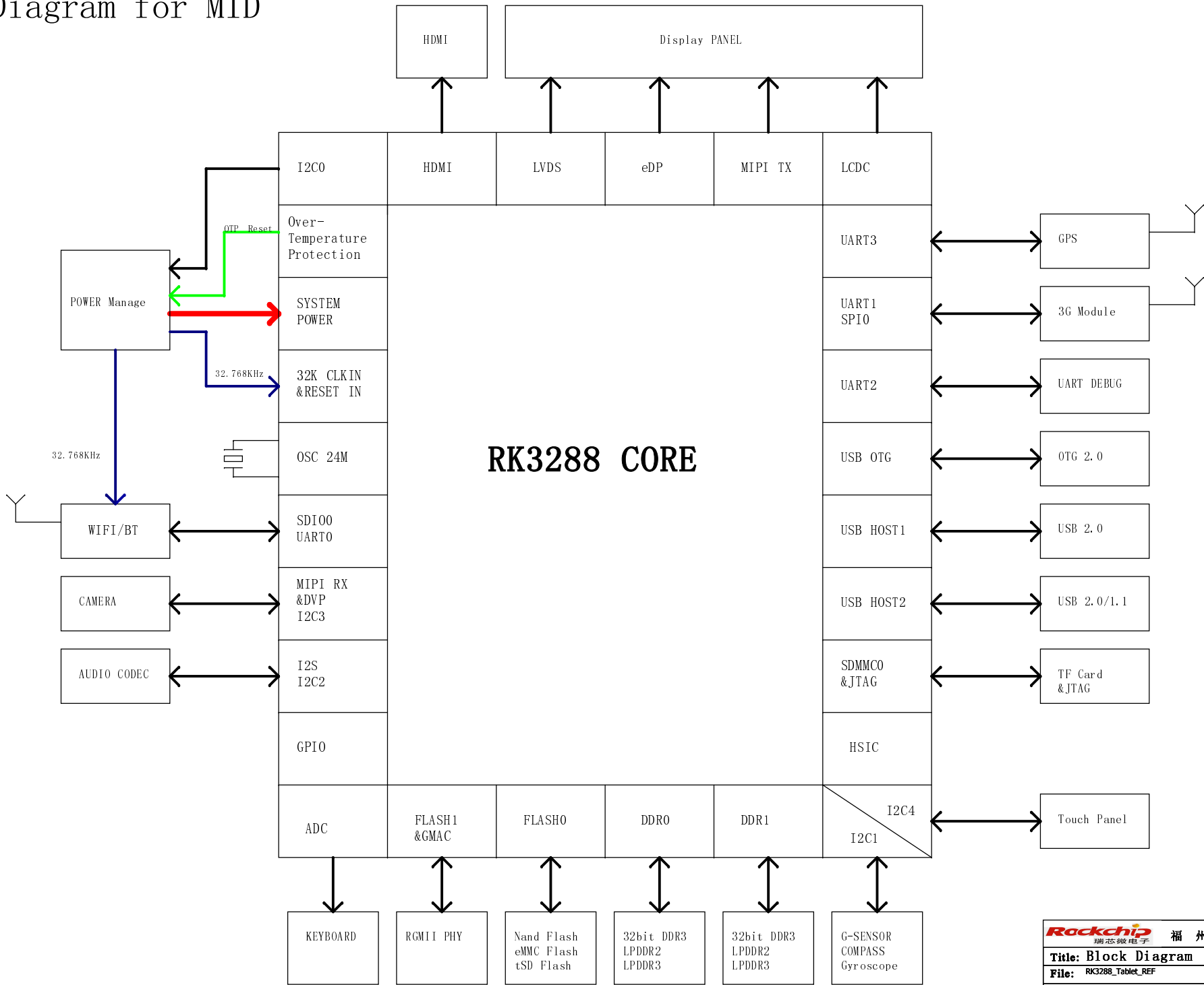
5.I2C4 Touch:

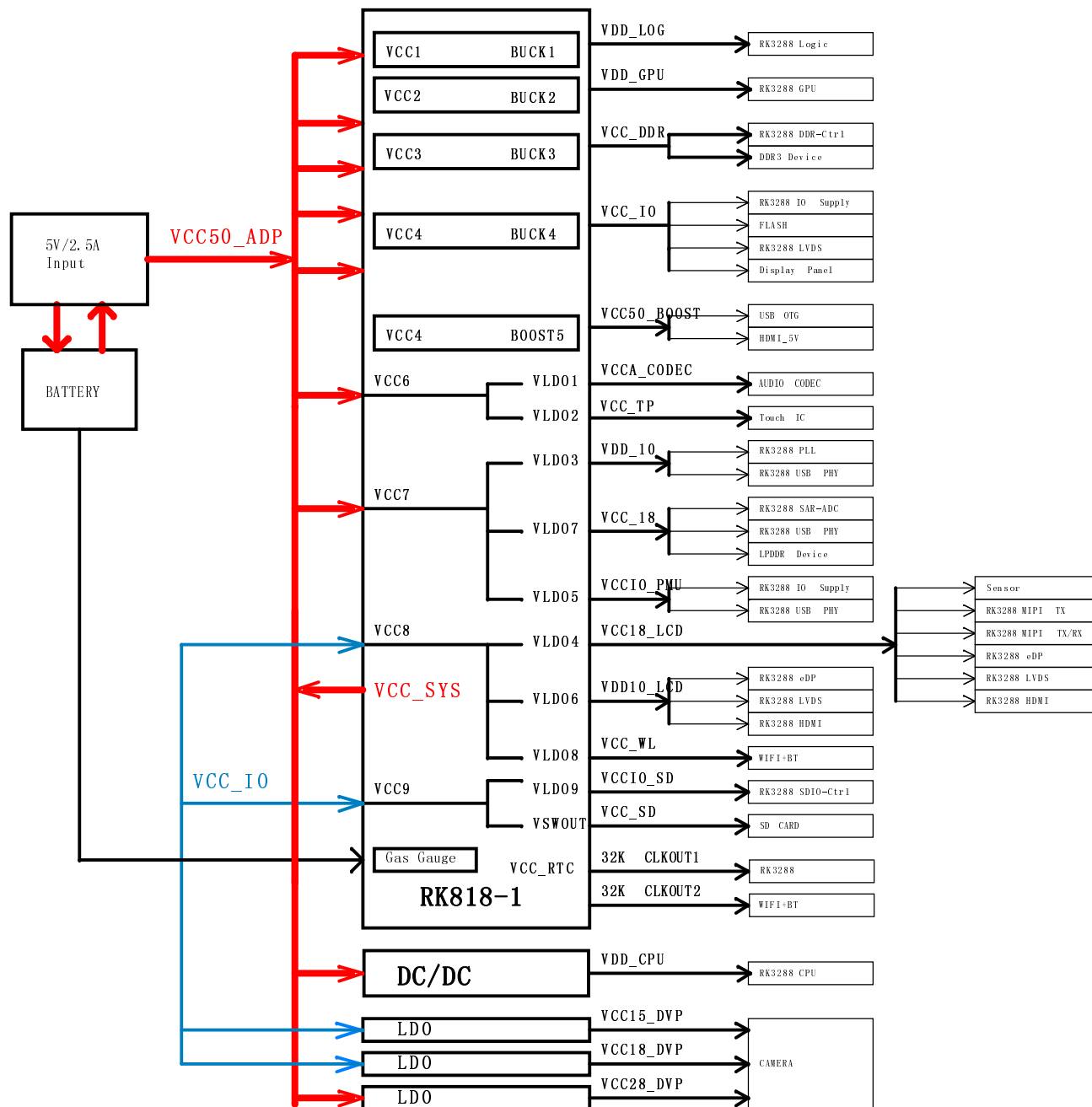
CT363 0x1b
FT5506
GSL3680 0x40

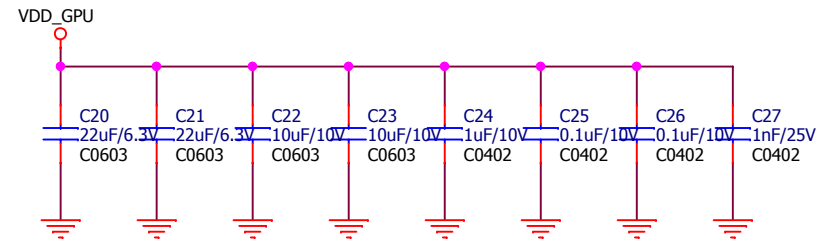
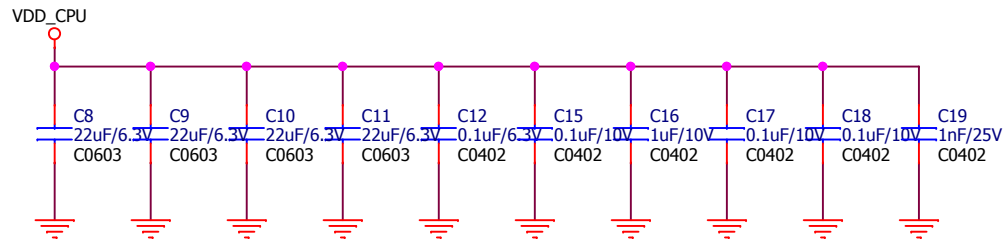
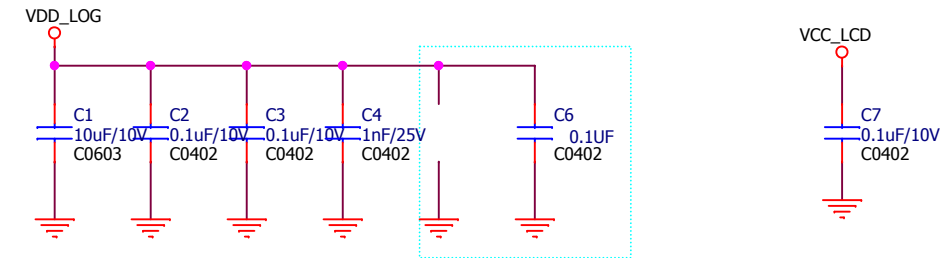
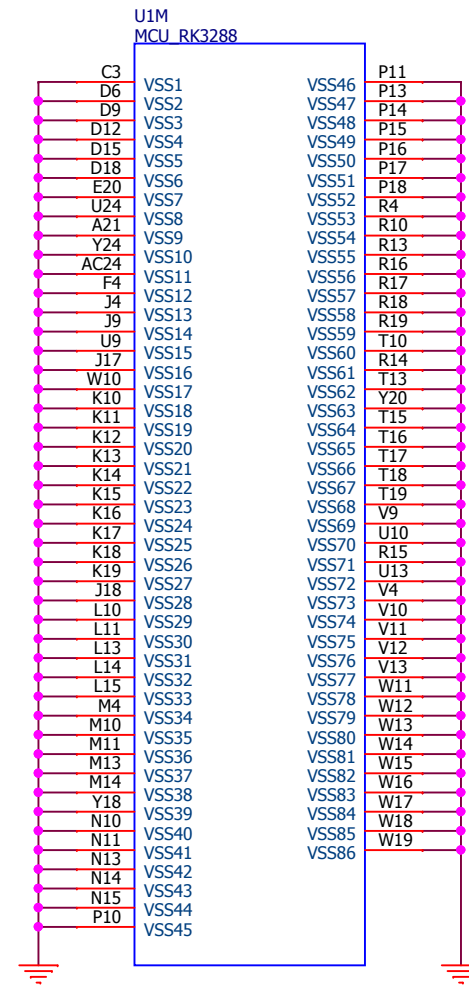
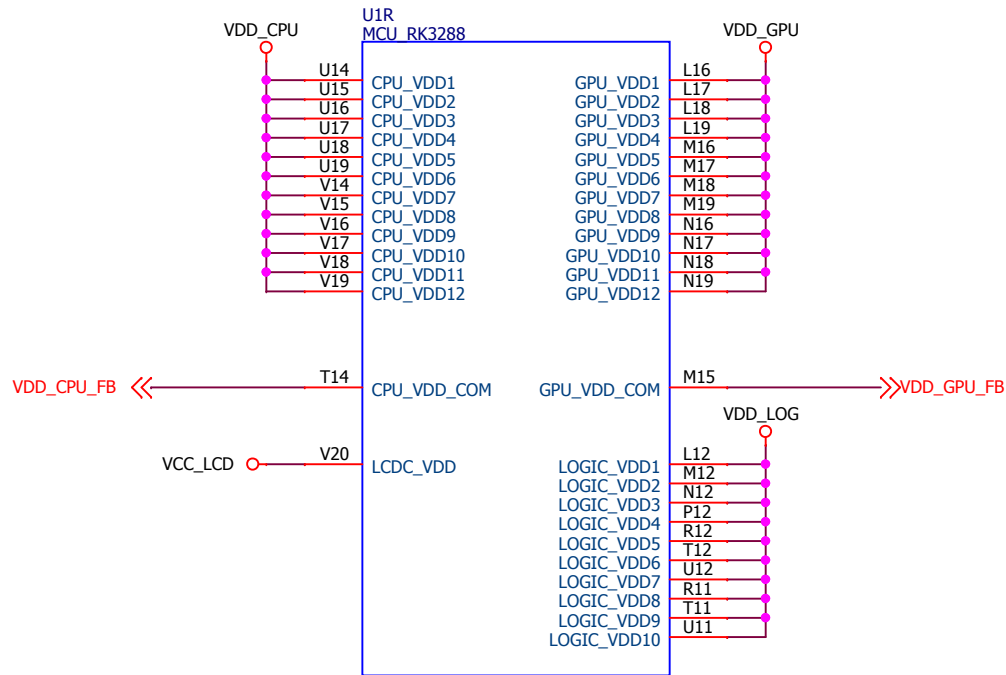
 瑞芯微电子		福州瑞芯微电子	
Title: Index			
File: RK3288_Tablet_REF			REV: V1.5
Create Date: Monday, February 10, 2014		Page Num: 1	
Modify Date: Tuesday, March 19, 2019		Page Total: 53	

Version	Date	Author	Change Note	Approved
V1.0	20140429	LX	First edictor	
V1.1	20140625	LX	Please refer to document of 《 RK3288 发 布 原 理 图修改点 V11 0 1 0 2 5 》	
V1.2	20140806	LX	Please refer to document of 《 RK3288 发 布 原 理 图修改点 V12 0 1 0 0 6 》 Note:GPIO外 部 所 接 是 开 漏 信号 或 者 利 用 G P I O 上下默认确定功能的， 有 可 能 出 现 小 概 率 的 不 确 定 状 态 ， 外 部 需 要 增锁留截 也 以 确状态。	
V1.3	20141210	LX	Please refer to document of 《 RK3288 发 布 原 理 图修改点 V13 0 1 1 1 0 》	
V1.4	20160218	LX	Please refer to document of 《 RK3288_Tablet_REF_V14_20160218 Modify Notes 》	
V1.5	20160810	LX	Please refer to document of 《 RK3288_Tablet_REF_V15_20160810 Modify Notes 》	

Block Diagram for MID




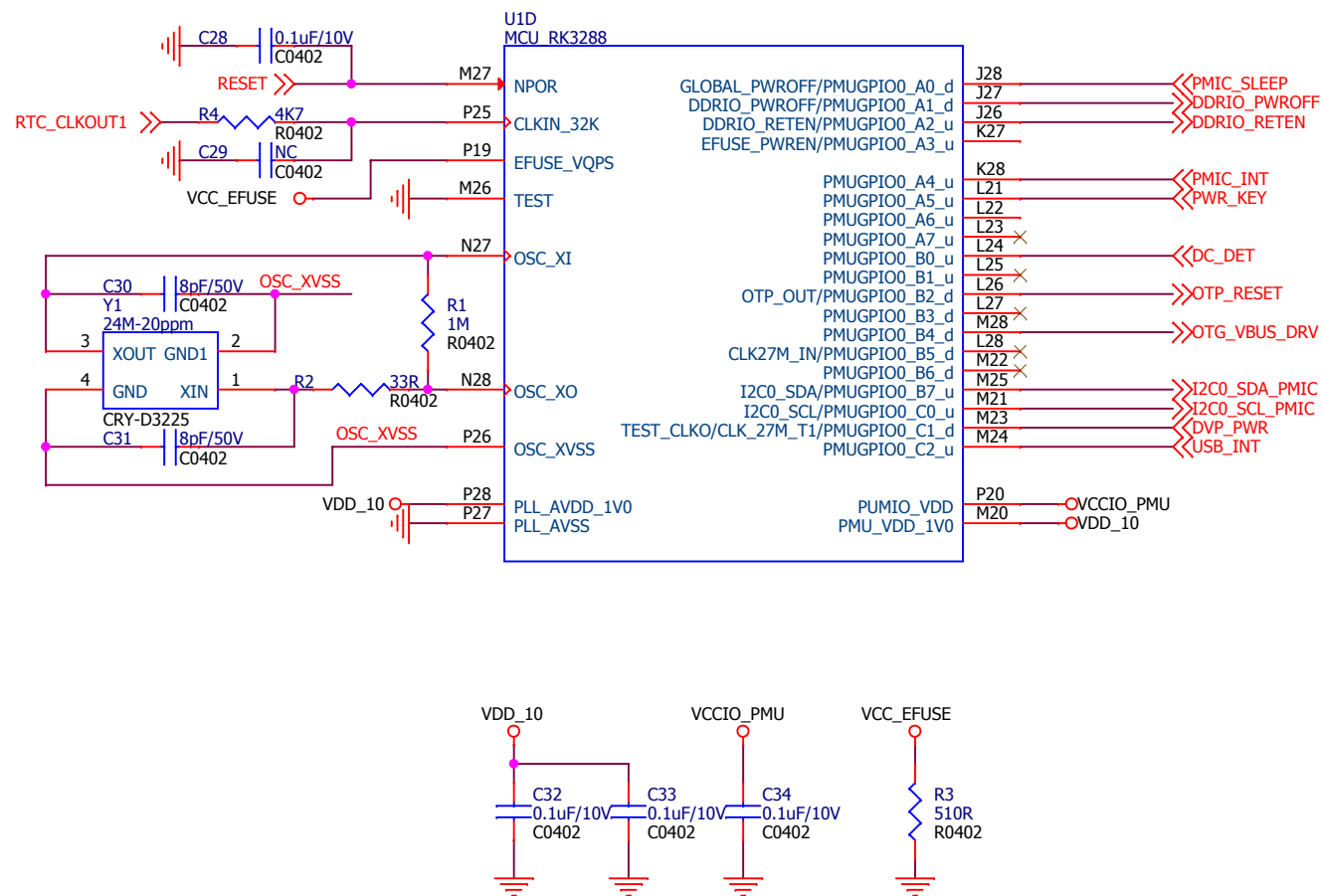




RK3288 Power Filter

Note: All the capacitor should be place close to the power pin of RK3288 .

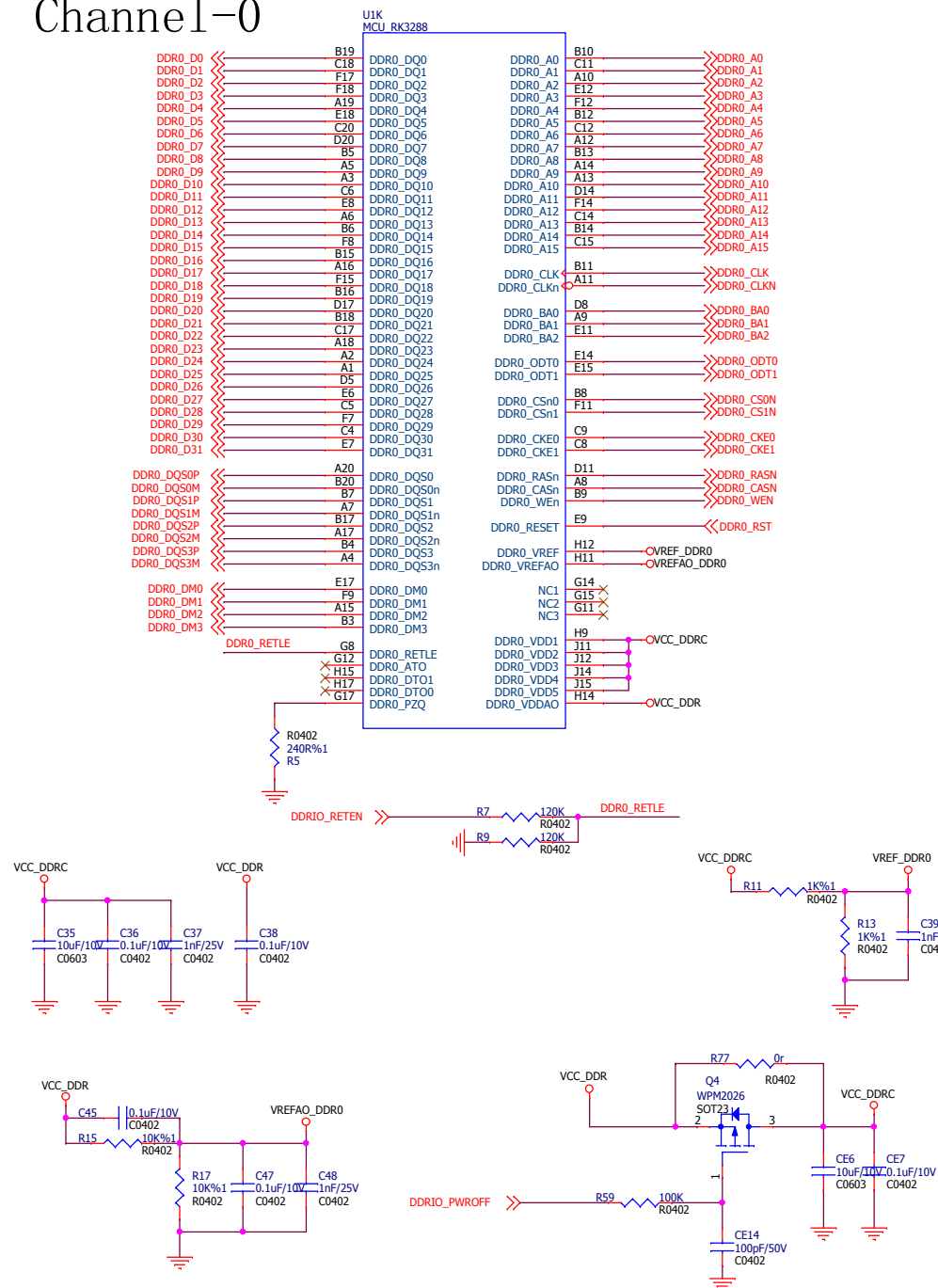
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File: RK3288_Tablet_REF			REV: V1.5
Create Date: Wednesday, February 12, 2014		Page Num: 7	
Modify Date: Tuesday, March 19, 2019		Page Total: 53	



Note: All the capacitor should be place close to the power pin of RK3288.

Note: Place the Component if need to write eFUSE

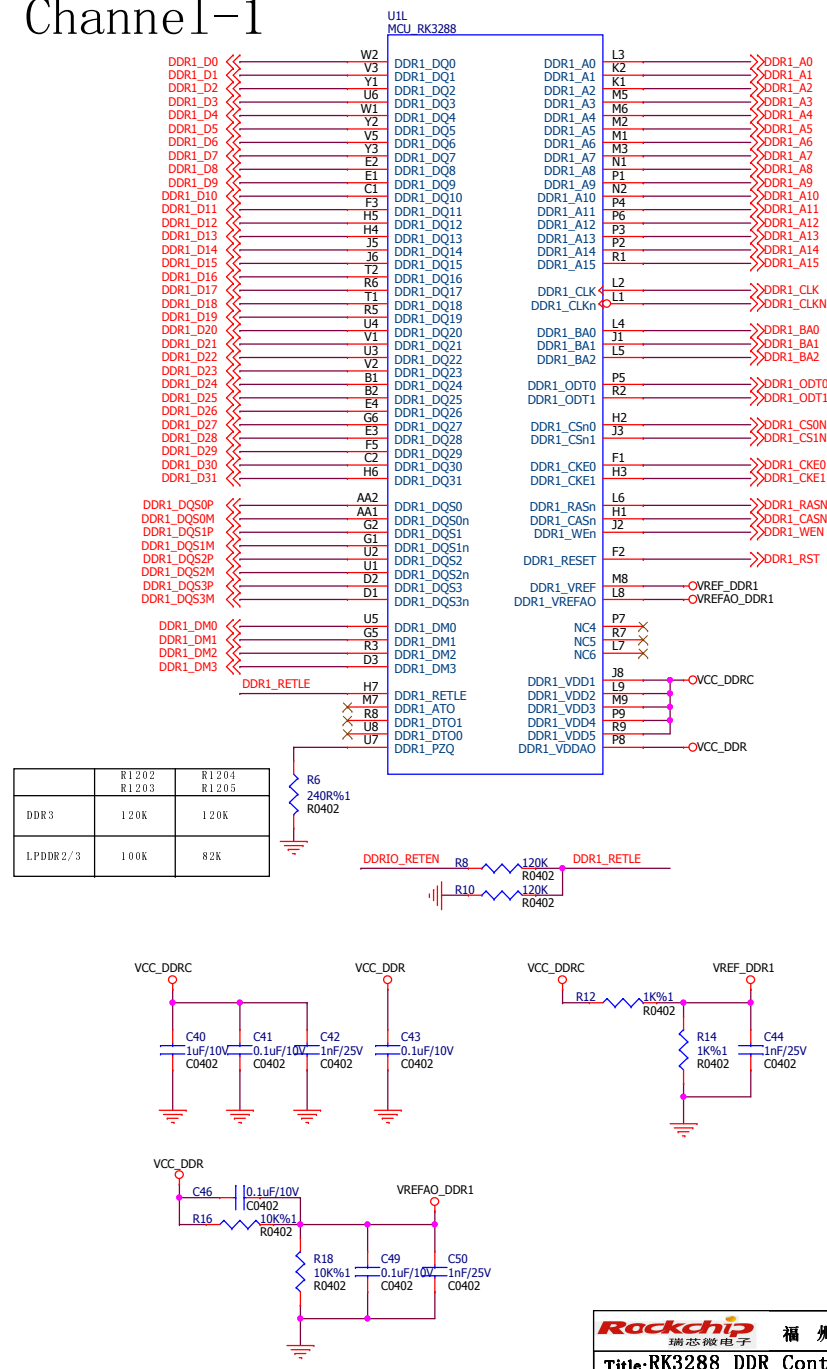
RK3288 DDR Channel-0



RK3288_K

Note: All the capacitor should be place close to the power pin of RK3288.

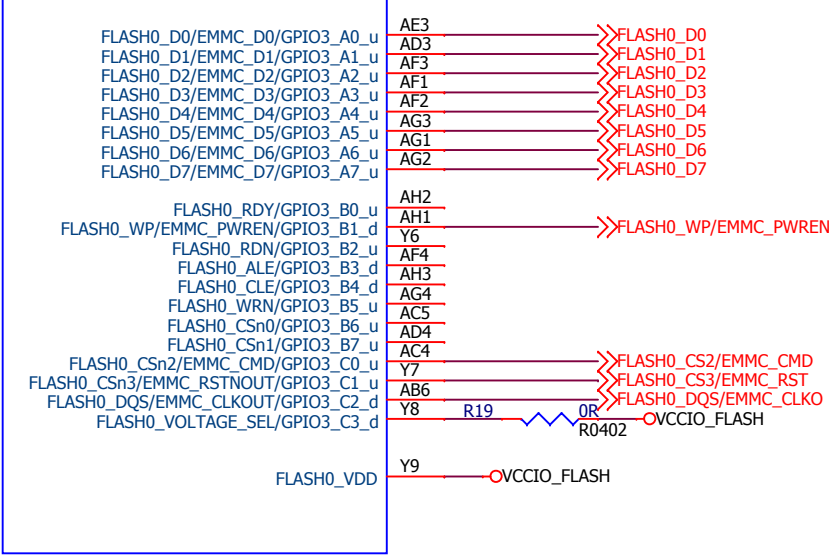
RK3288 DDR Channel-1



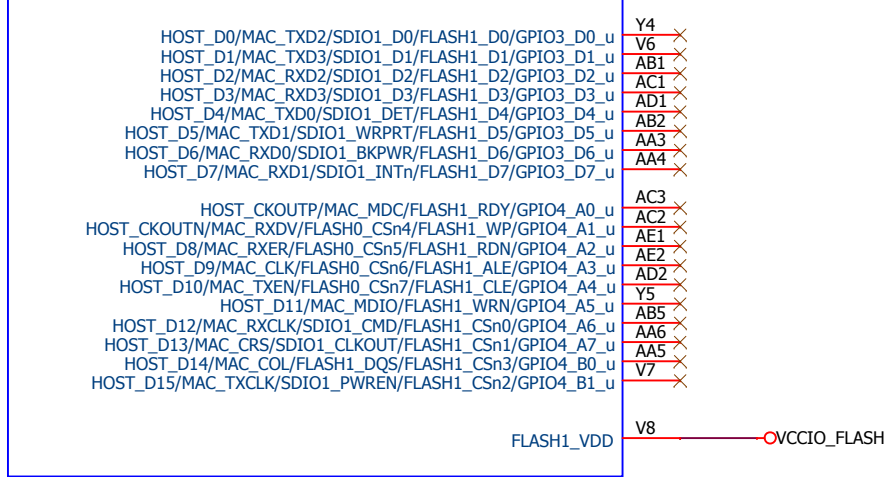
RK3288_L

Note: All the capacitor should be place close to the power pin of RK3288.

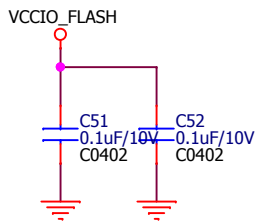
U1P
MCU_RK3288



U1Q
MCU_RK3288

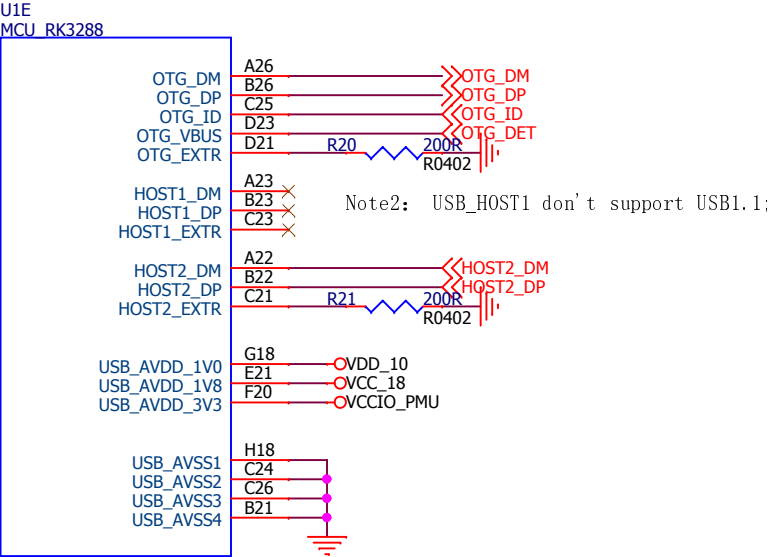


	VCCIO_FLASH=1.8V	VCCIO_FLASH=3.3V
FLASH0_VOLTAGE_SEL pin connect to	VCCIO_FLASH	Floating(Default)

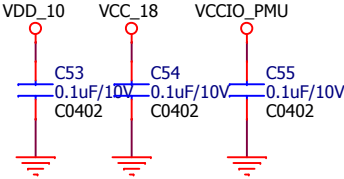


Note: All the capacitor should be place close to the power pin of RK3288.

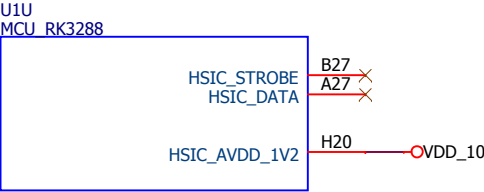
RK3288_E



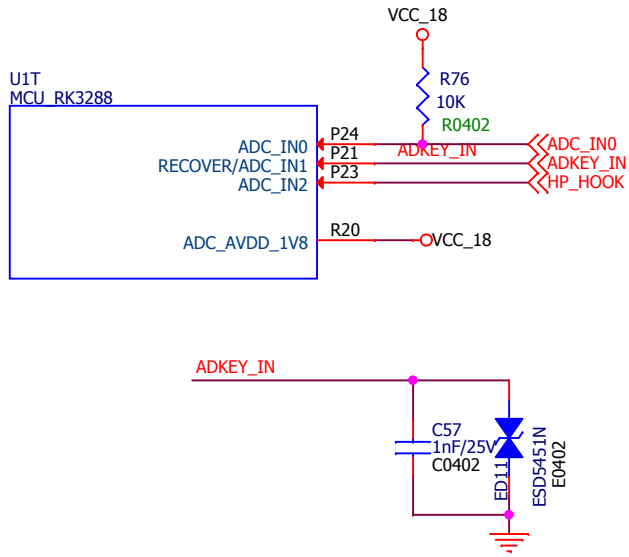
Note1: All the capacitor should be place close to the power pin of RK3288.



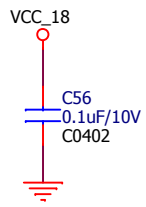
RK3288_U



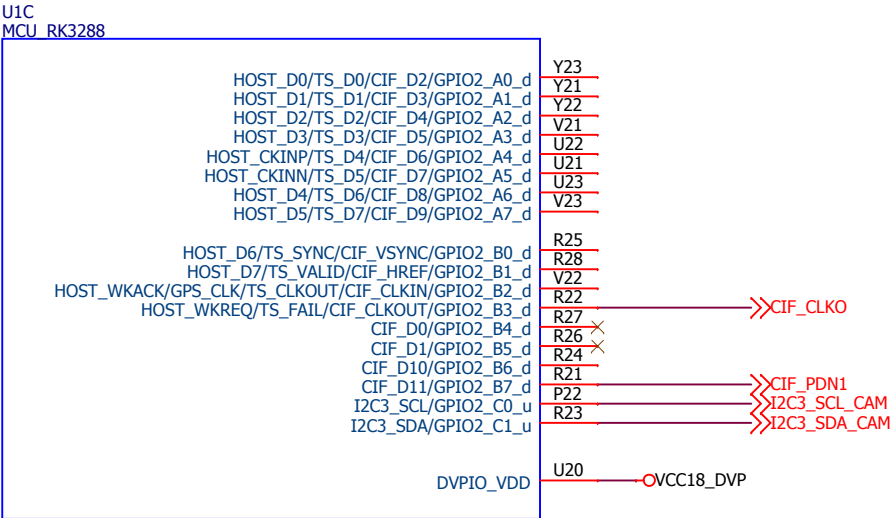
RK3288_T



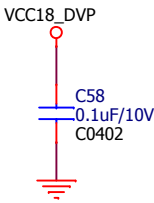
Note: All the capacitor should be place close to the power pin of RK3288.



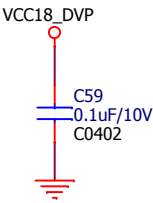
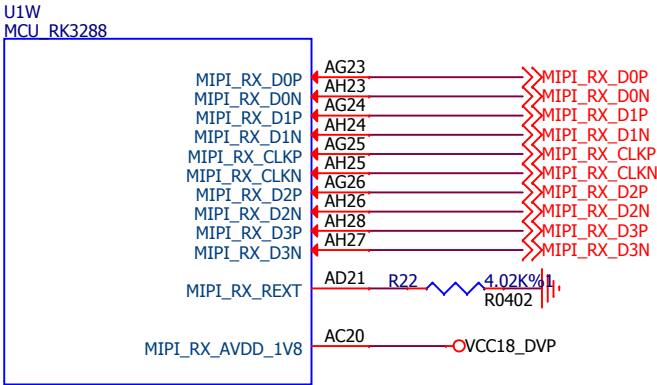
RK3288_C



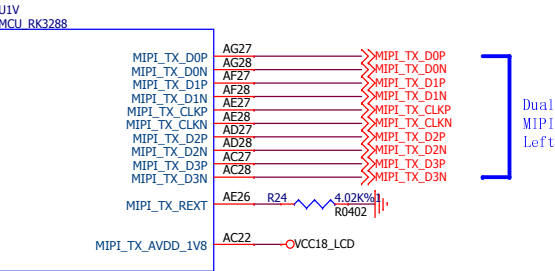
Note: All the capacitor should be place close to the power pin of RK3288.



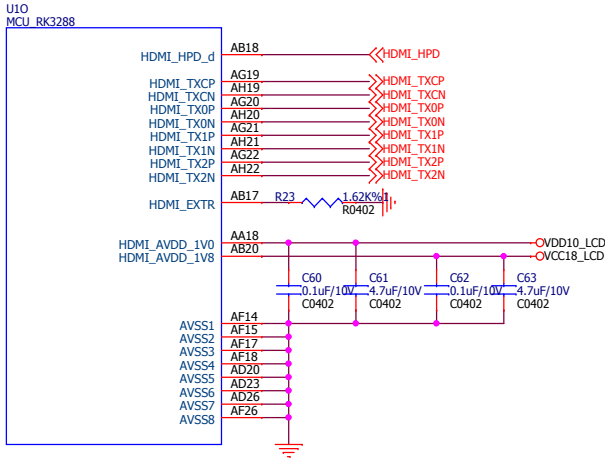
RK3288_W



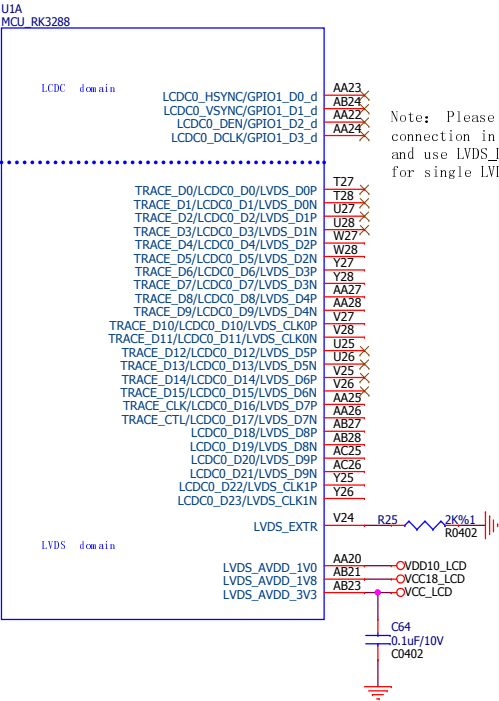
RK3288_V



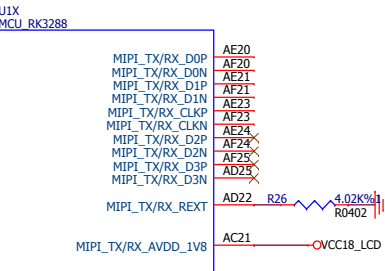
RK3288_0



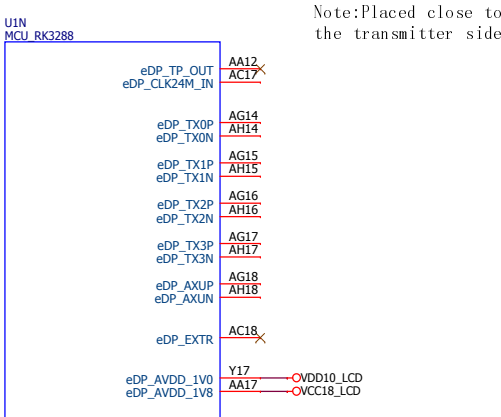
RK3288_A



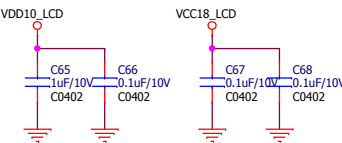
RK3288_X



RK3288_N

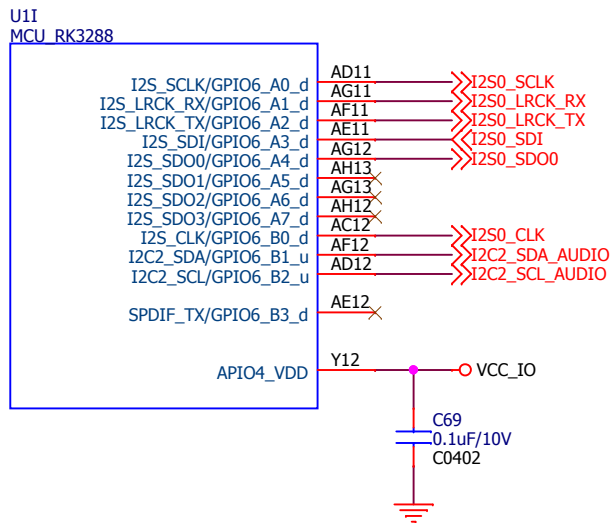
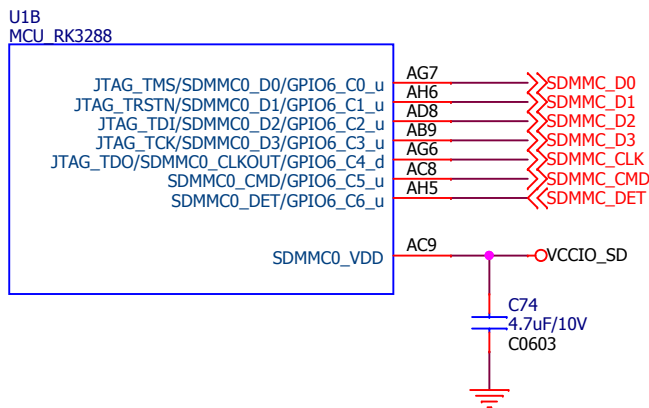
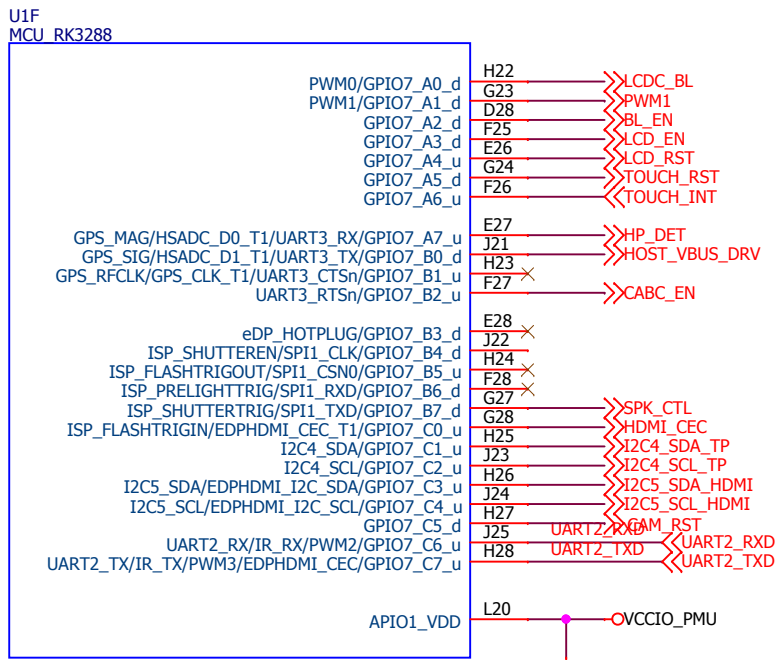


Note: All the capacitor should be place close to the power pin of RK3288.

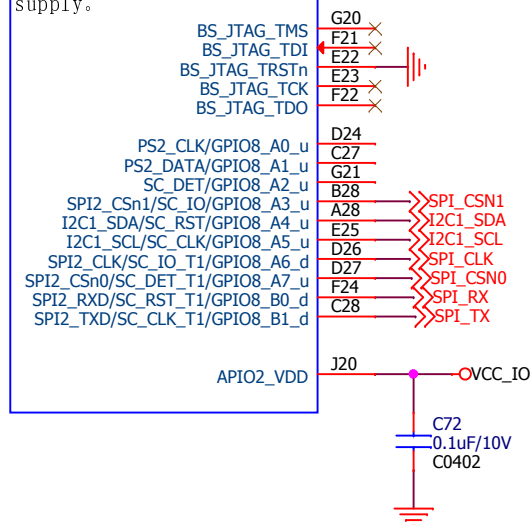


Correspondence between LCDC DATA and RGB

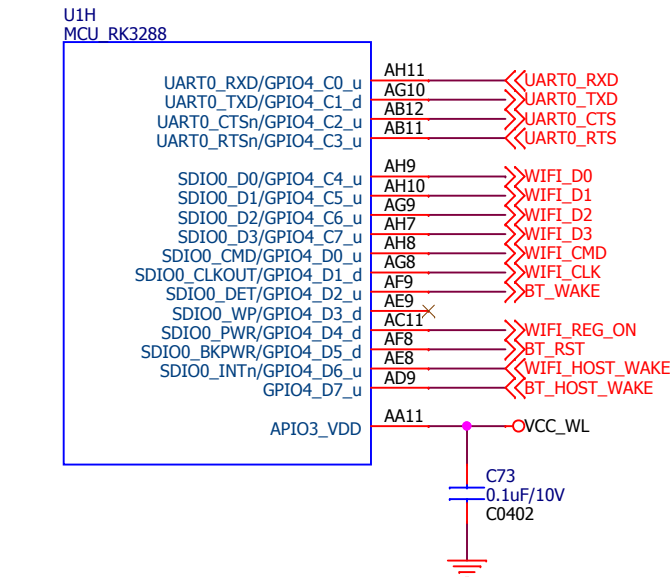
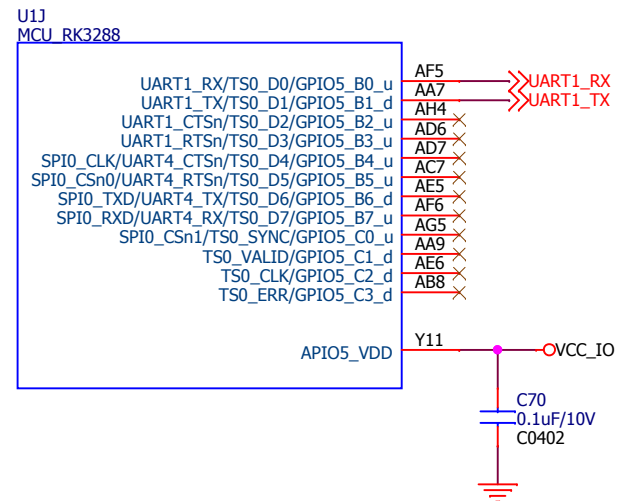
LCDC_D0	B0	LCDC_D8	G0	LCDC_D16	R0
LCDC_D1	B1	LCDC_D9	G1	LCDC_D17	R1
LCDC_D2	B2	LCDC_D10	G2	LCDC_D18	R2
LCDC_D3	B3	LCDC_D11	G3	LCDC_D19	R3
LCDC_D4	B4	LCDC_D12	G4	LCDC_D20	R4
LCDC_D5	B5	LCDC_D13	G5	LCDC_D21	R5
LCDC_D6	B6	LCDC_D14	G6	LCDC_D22	R6
LCDC_D7	B7	LCDC_D15	G7	LCDC_D23	R7



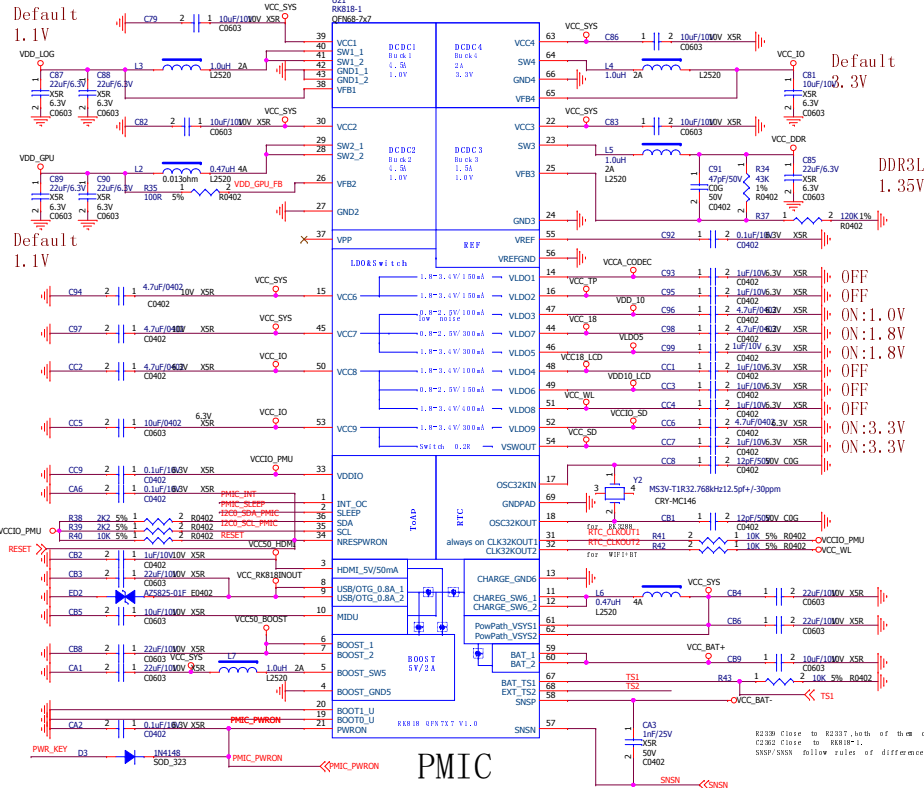
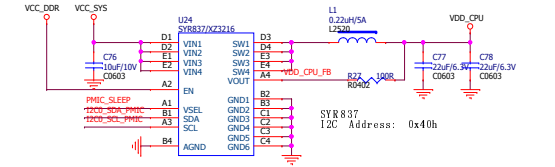
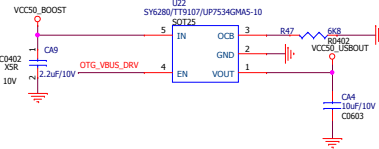
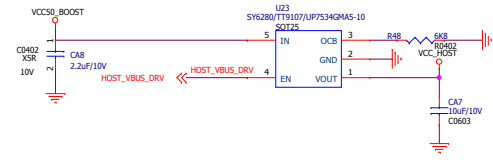
Note: BS_JTAG_TRSTn must connected to VSS
U1G MCU RK3288 VDD must be power supply.



Note: All the capacitor should be place close to the power pin of RK3288.



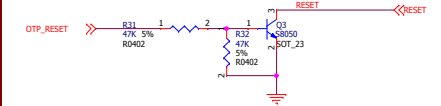
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File: RK3288_Tablet_REF			REV: V1.5
Create Date: Monday, December 30, 2013		Page Num: 15	
Modify Date: Tuesday, March 19, 2019		Page Total: 53	



DDR	Output	R231R	R232R
TYPE	1.0V	100K	100K
R231R	1.0V	100K	100K
R232R	1.0V	100K	100K

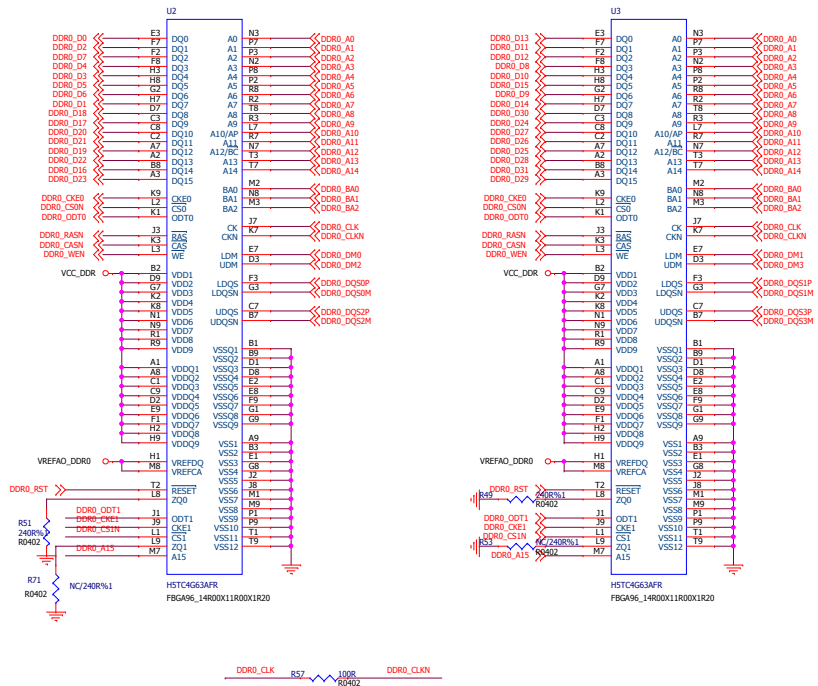
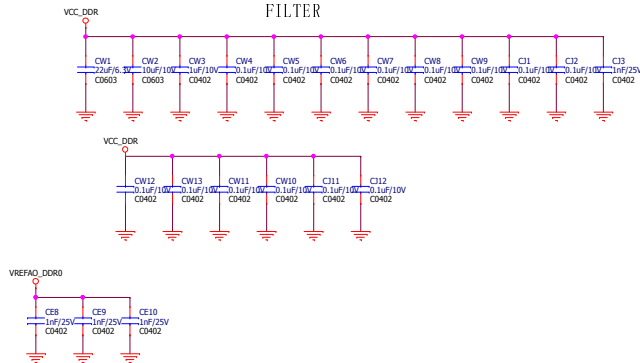
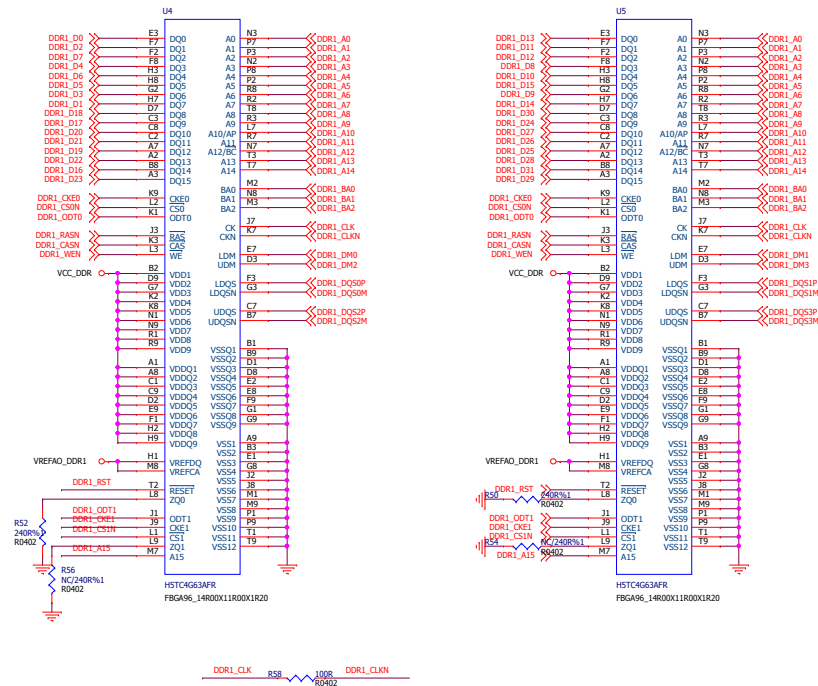
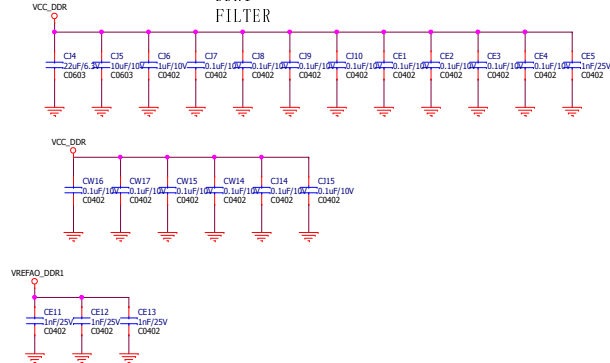
PowerName	PMU	TIMER	Output
VCC_DDR	0.0V	0.0V	0.0V
VCC_GPU	0.0V	0.0V	0.0V
VCC_IO	0.0V	0.0V	0.0V
VCC_PMU	0.0V	0.0V	0.0V
VCC_BOOST	0.0V	0.0V	0.0V
VCC_BAT	0.0V	0.0V	0.0V
VCC_SYS	0.0V	0.0V	0.0V
VCC_DDR	0.0V	0.0V	0.0V
VCC_GPU	0.0V	0.0V	0.0V
VCC_IO	0.0V	0.0V	0.0V
VCC_PMU	0.0V	0.0V	0.0V
VCC_BOOST	0.0V	0.0V	0.0V
VCC_BAT	0.0V	0.0V	0.0V
VCC_SYS	0.0V	0.0V	0.0V
VCC_DDR	0.0V	0.0V	0.0V
VCC_GPU	0.0V	0.0V	0.0V
VCC_IO	0.0V	0.0V	0.0V
VCC_PMU	0.0V	0.0V	0.0V
VCC_BOOST	0.0V	0.0V	0.0V
VCC_BAT	0.0V	0.0V	0.0V
VCC_SYS	0.0V	0.0V	0.0V

Over-temperature
Protection

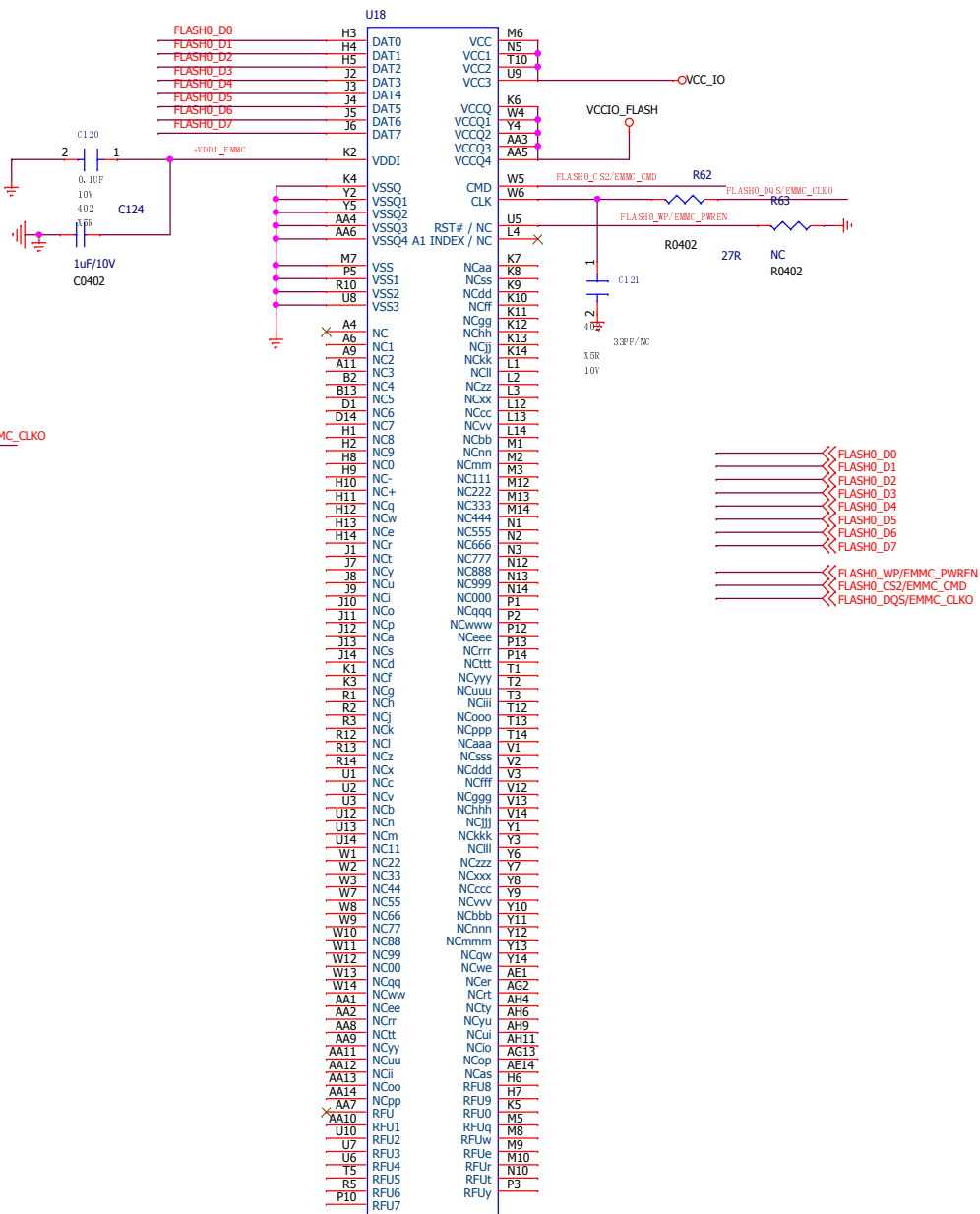
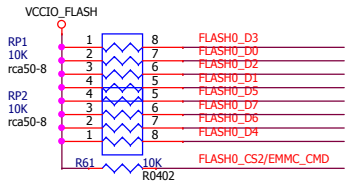


Power for
VCCIO_PMU



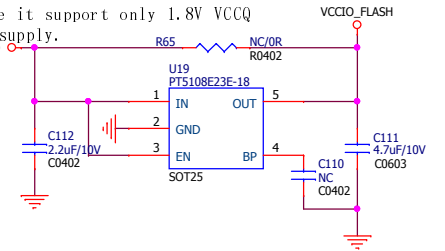
DDR3
Channel-0DDR0
FILTERDDR3
Channel-1DDR1
FILTER

eMMC FLASH

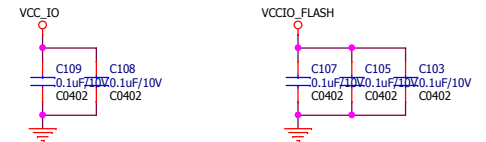


KLMBG4WBC-B031-32G
BGA169_14X18_12MILPAD

Note:
U4001 must be use for Sandisk 5.0 TLC
eMMC,
because it support only 1.8V VCCQ
power supply.



Note:
Reserve PAD
for Update.



note: if use Toshiba and Sandisk Flash at
DDR mode,
VCCQ1 and VCCQ4 must be connected to
VCC_IO.

NAND FLASH

Note:
Reserve PAD for
Update.

 福州瑞芯微电子股份有限公司	
Title: Memory-Nand FLASH (option)	
File: RK3288_Tablet_REF	REV: V1.5
Create Date: Monday, January 13, 2014	Page Num: 28
Modify Date: Tuesday, March 19, 2019	Page Total: 53

