In general TLB is a special high-speed cache for page table entries, and which contains those page table entries that have been most recently used. In the beginning of its implementation I created structure for logic address containing page and it offset. They are generated randomly in function log(). The we make search of this generated page in given file pagetable.txt, and if we find match we return value of this page in function search\_in\_pagetable() with parameter as page. Then in tlb() function we write this page, as new frame to tlb.txt file in order to reach TLB hit in the next cycle, so the frame number is retrieved and the real address is formed. In main() function we firsty search in tlb calling this function first. If randomly taken number of logicadress page is equal to one of frames in tlb, it shows its physical address, if not it calls function search\_in\_pagetable() then function tlb(). It will continuously repeated until user types 0 and exits.