

MDK3.5 下 jlink SWD 功能调试 stm32f103rbt6

1, 硬件平台:

(1), jlink v8 一台;

(2), stm32f103rbt6 核心板一块;

2, 软件,

(1), Jlink 驱动 4.10b;

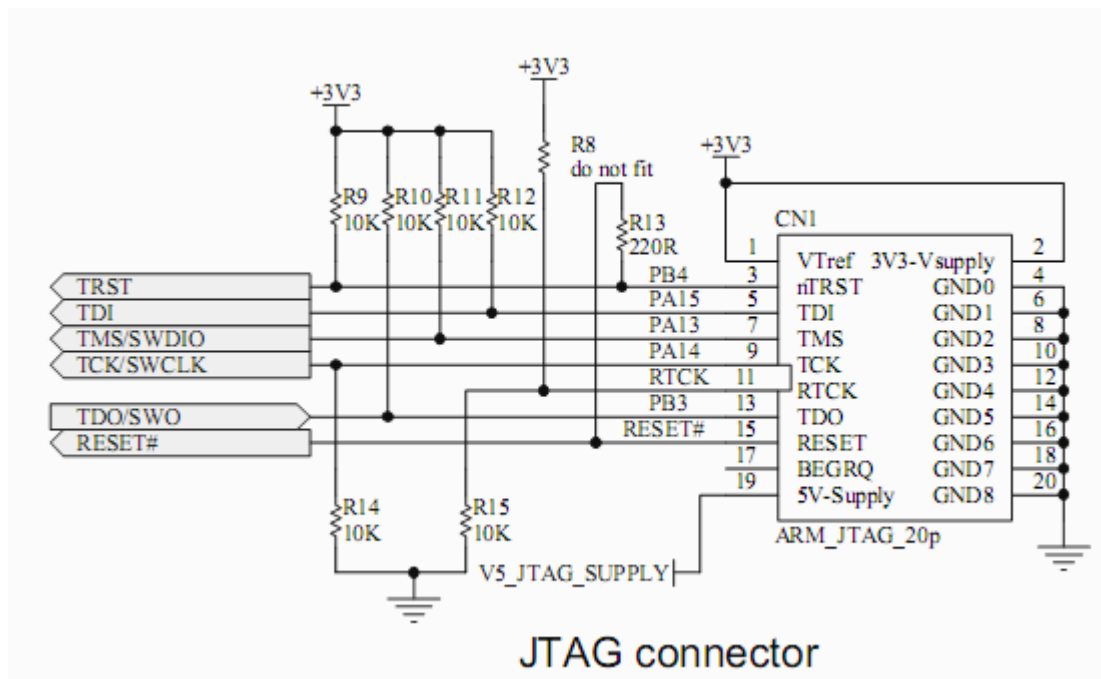
(2), Mdk3.5;

(3) MKD 下测试程序;

3, jlink swd 接口介绍(--UM08001_JLinkARM.pdf 第 154 页):

VTref	1 ●	● 2	NC
Not used	3 ●	● 4	GND
Not used	5 ●	● 6	GND
SWDIO	7 ●	● 8	GND
SWCLK	9 ●	● 10	GND
Not used	11 ●	● 12	GND
SWO	13 ●	● 14	GND
RESET	15 ●	● 16	GND
Not used	17 ●	● 18	GND
5V-Supply	19 ●	● 20	GND

4, stm32f103rb jtag 口:



5, jlink 的 jtag 口 和 stm32 jtag 口 :

Pin1 -- Pin1

Pin7 -- Pin7

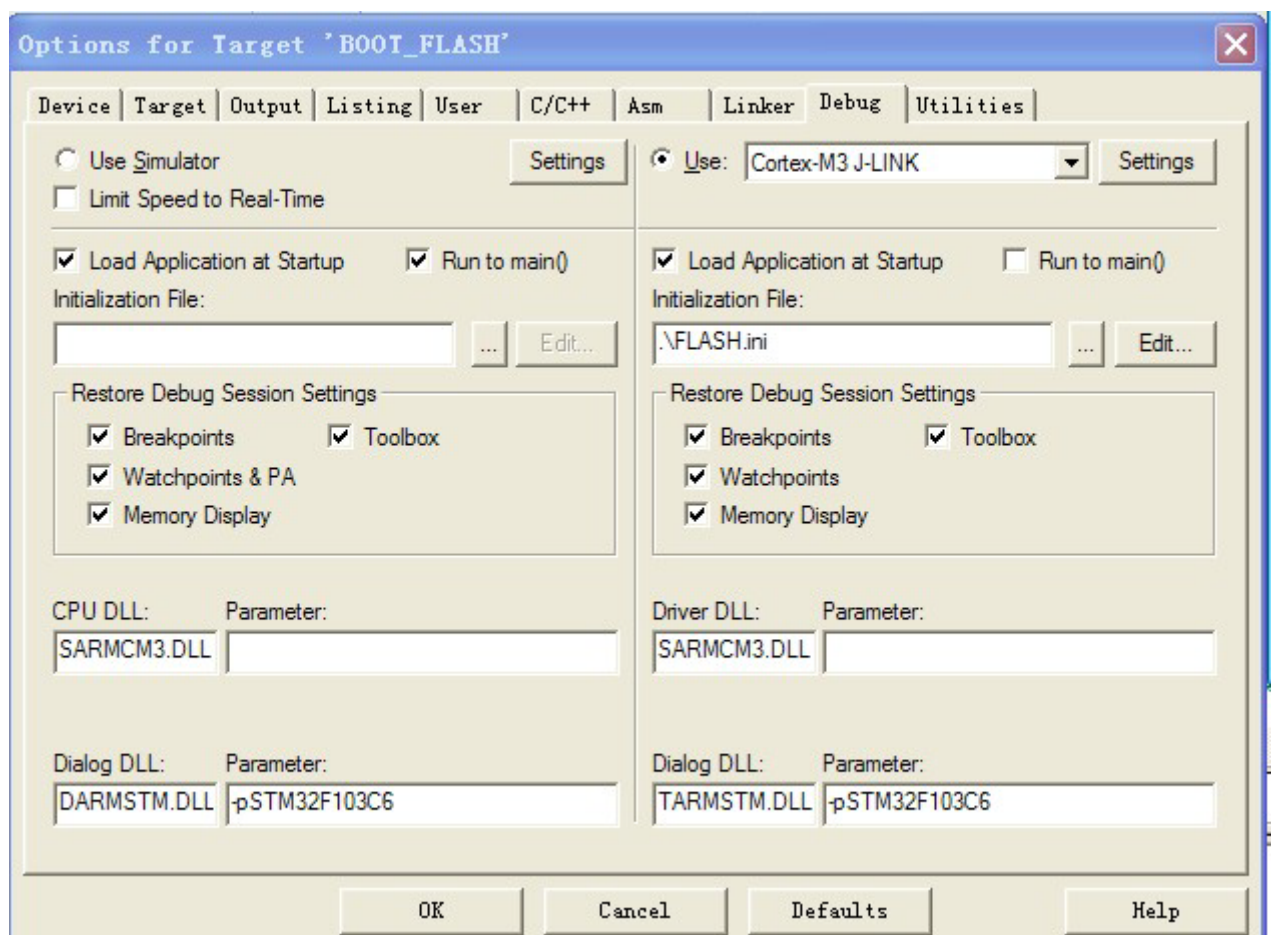
Pin9 -- Pin9

Pin13 -- Pin13

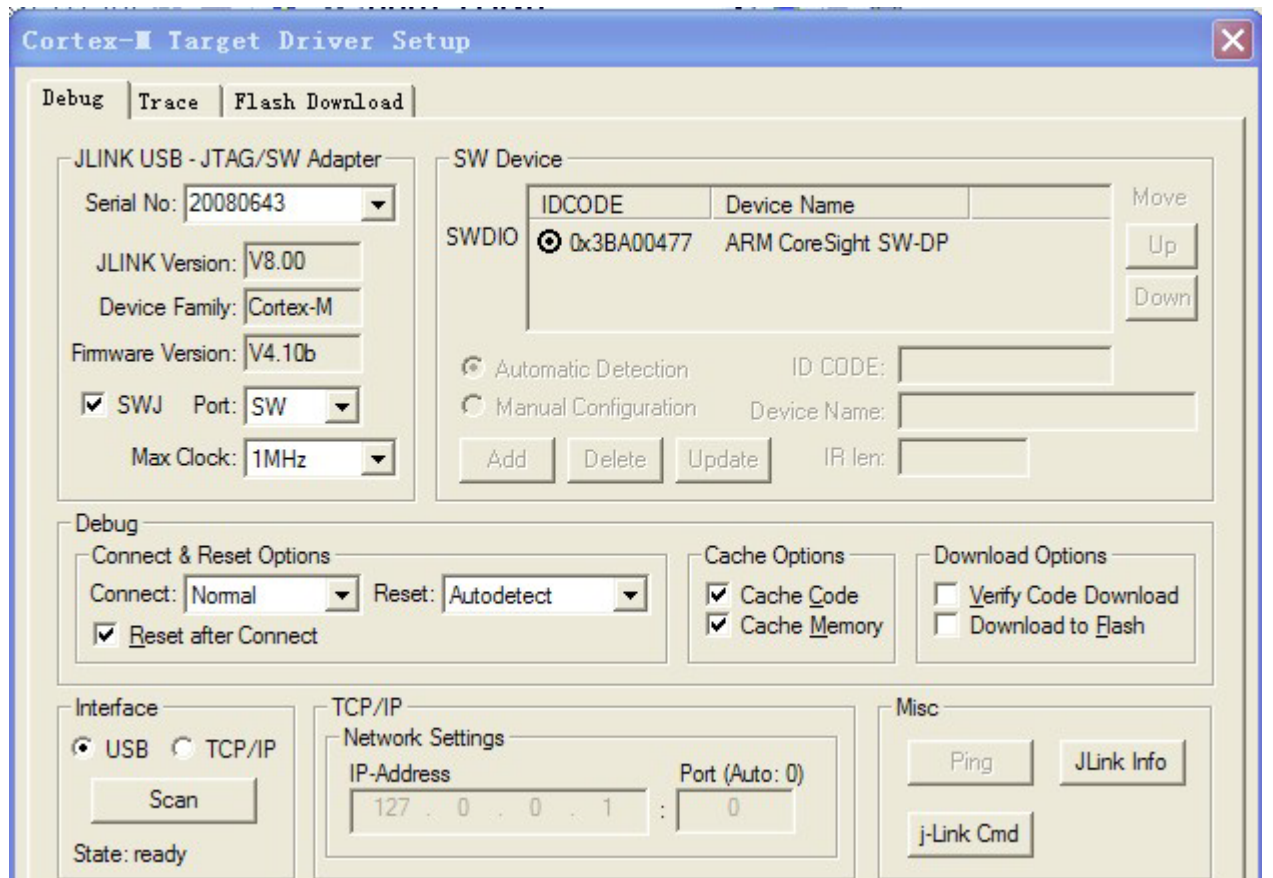
Pin4 -- Pin4

3, 调试:


(1) mdk 下调试设置:



(1)




(2)

Cortex- Target Driver Setup ✕

Debug | Trace | Flash Download

Download Function

 ☒ Erase Full Chip ☒ Program
☐ Erase Sectors ☒ Verify
☐ Do not Erase ☐ Reset and Run

RAM for Algorithm

Start: Size:

Programming Algorithm

Description	Device Type	Device Size	Address Range
STM32F10x Med-density Flash	On-chip Flash	128k	08000000H - 0801FFFFH