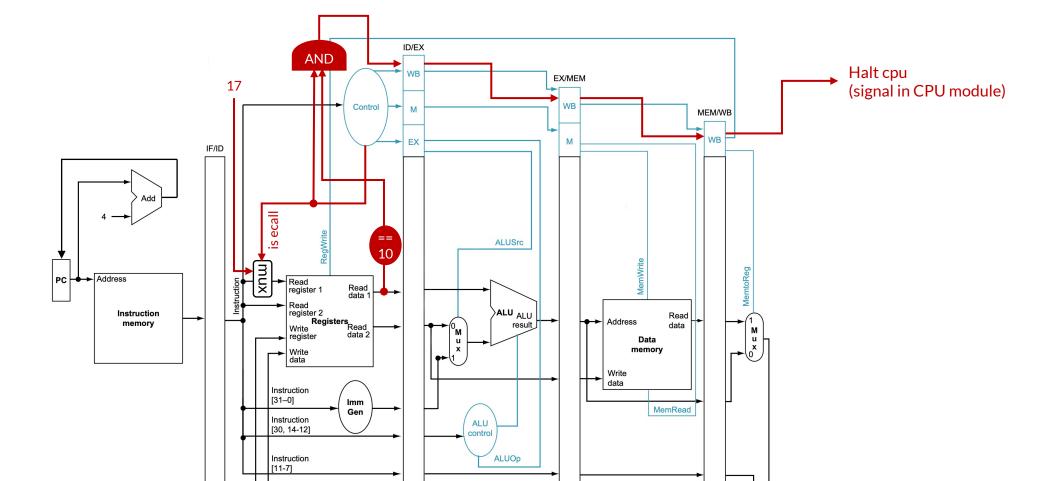
Handling ecall instruction

You need to pipeline the halt signal



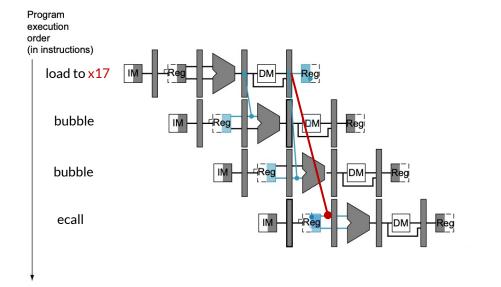
Handling ecall instruction

Hazard and fowarding

load from x17 ecall

Two cycles must be stalled

Time (in clock cycles) ————————————————————————————————————									
	CC 1	CC 2	CC 3	CC 4	CC 5	CC 6	CC 7	CC 8	CC 9
value of register x2:	10	10	10	10	10/-20	-20	-20	-20	-20



add to x17 ecall

One cycle must be stalled

