YR_DB_RUNTIME_VERIF: A FRAMEWORK FOR VERIFYING SQL DESIGN PROPERTIES OF GUI SOFTWARE AT RUNTIME

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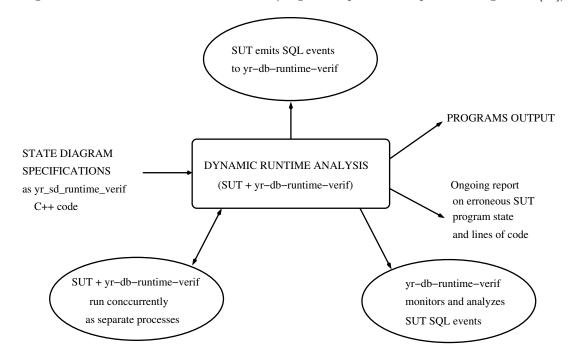
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Abstract. Software design properties are essential to maintain quality by continuous and regressive integration testing. This paper presents an effective and lightweight C++ program verification framework: YR_DB_RUNTIME_VERIF, to check SQL (Structure Query Language) [28] software design properties specified as temporal safety properties [10]. A temporal safety property specifies what behavior shall not occur, in a software, as sequence of program events. YR_DB_RUNTIME_VERIF allows specification of a SQL temporal safety property by means of a very small state diagram [27]; such a state diagram, would only be specified by a start and an accepting state, and by a pre- and post-condition on the state diagram transition between them. In YR_DB_RUNTIME_VERIF, a specification characterizes effects of program events (via SQL statements) on database table columns by means of set interface operations (\in, \notin) , and, enable to check these characteristics hold or not at runtime. Integration testing is achieved for instance by expressing a state diagram that encompasses both Graphical User Interface (GUI) states and MySQL [24] databases queries that glue them. For example, a simple specification would encompass states between 'Department administration' and 'Stock listing' GUI interfaces, and transitions between them by means of MySQL databases operations. YR_DB_RUNTIME_VERIF doesn't generate false positives because its specifications are not desirable (erroneous) specifications. This paper focuses its examples on MySQL database specifications, labeled as states diagrams events, for the newly developed and FOSS (Free and Open Source Software) Enterprise Resource Planing Software YEROTH-ERP-3.0 [25].

Keywords: model—based testing \cdot computer software program analysis \cdot computer software dynamic program analysis \cdot software integration testing with SQL and GUI \cdot integration testing with SQL and Qt–Gui

1 Introduction

Fig. 1: YR_DB_RUNTIME_VERIF WORKFLOW (diagram inspired from operation diagram in [11]).



1.1 Motivations

This paper describes an effective dynamic analysis framework, based on runtime monitors specified in C++ programs (implemented in the software library yr_sd_runtime_verif), to perform software temporal safety property checking of GUI (Graphical User Interface) based software.

GUI based software are very comfortable and handy to use. However, tools to perform temporal safety property verification of GUI software are allmost not available as FOSS. The testing of combinations between GUI windows and database queries that glue them to make sense to the user, is allmost unavailable as FOSS, or at all to the best of the knowledge of the author of this paper.

Unit testing for GUI widgets is available by use of "NUnit" test frameworks like e.g. Qt-Test [1],CppUnit [18], etc.. Software test across GUI widgets (and MySQL queries) is however limited in support by these "NUnit" framework. To the best of the knowledge of the author of this paper, DejaVu [4] provide some support for Java'record and replay' testing while FROGLOGIC [12] provide support for C++ GUI software 'record and replay' technology for testing thick—client GUI. 'Record and replay' testing means a user performs a sequence of events that are recorded by testing infrastructure and automatically replay later on to see if expected events thereof occur. However, none of this 'record and replay' technology tool enable temporal safety property specification as FOSS.

As we will see in the related work, section 7, of this paper, most software design property checking framework don't put an emphasis on checking temporal safety property of GUI software. Characterizing the effects of program statements (via SQL statements) on database table columns, and to check that these characteristics hold or not, is of predominant importance for large software systems with an impressive number of database tables: FOSS YEROTH–ERP–3.0 for instance has about 300 000 lines of physical source code, 34 used SQL tables, and around 290 MariaDB SQL table columns.

It means it can be very difficult for developers to keep application related logical requirements between the tables without appropriate software testing or analysis tools.

A large amount of former work on runtime monitoring assumes for a sequential program, or an abstraction of the program as one single source code, on which program analysis is performed [8,23,3,20,5,9,6].

The program analysis technique the author of this paper presents here abstract SQL events, GUI events, or sequences of them, as a state diagram, and enables developers to run them sequentially against a runtime monitor specified as a C++ program. Figure 1 shows a high level overview of YR_DB_RUNTIME_VERIF operation.

1.2 Main Contributions

This paper presents 3 original main contributions:

- an industrial level quality framework (YR_DB_RUNTIME_VERIF: http://github.com/yerothd/ yr-db-runtime-verif), that solves temporal property verification by dynamic program analysis. YR_DB_RUNTIME_VERIF makes use of the C++ Qt-Dbus library, to input a runtime monitor specification (yr_sd_runtime_verif) as C++ program code, that also enables softwarelibrary-plugin checks;
- 2. a C++ library: yr_sd_runtime_verif (http://github.com/yerothd/yr_sd_runtime_verif); modeling a state diagram runtime monitoring interface using only set algebra inclusion operations (∈, ∉) for state diagram program state specification as pre- and post-conditions. yr_sd_runtime_verif only enables the specification of states diagrams specifications as not desirable (erroneous) behavior specifications. Thus, YR_DB_RUNTIME_VERIF doesn't generate any false positive. A violation of a safety rule has been found whenever a final state could be reached. On the other hand, not reaching a final state doesn't mean that there is not a test case (or test input) that cannot reach this final state.
- 3. An application of YR_DB_RUNTIME_VERIF to check 1 temporal safety property error, found in the ERP FOSS YEROTH-ERP-3.0.

1.3 Overview

This paper is organized as follows: Section 2 presents a motivating example that will be used throughout this paper to explain the presented concepts of this paper. Section 3 presents formal definitions of the principal concepts used in this paper. Section 4 presents the software architecture of YR_DB_RUNTIME_VERIF, our GUI dynamic analysis framework. Section 5 introduces the C++ software library yr_sd_runtime_verif to model states diagrams, and reused by YR_DB_RUNTIME_VERIF. We evaluate our dynamic runtime analysis in Section 6. Section 7 compares this paper with other papers that achieve similar work or endeavors. Section 8 concludes this paper.

2 Motivating Example

Fig. 2: A motivating example, as current bug in YEROTH-ERP-3.0. $\frac{Q0}{\overline{Q1}} := \text{NOT_IN}(\text{YR_ASSET}, \text{department_department_name}).$ $\overline{\overline{Q1}} := \text{DB_IN}(\text{YR_ASSET}, \text{stocks.department_name}).$



Fig. 3: YEROTH-ERP-3.0 administration section displaying departments ($\neg Q0$).

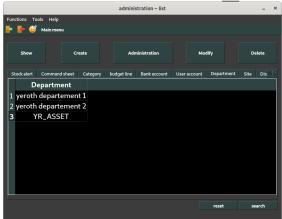


Fig. 4: YEROTH-ERP-3.0 stock asset window listing some assets $(\overline{Q1})$.



${\bf 2.1} \quad {\bf The \ Enterprise \ Resource \ Planing \ Software \ YEROTH-ERP-3.0}$

YEROTH–ERP–3.0 is a fast, yet very simple in terms of usage, installation, and configuration Enterprise Resource Planing Software developed by Noundou et al. [25] for very small, small, medium, and large enterprises. YEROTH–ERP–3.0 is developed using C++ by means of the Qt development library. YEROTH–ERP–3.0 is a large software with around 300 000 (three hundred

Fig. 5: YR_DB_RUNTIME_VERIF command line shell output demonstrating that a final state has been reached.

thousands) of physical source lines of code. YR_DB_RUNTIME_VERIF could be used for integration testing of YEROTH-ERP-3.0, among different software modules.

2.2 Example Temporal Safety Property

The motivating example of this paper consists of the temporal safety property stipulating that "A DEPARTMENT SHALL NOT BE DELETED WHENEVER STOCKS ASSET STILL EXISTS UNDER THIS DEPARTMENT". This statement means that a user shall be denied the removal of department 'YR_ASSET' in Figure 3 because there are still a stock asset listed within department 'YR_ASSET', as illustrated in Figure 4. Figure 2 illustrates the above temporal safety property as a simple state diagram.

State Diagram Explanation 'D' is a *start* state as illustrated by an arrow ending on its state shape. 'E' is a *final* (error, or accepting) state as illustrated by a double circle as state shape.

The pre-condition Q0 (as a predicate) in state 'D':

'NOT_IN(YR_ASSET, department_department_name)' means:

1. a department named 'YR_ASSET' is not in column 'department_name' of database table 'department'.

Similarly, the post-condition $\overline{Q1}$ (as a predicate) 'DB_IN(YR_ASSET, stocks.department name)', in accepting state 'E', means:

1. a department named 'YR_ASSET' is in column 'department_name' of database table 'stocks'.

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The state diagram event transition in Figure 2: 'select.department' denotes that when in 'D', a SQL 'select' on database table 'department' has occurred; 'E' is then reached as an *accepting state*. The source code specified in Listing 1.4 also illustrates a specification in C++ using software library yr_sd_runtime_verif of the state diagram specification above.

2.3 YR_DB_RUNTIME_VERIF Analysis Report

The motivating example automaton in Figure 2 is analyzed by YR_DB_RUNTIME_VERIF as follows:

- 1. Whenever department 'YR_ASSET' is deleted in YEROTH-ERP-3.0, as done in Figure 3, the runtime monitor state 'D' with a state condition Q0 is entered
- 2. when MySQL library (plugin) event 'select.department' occurs, in Figure 3 because of YEROTH-ERP-3.0 displaying the remaining product departments, the guarded condition for edge event 'select.department' is automatically evaluated to 'True' by C++ library yr_sd_runtime_verif, because no other guarded condition was specified by the developer
- 3. $yr_sd_runtime_verif$ enters the runtime monitor state to 'E' and state condition $\overline{Q1}$ via method $YR_trigger_an_edge_event(QString an_edge_event)$ because there are still assets ($yeroth_asset_3$) left within product department 'YR_ASSET', as illustrated in Figure 4. 'E' is then an accepting (or final or error) state.

Figure 5 illustrates an analysis result of the afore described process, which gets evaluated and described in Evaluation Section 6.

2.4 Runtime Analysis Interpretation Of yr_sd_runtime_verif Models By YR_DB_RUNTIME_VERIF

The framework YR_DB_RUNTIME_VERIF assumes the following characteristics of a specification automaton in order to enable proper software integration testing:

- 1. the state diagram automaton only has 2 states: a start and a final state;
- 2. at most 1 state diagram transition pre-condition on the start state
- 3. exactly 1 post-condition on the final state, *that must hold*, when the state diagram automaton reaches this final state
- 4. exactly one state diagram transition between the start and the final state
- 5. no edge guard condition

Future releases of $yr_sd_runtime_verif$, as well as $yr_DB_RUNTIMe_verif$ will handle composition of states diagrams with 2 states in order to have states diagrams with more than only 2 states.

Formal Definitions 3

yr_sd_runtime_verif's formal description of the state diagram formalism follows Mealy machine [27] added with accepting states (final or erroneous state), and state diagram transition pre- and post-conditions. In comparison to statechart [14], which is a visual formalism for states diagrams, yr_sd_runtime_verif doesn't support for instance the following

- 1. hierarchical states (composite state, submachine state, etc.)
- 2. timing conditions
- 3. etc.

Definition 1. A state diagram is a 8-tuple $(S, S_0, C, \Sigma, \Lambda, \delta, T, \Gamma)$ where:

- S: a finite set of states
- $-\mathbf{S_0} \in S$: a start state (or initial state)
- C: a set of predicate conditions; pre-conditions are underlined (e.g.: Q0), and post-conditions are overlined (e.g.: $\overline{Q1}$). A pre-condition is comparable to a Harel-state chart guard condition.
- Σ : an input alphabet, $\Sigma := \{False, True\}.$ 'False' means no input from SUT into YR_DB_RUNTIME_VERIF. 'True' means any input could come from SUT.
- Λ : an output alphabet, ϕ the no output element
- $-\delta: S \times C$: a 2-ary relation that maps a state s to a state-condition c as either a state diagram transition pre-condition (\underline{c}), or as a state diagram transition post-condition (\overline{c}).
- $-\mathbf{T}: S \times \Sigma \to S \times \Lambda$: a transition function that maps an input symbol to an output symbol and the next state.
- Γ : a set of accepting states.

For instance, for the motivating example described in Figure 2 we have:

```
\mathbf{S} = \{D, E\}; \mathbf{S_0} = D; \mathbf{C} = \{Q0, \overline{Q1}\}; \mathbf{\Sigma} = \{False, True\}; \mathbf{\Lambda} = \{\phi, \text{'select.department'}\}; \delta
= \{(D, Q0), (E, \overline{Q1})\}; \mathbf{T} = \{((D, False), (D, \phi)), ((D, True), (E, 'select.department'))\}; \mathbf{\Gamma} = \{E\}.
```

Definition 2. A pre-condition of a state diagram transition is a predicate that must be true before the transition can be triggered. A pre-condition Q0 could have 2 forms:

```
-Q0 := IN(X, Y) that means value "X" is in (\in) database column value set "Y".
```

 $-\overline{Q0} := NOT_IN(X, Y)$ that means value "X" is not in $(\not\in)$ database column value set "Y".

Definition 3. A post-condition of a state diagram transition is a predicate that must be true after the transition was triggered. A post-condition Q1 could have 2 forms:

 $\begin{array}{l} -\overline{Q1}:=\mathrm{DB_IN}(\mathrm{A,\,B}) \text{ that means value "}A" \text{ is in (€) database column value set "}B".\\ -\overline{Q1}:=\mathrm{DB_NOT_IN}(\mathrm{A,\,B}) \text{ that means value "}A" \text{ is not in (ξ) database column value set} \end{array}$ "B".

Definition 4. A trace $T_n = \langle e^0, e^1, ... e^n \rangle$ is a sequence of SUT events $e^i (i \in \{0, ..., n\})$ of

trace(D) is the trace of SUT events up to state D.

For instance, for the motivating example described in Figure 2 we have:

-trace(D) = <>, trace(E) = < select.department >.

The Software Architecture of YR-DB-RUNTIME-VERIF 4

Fig. 6: YR_DB_RUNTIME_VERIF: simplified software system architecture.

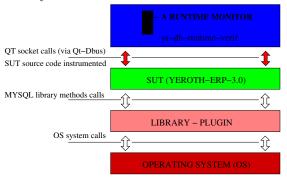
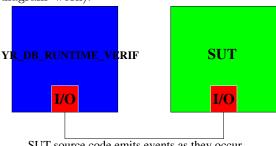


Fig. 7: YR_DB_RUNTIME_VERIF and SUT socket communication (diagram inspired from Jan Peleska diagram-work).



SUT source code emits events as they occur.

Dynamic Analysis 4.1

SUT Source Code Instrumentation. YR_DB_RUNTIME_VERIF runs as a separate Debian Linux process from the application to dynamically analyze (YEROTH-ERP-3.0 in this case). Figure 6 illustrates a software system architecture layer of a software system that uses YR_DB_RUNTIME_VERIF. Figure 6 and Figure 7 illustrate how YEROTH-ERP-3.0 is instrumented to send MySQL database events, as they occur on due to the GUI of YEROTH-ERP-3.0, to process YR_DB_RUNTIME_VERIF, so it can perform runtime analysis of the monitor implemented within it.

Debugging Information. Each GUI manipulation of YEROTH-ERP-3.0 in its instrumented source code part could generate a state transition within the analyzed runtime monitor state diagram in YR_DB_RUNTIME_VERIF. Visualize line 33 of Figure 5 to observe that a specific analysis message is sent to the console of YR_DB_RUNTIME_VERIF in cases where a final state has been reached; the message at line 31 is for an accepting (final) state of the state diagram specification of the motivating example presented in Figure 2.

A Runtime Monitor (An Analysis Client)

Listing 1.1: "XML file adaptor for YEROTH-ERP-3.0 test cases (reduced from 4 to only 1 SQL event for paper)."

```
<\!!\textbf{DOCTYPE} \text{ node } \textbf{PUBLIC "-//freedesktop//DTD D-BUS Object Introspection } 1.0//\text{EN"}
"http://www.freedesktop.org/standards/dbus/1.0/introspect.dtd"> < node name= "/YRruntimeverification">
   <interface name="com.yeroth.rd.IYRruntimeverification">
<method name="YR_slot_refresh_SELECT_DB_MYSQL">
<annotation name="org.qtproject.QtDBus.QtTypeName.In0" value="QString"/>
         < annotation \ name = "org. qtproject. QtDBus. QtTypeName. In 1" \ value = "uint"
        <annotation name="org.qtproject.QtDBus.QtTypeName.In2" value="bool"/>
<arg type="QString" direction="in"/>
        <arg type= Qotting direction= in /
<arg type="uint" direction="in"/>
<arg type="bool" direction="out"/>
       </method>
   </interface>
```

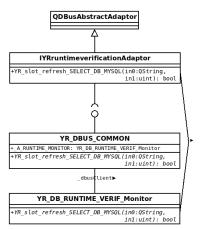


Fig. 8: YR_DB_RUNTIME_VERIF: simplified class diagram in UML [7].

A user (an analysis client) of YR_DB_RUNTIME_VERIF needs to subclass class YR_DB_RUNTIME_VERIF_Monitor. The UML class diagram in Figure 8 displays the class structure of YR_DB_RUNTIME_VERIF. Classes in the class diagram in Figure 8 only display a subset of their methods and interfaces so the diagram could fit in this paper.

Qt-Dbus communication adaptor IYRruntimeverificationAdaptor shall be generated by the user of this library (on YR_DB_RUNTIME_VERIF side) using Qt-Dbus command qdbusxml2cpp and an XML file, similar to the one displayed in Listing 1.1:

Listing 1.2: Command to generate Qt-Dbus adaptor on YR_DB_RUNTIME_VERIF side

 ${\tt qdbusxml2cpp-a\ YRruntimeverification_adaptor\ yr.db-runtime.verif.xml}$

Then, Qt-Dbus communication adaptor IYRruntimeverificationAdaptor must be generated on System Under Test (SUT) side (YEROTH-ERP-3.0 in this case):

Listing 1.3: Command to generate Qt-Dbus adaptor interface on SUT side (YEROTH-ERP-3.0 in this case)

 $\label{eq:continuous} $$ $\operatorname{P}_{\operatorname{C}} \operatorname{IYRruntimeverificationAdaptor_Interface} \setminus -p \operatorname{src/IYRruntimeverificationAdaptor_interface.h:src/IYRruntimeverificationAdaptor_interface.cpp} \setminus \operatorname{yr.db-runtime.verif.xml}$

5 yr_sd_runtime_verif: A C++ Library to Model States Diagrams

Fig. 9: Class diagram in UML [7] to model a State Transition Diagram.

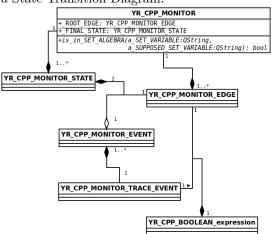
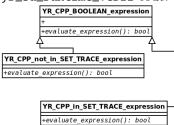


Fig. 10: Class diagram in UML [7] to model state diagram transition trace conditions in yr_sd_runtime_verif code.



Listing 1.4: yr_sd_runtime_verif C++ code modeling a current bug in YEROTH-ERP-3.0 (Figure 2).

5.1 Structure Of yr_sd_runtime_verif

yr_sd_runtime_verif is a state diagram C++ library the author of this paper created to work with the dynamic analysis program YR_DB_RUNTIME_VERIF. Figure 9 and Figure 10 represent the class structure, in UML, of yr_sd_runtime_verif. Listing 1.4 shows the C++ code that models the motivating example in Figure 2, and that uses runtime monitoring C++ state diagram library yr_sd_runtime_verif.

State Diagram Feature	Class
State	YR_CPP_MONITOR_STATE
Transition	YR_CPP_MONITOR_EDGE
Event	YR_CPP_MONITOR_EVENT
Trace at state level	YR_CPP_MONITOR_TRACE_EVENT
Guard Condition	YR_CPP_BOOLEAN_expression
Set Trace Inclusion at edges	YR_CPP_in_SET_TRACE_expression
Set Trace non Inclusion at edges	YR_CPP_not_in_SET_TRACE_expression
Runtime Monitor	YR_CPP_MONITOR

Table 1: Runtime Monitor Specification Classes

There is no need to write C++ code for the red specified edge of Figure 2; this represents runtime cases where no input event arrives from SUT into YR_DB_RUNTIME_VERIF.

Table 1 specifies which class is in **yr_sd_runtime_verif** code for each runtime monitor/state diagram element.

5.2 Methods for Pre- and Post-Condition Specifications

Table 2: yr_sd_runtime_verif Methods for Pre-/Post-Condition Specification

$Runtime\ Monitor\ State\ (class\ YR_CPP_MONITOR_EDGE)\ Methods$	Utility	
set_PRE_CONDITION_notIN(QString DB_VARIABLE, QString db_TABLE_db_COLUMN)	sets a NOT IN DATABASE pre-condition	
set_PRE_CONDITION_IN(QString DB_VARIABLE, QString db_TABLE_db_COLUMN)	sets an IN DATABASE pre-condition	
set_POST_CONDITION_notIN(QString DB_VARIABLE, QString db_TABLE_db_COLUMN)	sets a NOT IN DATABASE post–condition	
set_POST_CONDITION_IN(QString DB_VARIABLE, QString db_TABLE_db_COLUMN)	sets an IN DATABASE pre-condition	

Table 2 illustrates methods for specifying pre– and post–conditions of a runtime monitor state diagram transition. Each method takes in 2 arguments:

- QString DB_VARIABLE
- QString db TABLE db COLUMN

The first method argument "DB_VARIABLE" specifies which variable is to be expected as value for the specification of the second variable argument "db_TABLE__db_COLUMN". The second variable gives in a string to be specified in format "DB_table_name.DB_table_column"; and its supposed value is the returned value of the first variable argument "DB_VARIABLE".

These 4 pre- and post-conditions methods make assumptions that a **program variable value** "DB_VARIABLE" is in set "DB_table_name.DB_table_column" or not; if the value of "DB_VARIABLE" is in the database table column, it means it is **in the set** (\in) of values "DB_table_name.DB_table_column"; and not being in the table column means it is **not in the set** (\notin).

Example from the motivating example in Section 2 Listing 1.4 of the runtime monitoring specification stipulates for instance in its line 10, as post-condition:

that 'YR_ASSET' shall be a value in the value set (\in) of SQL table 'stocks' column 'nom departement produit'.

5.3 Processing of SUT Event Stream By An Analysis Client

Listing 1.5: 'DO_VERIFY_AND_or_CHECK_ltl_PROPERTY': $YR_DB_RUNTIME_VERIF$'s overridden method for processing SUT event stream C++ pseudocode.

```
bool DO_VERIFY_AND_or_CHECK_ltl_PROPERTY(
    QString_sql_table_NAME,
    SQL_CONSTANT_IDENTIFIER_cur_SQL_command)
 1
 2
 3
 5
           switch (cur_SQL_command)
 6
               case SELECT:
 7
 8
 9
                    if ("department" == sql table NAME))
10
                         return YR_trigger_an_edge_event("select.department");
12
13
\frac{14}{15}
                    break;
16
               default:
17
19
20
           return false:
21
```

Listing 1.5 illustrates the pseudo-code of YR_DB_RUNTIME_VERIF SUT event processing method 'DO_VERIFY_AND_or_CHECK_ltl_PROPERTY'. An analysis client must first override method 'DO_VERIFY_AND_or_CHECK_ltl_PROPERTY' of class 'YR_DB_RUNTIME_VERIF_Monitor' so to implement a checking algorithm for each event received from SUT, as for instance the events illustrated in Figure 2 of the motivating example.

The analysis client then calls method 'YR_trigger_an_edge_event(QString of an_edge_event)' class 'YR_CPP_RUNTIME_MONITOR' of C++library yr_sd_runtime_verif for each corresponding state diagram transition event. Method 'YR_trigger_an_edge_event(QString an_edge_event)' first evaluates a state diagram transition pre-condition (see Listing 1.4) before it can trigger the corresponding state diagram transition event.

6 Evaluation

The main experimental results in this paper demonstrate the efficacy of our tool to find errors in the SUT (YEROTH-ERP-3.0), presented in Subsection 2.2.

6.1 Qualitative Results

	Table 3: SUT	(YEROTH-ERP-3.0)	Trace Output	(Figure 5).
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CONSOLE OUTPUT LINE	SQL EVENT	SUT PROGRAM POINT (TRACE)
20	"DELETE.department.YR_ASSET"	"src/admin/lister/yeroth-erp-admin-lister-window.cpp:1603"
21	"DELETE.merchandise.YR_ASSET"	"src/admin/lister/yeroth-erp-admin-lister-window.cpp:1626"
22	"SELECT.department"	"src/yeroth-erp-windows.cpp:967"

SUT (YEROTH-ERP-3.0) TRACING. Table 3 illustrates SUT source code trace information as presented in YR_DB_RUNTIME_VERIF console output in Figure 5. We have translated from French to English the MariaDB SQL table names.

SQL EVENT CALL SEQUENCE. Listing 1.4 illustrates the C++ code that we created to model and generate **YR_DB_RUNTIME_VERIF** binary executable that generates output in Figure 5 after deletion of department 'YR_ASSET' in Figure 3 of our motivating example. A careful observation of the output in Figure 5 illustrates the following sequence:

1. **line** 22: at state D, execution of the state diagram event "select.department" (SUT button 'Delete' has been pressed at **line** 20):

```
select * from departements_produits WHERE
nom_departement_produit = 'YR_ASSET';
```

2. **line** 27, **line** 28: evaluation of the pre–condition <u>Q0</u> of state *D* stating that product department 'YR_ASSET' is not existent evaluates to 'TRUE' (triggering of event "delete.department.YR_ASSET" by pressing of SUT button 'Delete' at line 20 has removed any asset department name 'YR ASSET').

```
precondition_IS_TRUE: True
```

3. line 29, line 30: checking post–condition $\overline{Q1}$ in state E (there are still stocks in stock department 'YR_ASSET') evaluates to 'TRUE', thus state E is reached as an accepting state, because department name 'YR_ASSET' still exists in SUT SQL table "stocks", as illustrated in Figure 4 of the motivating example:

```
"execQuery: select * from stocks WHERE nom_departement_produit = 'YR_ASSET';"
*[YR_CPP_MONITOR::CHECK_db_post_condition_IN:] postcondition_IS_TRUE: True **
```

6.2 Runtime Performance

YR_DB_RUNTIME_VERIF and yr_sd_runtime_verif don't incur a runtime supplemental overhead to the SUT, apart from emitting SQL events from SUT to YR_DB_RUNTIME_VERIF as they occur, because no hand—shaking mechanism is used between YR_DB_RUNTIME_VERIF and the SUT. The emission of an SQL event from SUT to YR_DB_RUNTIME_VERIF doesn't cost more than 2 statements execution time (getting a pointer to the DBUS server, and calling a method 'YR_slot_refresh_SELECT_DB_MYSQL' or other similar 3 methods (for INSERT, UPDATE, and, DELETE) on it).

7 Related Work

1. SUT source code instrumentation with specification. "Clara" [6] enables developers to express software design properties using AspectJ and dependency state machines, both as instances of the typestate formalism, a formalism that is merely used for checking correctness of programs by a static compilation (analysis) technique called typestate checking. The Clara framework weaves (instruments), and annotates a program with runtime monitors using AspectJ, then tries to optimize the weaved program by static analysis. The "residual program", meaning the weaved statically optimized program is then executed and runtime monitored by developers to detect runtime errors. Runtime monitoring tools [8,23,3,20,5,9] work as similar as the Clara framework does.

YR_DB_RUNTIME_VERIF doesn't instrument the System Under Test (SUT) with any specification. It runs the runtime monitor concurrently from the analyzed SUT, but not with hand—shaking mechanism, thus not augmenting runtime execution of the SUT as Clara does. YR_DB_RUNTIME_VERIF specifies the runtime monitor as a state diagram, a subset of typestates, specified as a C++ program, and augmented with accepting states and state transition preand post-condition.

2. Specification as set interface operations. "Hob" [21,22] is a program verification framework that enables developers to: characterize effects of program statement on data structures by means of all $(\forall, \exists, \text{ etc.})$ algebra abstract set interface operations; and to check that these characteristics hold or not, using static analyses.

YR_DB_RUNTIME_VERIF is a program verification framework that enables developers to: characterize effects of program statements (via SQL [24] (Structure Query Language) on database table columns by means of set interface operations (\in, \notin) ; and to check that these characteristics hold or not, using dynamic runtime analysis.

3. Offline analysis Vs. Online analysis.

"DejaVu" [17] enables developers to check software systems safety temporal property expressed in first-order past linear-time temporal logic (FO-PLTL) for events that carry data. DejaVu inputs a trace log (offline) and a FO-PLTL formula, and outputs a boolean value for each position in the inputted trace. "LogScope" [16] checks, offline, software systems correctness properties expressed using a rule-based specification language over state machines. "LogScope" translates specifications into C++ monitors (that could carry data). "EventRaceCommander" [2] repairs in web applications (online), event race errors, a kind of safety error. "Purify" [15] doesn't allow for custom SUT specifications. It has built-in memory access safety properties to check offline on program execution, after instrumentation of the SUT, its third-party, and vendor object-code libraries.

YR_DB_RUNTIME_VERIF inputs a SUT correctness property specification as a state diagram (as a subset of LTL [10]). States diagrams specifications are implemented as C++ program monitors using C++ library yr_sd_runtime_verif. In contrast to DejaVu and "LogScope" which input desirable SUT behavior, YR_DB_RUNTIME_VERIF inputs a not desirable (erroneous) SUT behavior. YR_DB_RUNTIME_VERIF outputs a developer given string message ¹ in case an accepting state was entered, and a trace event of YEROTH-ERP-3.0 leading to it (online). YR_DB_RUNTIME_VERIF's monitors need not store data, as DejaVu monitors must. YR_DB_RUNTIME_VERIF events also carry data (database table and column name, records quantity modified by current SUT event). Runtime monitors could be checked against programs written in any programming language or framework, as long as they emit necessary SQL events to YR_DB_RUNTIME_VERIF.

¹ 'YR_DB_RUNTIME_VERIF_Monitor_notify_SUCCESS_VERIFICATION' in this paper motivating example in Figure 5.

8 Conclusion And Future Work

This paper has presented a lightweight C++ Qt-Dbus [19] tool to check a program against a runtime monitor using set interface operations (\in , \notin) on program statement: YR_DB_RUNTIME_VERIF. YR_DB_RUNTIME_VERIF doesn't generate false positives because its specifications are *not desirable* (erroneous) specifications.

Since the concurrent communication between YR_DB_RUNTIME_VERIF and a program occurs over the RPC instance Dbus, a runtime monitor could be checked against programs written in any programming language or framework, as long as they emit necessary SQL events to YR_DB_RUNTIME_VERIF.

Runtime monitors specifications in **yr_sd_runtime_verif**, modeled as state diagram mealy machines, augmented with accepting states, and pre-/post-condition on state diagram transitions, as introduced in Section 3 of this paper, don't allow more than 2 states and 1 state diagram transition currently.

Future work would be an algorithm within <code>yr_sd_runtime_verif</code>, to compose single 2–states state diagram mealy machines. The author of this paper is also planing to create a specification language and a compiler for it so to automatically generate <code>yr_sd_runtime_verifC++</code> state diagram specification code.

STATE(D);
NOT_IN (YR_ASSET,
product_department.department_name)

select.product_department
DB_IN (YR_ASSET,
stocks.department_name)

Fig. 11: 'YR_QVGE' model for the running example in Figure 2.

Also, the author of this paper has started, as future work, creating a graphical drawing tool (YR_QVGE) for in Section 3 defined state diagrams. A model is shown in Figure 11. It is an extension of the FOSS (Free and Open Source Software) Graphviz [13] drawing tool QVGE [26].

9 Acknowledgments

THE AUTHOR OF THIS PAPER THANK JAN PELESKA, AND THOMAS NDIE DJOTIO FOR HELPING THEM IN CONTINUING THIS RESEARCH.

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A YEROTH-ERP-3.0 MAINTENANCE VERIFICATION INTERFACE

