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User's Guide for the Design and Testing System YEROTH_QVGE (YR_QVGE)

PROF. DR.–ING. DIPL.–INF. XAVIER NOUMBISSI NOUNDOU CONTACT: yeroth.d@gmail.com

Table 1: STATE DIAGRAM MEALY MACHINE SPECIFICATION KEYWORDS in YEROTH_QVGE

scientific keywords	engineering keywords
IN_PRE	IN_BEFORE
IN_POST	IN_AFTER
IN_SET_TRACE	IN_SQL_EVENT_LOG
NOT_IN_PRE	NOT_IN_BEFORE
NOT_IN_POST	NOT_IN_AFTER
NOT_IN_SET_TRACE	NOT_IN_SQL_EVENT_LOG

Figure 1: A SAMPLE state diagram mealy machine file.

```
1. yr_sd_mealy_automaton_spec yr_missing_department_NO_DELETE
2. {
3. START_STATE(d):NOT_IN_BEFORE(YR_ASSET,department.department_name)
4. ->[in_sql_event_log('DELETE.departement.YR_ASSET',STATE(d))]/'SELECT.department'->
5. ERROR_STATE(e):IN_AFTER(YR_ASSET,stocks.department_name).
6. }
```

Figure 2: A SCREENSHOT OF YEROTH_QVGE.

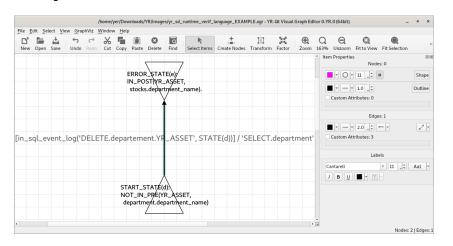
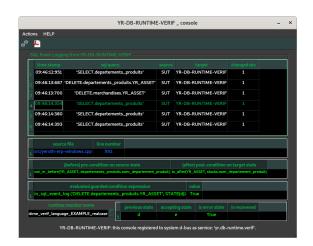
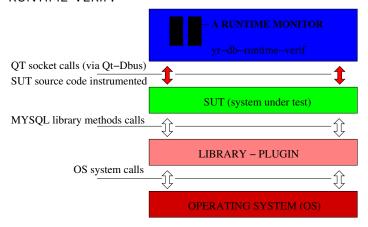


Figure 3: A SCREENSHOT OF YR-DB-RUNTIME-VERIF SQL EVENT LOG.



1 Introduction

Figure 4: SOFTWARE ARCHITECTURE OF YR-DB-RUNTIME-VERIF.



This user's guide helps briefly and concisely how to create a binary executable of the runtime monitoring testing tool YRDB-RUNTIME-VERIF having user defined runtime monitors. The guide also specifies keywords allowed within runtime monitor specifications as State Diagram Mealy Machines.

2 State Diagram Mealy Machine Specification Keywords

TABLE 1 details scientific keywords and their engineering counterpart that can be used in describing state diagram mealy machine in YEROTH_QVGE Design and Testing System.

3 Custom User Project

- Property configuration file:
- User project directory structure:
- Generate an executable for a user defined runtime monitor:

4 Formal Scientific and Engineering Project Description

Information Brochure of the Design and Testing System YEROTH_QVGE (YR_QVGE)

PROF. DR.–ING. DIPL.–INF. XAVIER NOUMBISSI NOUNDOU CONTACT: yeroth.d@gmail.com

Table 1: EQUIVALENCES

scientific literature	engineering acronym
PRE	BEFORE
POST	AFTER
A TRACE	AN EVENT LOG
A FINAL STATE	AN ERROR STATE

Figure 1: A SAMPLE state diagram mealy machine file.

```
1. yr_sd_mealy_automaton_spec yr_missing_department_NO_DELETE
2. {
3. START_STATE(d):NOT_IN_BEFORE(YR_ASSET,department.department_name)
4. ->[in_sql_event_log('DELETE.departement.YR_ASSET',STATE(d))]/'SELECT.department'->
5. ERROR_STATE(e):IN_AFTER(YR_ASSET,stocks.department_name).
6. }
```

Figure 2: A SCREENSHOT OF YEROTH_QVGE.

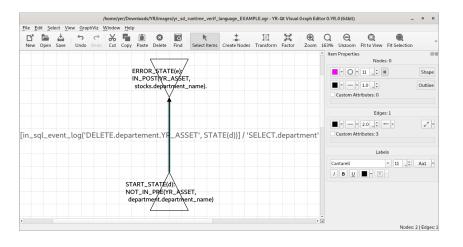
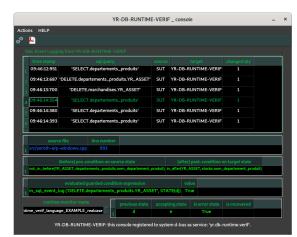


Figure 3: A SCREENSHOT OF YR-DB-RUNTIME-VERIF SQL EVENT LOG.



Developer Biography



Figure 4: Portrait of XAVIER.

PROF. DR.-ING. DIPL.-INF. XAVIER NOUMBISSI **NOUNDOU** is a CHRISTIAN BY FAITH, Cameroonian, born on September 16 1983 in DOUALA (LITTORAL region, CAMEROON). Xavier has a "Diplom-Informatiker (Dipl.-Inf.)" qualification from the University of Bremen, Bremen, Bremen, GERMANY (May 25, 2007). XAVIER NOUMBISSI NOUNDOU IS A PHILOSOPHIAE DOCTOR (PH.D.) from THE UNIVERSITY OF WATERLOO (ON, CANADA); DECEM-BER 20, 2011!

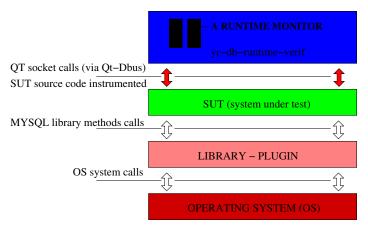
PROF. DR.-ING. DIPL.-INF. XAVIER NOUMBISSI NOUNDOU has worked together with PROF. DR. RER. NAT. HABIL. jan peleska, at AGBS-university of bremen; and 2 years later at WatForm (Waterloo Formal Methods) with Prof. PATRICK LAM, PH.D. (MIT, BOSTON, MA, USA).

Xavier has following academic and professional engineering research contributions:

- 1. 'Context-Sensitive Staged Static Taint Analysis For C using LLVM'
 - 1. source code in C++: https://github.com/sazzad114/saint
 - 2. fulltext: https://zenodo.org/record/8051293
- 2. 'YEROTH-ERP-3.0':
 - 1. source code in C++:
 - a. YEROTH-ERP-3.0: https://github.com/yerothd/ yeroth-erp-3-0
 - b. YEROTH-ERP-3.0 SYSTEM DAEMON: https://github.com/yerothd/ yeroth-erp-3-0-system-daemon
 - 2. full text (ongoing publication): https://zenodo.org/record/8052724
- 3. 'Statistical test case generation for reactive systems' at RTT-MBT at (https://www.verified.de) Verified Systems International GmbH.

Introduction

Figure 5: RUNTIME-VERIF.



YEROTH_QVGE is a CASE (Computer-Aided Software Engineering) design tool to generate "domain-specific language (DSL) YR_SB_RUNTIME_VERIF_LANG 1" files, to be inputted into the "compiler YR_SB_RUNTIME_VERIF_LANG_COMP", so to generate C++ files for the "runtime verifier tester YR-DB-RUNTIME-VERIF 2" that allows for manual verification of SQL correctness properties of Graphical User Interface (GUI) software.

YR-DB-RUNTIME-VERIF inputs SQL correctness properties expressed using the formalism "state diagram mealy machine (YR_SD_RUNTIME_VERIF)". The Free Open Source Code Software (FOSS) tool-chain of development testing is located as follows for free, EXCEPT for "YEROTH_QVGE" that is a Closed Source Code Software (CSCS):

- COMPILER YR_SB_RUNTIME_VERIF_LANG_COMP (i.e.: YR_SB_RUNTIME_VERIF_LANG): https://github.com/yerothd/yr_sd_runtime_ verif_lang
- RUNTIME VERIFIER TESTER YR-DB-RUNTIME-VERIF: https://github.com/yerothd/ yr-db-runtime-verif
- state diagram mealy machine UNIT TESTS CODE (i.e.: YR_SD_RUNTIME_VERIF): https://github.com/yerothd/yr_sd_runtime_ verif_UNIT_TESTS
- state diagram mealy machine (i.e.: YR_SD_RUNTIME_VERIF): https://github.com/yerothd/yr_sd_runtime_ verif

Advantages of YEROTH_QVGE

A sample state diagram mealy machine is shown in Figure 1.

WITH manual drawing of SQL CORRECTNESS PROPERTY MODEL, you are freed from manually writing "state diagram mealy machine text files" that could be tedious and lengthy. Also, editing state diagram mealy machine files manually could be more error-prone than letting a compiler (YR_SB_RUNTIME_VERIF_LANG_COMP) do it for you.

Conclusion

SOFTWARE ARCHITECTURE OF YR-DB- YEROTH_QVGE costs only 3,000 EUROS. WE ONLY SUP-PORT DEBIAN-LINUX (https://www.debian.org).

¹https://github.com/yerothd/yr_sd_runtime_verif_lang

²https://github.com/yerothd/yr-db-runtime-verif