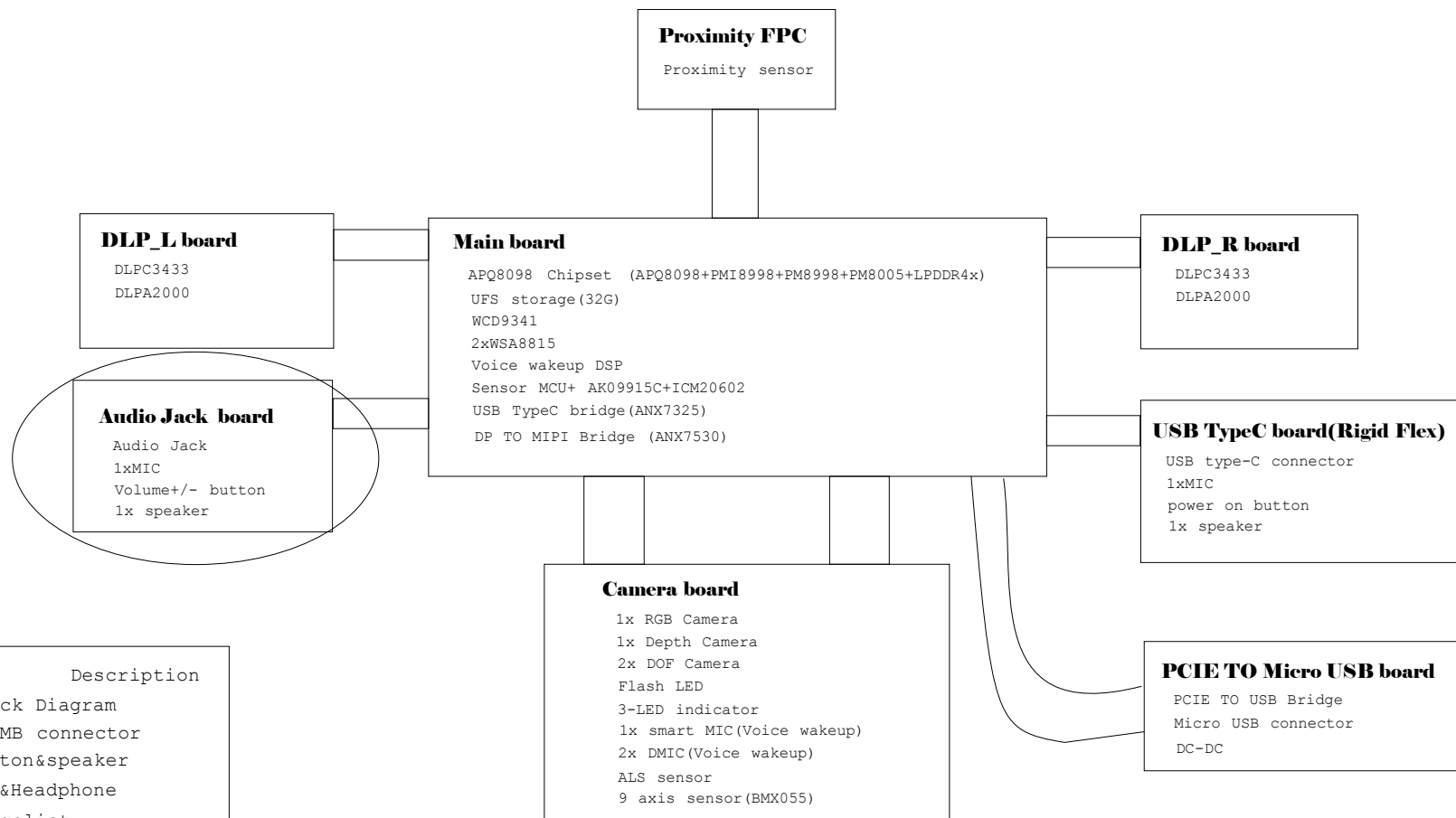


HMD system Architecture

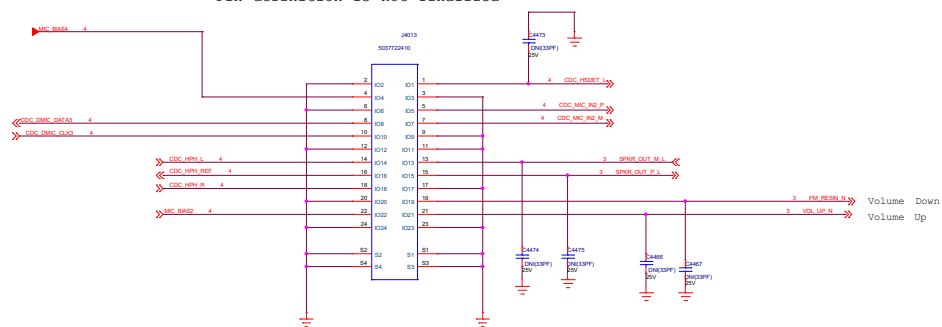


INDEX

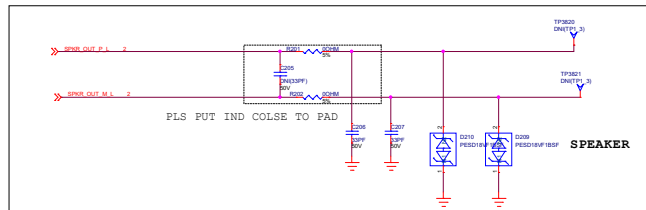
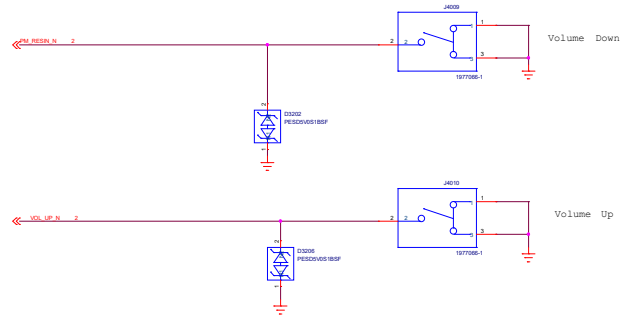
| Page | Description |
|------|-----------------|
| 1. | Block Diagram |
| 2. | To MB connector |
| 3. | Button&speaker |
| 4. | MIC&Headphone |
| | Changelist |



Pin definition is not finalized



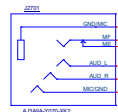
| Signal | Description |
|-----------------------|----------------|
| KPSNS0 | Volume Up |
| PM_RST_N | Volume Down |
| KYPD_PWR_N | PWER ON |
| PM_RST_N + KYPD_PWR_N | Hardware Reset |





AUDIO JACK

confirm with Audio Jack supplier



Note: EURO/US compatible design,
Default US type

Note: Place close to headset jack connector

 Note: For Cable detection



Change list

EVT change list

Revision History



| | | | |
|---------|-----------------------|--|--------------|
| File | | | |
| <Title> | | | |
| Size | Document Number | | Rev |
| D | <Doc> | | <R> |
| Date: | Friday, July 14, 2017 | | Sheet 3 of 3 |