

- (b) [6 points] Find the clock cycles required in both cases.

According to my previous work from 3a,
I calculated CPU clock cycles for
Both cases.

P₁ clock cycle required is 2.6×10^6 and

P₂ clock cycle required is 2×10^6

4. Compilers can have a profound impact on the performance of an application. Assume that for a program, compiler A results in a dynamic instruction count of 1.0E9 and has an execution time of 1.1s, while compiler B results in a dynamic instruction count of 1.2E9 and an execution time of 1.5s.

- (a) [6 points] Find the average CPI for each program given that the processor has a clock cycle time of 1ns.

$$\text{CPU time} = \frac{\text{IC} \times \text{CPI} \times \text{cycle time}}{\text{IC}}$$

$$1\text{ns} = 10^{-9}\text{s}$$

$$\text{CPI} = \frac{\text{CPU time}}{\text{IC} \times \text{cycle time}}$$

$$A \rightarrow \text{CPI} = \frac{1.1\text{s}}{10^9 \times 10^{-9}\text{s}} = 1.1$$

$$B \rightarrow \text{CPI} = \frac{1.5\text{s}}{1.2 \times 10^9 \times 10^{-9}\text{s}} = 1.25$$