Study Questions (Chapter 03 – Part 1)

1. What do the following terms stand for?
   a. CPU
   b. MAR
   c. MBR
   d. IR
   e. PC
   f. ISA
   g. RTL
   h. RISC
   i. CISC
   j. ALU
   k. CCR

2. List the various registers in a stores program computer.

3. Describe, using RTL notation, how RISC processors perform the fetch phase of the processing cycle.

4. Describe, using RTL notation, how RISC processors perform the execute phase of the processing cycle.

5. List the name of the ARM CPSP's status bits (flags).

   Question 3.3 on page 224: For each of the following 6-bit operations calculate the values of the C, Z, V, and N flags.
   a. 001011 + 001101
   b. 111111 + 000001
   c. 000000 - 111111
   d. 101101 + 011011
   e. 000000 – 000001
   f. 111110 + 111111