

N: negative

R13: SP

Z: zero

R14: LR

C: carry

R15: SP.

V: overflow.

H=0 \Rightarrow second operand is not a constant.

*: first digit \rightarrow sign bit.

test/compare \Rightarrow S=1.

immediate	shift	literal	LSL/LSR
dynamic		register	ASR
			ROR
word access			RRX

STM LDM

FD: DB 10 IA 01

FA: IB 11 DA 00

ED: DA 00 IB 11

EA: IA 01 DB 10

FD: the position that SP pointing is Full after the line is executed.

push: decrement SP before storing value

pop: increment SP before popping value

(since SP stays in the next position in stack).