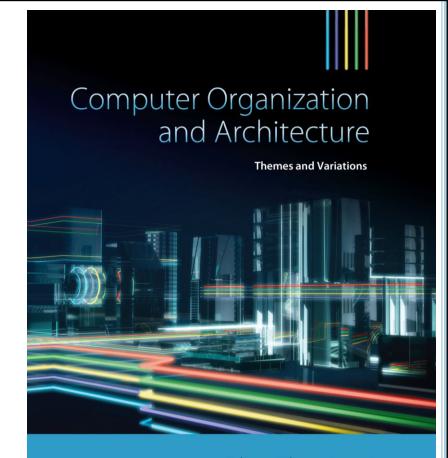
Part B

CHAPTER 3

Architecture and Organization



Alan Clements

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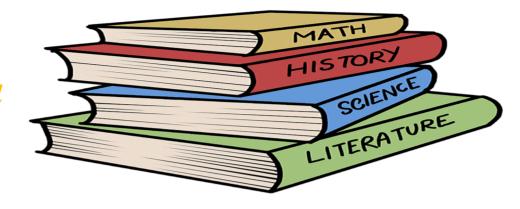
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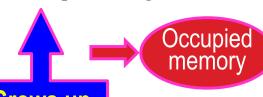
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☐ The stack is a data structure, a *last in first out queue*, LIFO, in which items *enter at one end* and *leave from the same end* in a *reverse order*.



- □ Stacks in microprocessors are implemented by using a *stack pointer* to point to the *top of the stack (TOS)* in memory.
- ☐ As items are
 - o added (*pushed*) onto the stack, the stack pointer is moved *forward*, or
 - o removed (popped) from the stack, the stack pointer is moved backward
- □ There are four ways of constructing a stack, depending on the definition of the *top of the stack (TOS)* and the definition of the *forward / backward*. (See Figure 3.45 over the coming 4 slides)

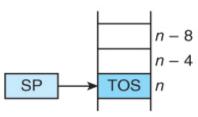


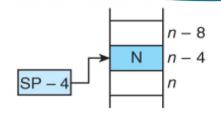
Grows up

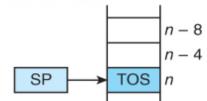
Initial state of the stack

Each stack's entry here is 4 bytes.

(a) Stack grows up. Stack pointer points to TOS.







Pre-update

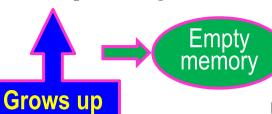
```
PUSH: SUB SP, #4 ; [SP] \leftarrow [SP] - 4 Adjust the stack pointer STR R0, [SP] ; [[SP]] \leftarrow data Push data onto the stack or simply STR R0, [SP, #-4]!
```

```
POP: LDR R0, [SP] ; data \leftarrow [[SP]] Pull data off the stack ADD SP, #4 ; [SP] \leftarrow [SP] + 4 Adjust the stack pointer or simply LDR R0, [SP], #4
```

Post-update

TOS means top of stack

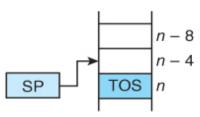
163

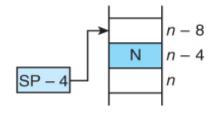


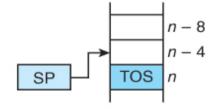
Initial state of the stack

Each stack's entry here is 4 bytes.

(b) Stack grows up. Stack pointer points to first free space.







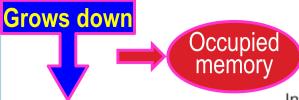
```
PUSH: STR R0,[SP] ;[[SP]] \leftarrow data Push data onto the stack SUB SP,#4 ; [SP] \leftarrow [SP] - 4 Adjust the stack pointer or simply STR R0,[SP],#-4
```

Post-update

Pre-update

```
POP: ADD SP, #4 ; [SP] \leftarrow [SP] + 4 Adjust the stack pointer LDR R0, [SP] ; data \leftarrow [[SP]] Pull data off the stack or simply LDR R0, [SP, #4]!
```

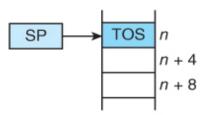
TOS means top of stack

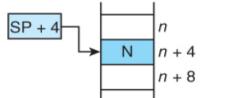


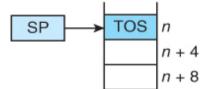
Initial state of the stack

Each stack's entry here is 4 bytes.

(c) Stack grows down. Stack pointer points to TOS.







Pre-update

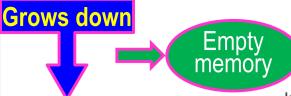
```
PUSH: ADD SP, #4 ; [SP] \leftarrow [SP] + 4 Adjust the stack pointer STR R0, [SP] ; [[SP]] \leftarrow data Push data onto the stack or simply STR R0, [SP, #4]!
```

```
POP: LDR R0,[SP]; data \leftarrow [[SP]] Pull data off the stack SUB SP,#4 ;[SP] \leftarrow [SP] - 4 Adjust the stack pointer or simply LDR R0,[SP],#-4
```

Post-update

TOS means top of stack

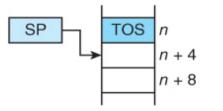
165

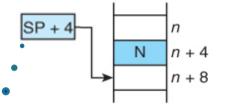


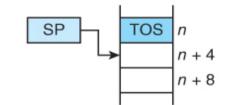
Initial state of the stack

Each stack's entry here is 4 bytes.

(d) Stack grows down. Stack pointer points to first free space.







It is SP+4, not SP+8

```
PUSH: STR R0, [SP]; [[SP]] \leftarrow data Push data onto the stack ADD SP, #4; [SP] \leftarrow [SP] + 4 Adjust the stack pointer
```

or simply

STR R0, [SP],#4

Post-update

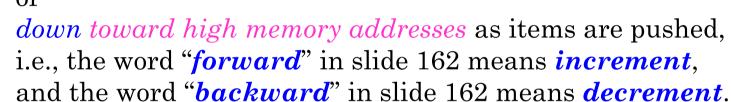
Pre-update

POP: SUB
$$SP$$
, #4 ; [SP] \leftarrow [SP] - 4 Adjust the stack pointer LDR $R0$, [SP] ; data \leftarrow [[SP]] Pull data off the stack or simply LDR $R0$, [SP, #-4]!

TOS means top of stack

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- ☐ The *two decisions* need to be made when implementing a stack are
 - o whether the stack grows
 - up toward low memory addresses as items are pushed, i.e., the word "forward" in slide 162 means decrement, and the word "backward" in slide 162 means increment or





- o whether the stack pointer points to
 - the *top item* on the stack or



• the *first free empty space* on the stake.



- □ CISC processors automatically maintain the stack.
- □ RISC processors force the programmer to maintain the stack.