```
Neek 2.
  P+. 1
  Bits (Binary digits)
                        1 byte = 8 bits.
  10 bits = 1 k values.
                        ASCII => 7 bit vode.
                                reperesent 128 Latin Alphabet.
           1 Mega
  W
           I higa. Unicode => 16 bits.
  ζο
                                 includes non-Latin alphabets.
 ASCII lodes: : 6-31 Non-Printable charactors. 127 del.
                32-47, 18-64, 91-96, 123-126 Symbols.
               48-57 Numbers.
               65-90 Capital charactors.
               97-122 Minor charactors.
  Base b: N= \sum a; bi
  Some Fractions cannot be reperesented in other base.
 Range Max-Min
         n bits binary range from o to 2n-1.
 Precision: How precise the number is reperesent. e.f. 3.14119
         a. b cde => 5 decimal digits
                       its presicion is I in 105.
                       I significant Figures.
  Accuracy & Error: The difference between the measure cake
  Acuracy
                and the object's actual rathe.
                  e.f. Th=3.14 is not error.
                       Measure a wo cm stick as 19.89 cm,
```

is not the same. It the error is 0.11cm.

Pt.2

Sign and Magnitude representation.

(-1) 5 x M. the range is 2n. (-2n-2n-1)

=> e.g. +13,0 = 000011012

-13.0=100011012.

Which is used in Hoating point arithmetic.

Two vorys en reperesent 0: 00 or 10 (to/-0)

Biased Reperesent Lexiess - k)

Shift the scale to have only non-negative values.

Range: 2n (0-2n-1)

Two's Complementary Arithmetic.

Range: 2n C-2n-1-2n-1) zero has only one reperentation.

P+Q=2ⁿ In 2's Complementary, negative starts with 1 While positive numbers start with 0.

e.g. 210 = 000000010 - 210 = [11111110

Operation results fall outside the range => Overflow occurs.

overflow; adding two positive number yielding negative.

two negative positive.

e.f. 0/1/1/2+0/0002= 10/1/2.
positive positive negative.

Sign bit of result different from sign but of A and B => Overflow Occurs.

Carry-in bit => sign bit Carry-out bit - next bit of sign bit.

e.f. 0011 In this case, corry in bit is not equal to 111 to 10 to carry out bit, an overflow occurs. Shifting: Moving all bits left/right, and put zero

at the end.

Shifting left equals x the base number.