

# Yamilet Rocha

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## EDUCATION

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### Bachelor of Science in Electrical Engineering - Junior

The University of Texas at El Paso (UTEP)

Expected: May 2027.

Overall GPA: 3.00/4.00

- M.S. and Meek Lane Doss - Academic Scholarship

Awarded: 2022

## EXPERIENCE

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### Silicon Engineering Intern

May 2025 – Aug 2025

#### Microsoft – Silicon Valley Campus

Mountain View, CA | Silicon Valley

- Supported post-silicon validation and debug for next-generation Microsoft processors, collaborating with cross-functional teams to identify and resolve hardware issues.
- Developed Python automation scripts to streamline test workflows and analyze silicon performance data, improving validation efficiency by 20%.

### VLSI Nanotechnology Teacher Assistant

Jan 2025 – May 2025

#### Nanotechnology Lab - UTEP

El Paso, TX

- Instructed students in cleanroom fabrication processes, including solar wafer fabrication.
- Led small-group sessions and virtual labs using Synopsys tools for VLSI design training.

### Undergraduate Biomedical Research Assistant

Oct 2024 – Present

#### Castro Lab - UTEP

El Paso, TX

- Conducted research on mitochondrial biosensors, focusing on impedance modeling for biomedical applications.
- Co-authored a paper accepted for publication in IEEE Transducers, contributing to experimental design, data analysis, and manuscript preparation.

### Microelectronics Mentorship Intern

Jun – Dec 2024

#### Siemens EDA - Remote

Indiana

- Designed hardware stack-up for drone system, defining system-level PCB integration requirements.
- Developed a Verilog-based elevator controller and explored advanced verification using UVM.

## VOLUNTEERING AND ORGANIZATIONS

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- IEEE Member Resources Officer, UTEP – El Paso, TX | Jan 2025 – Present
- Volunteer, Kelly Center for Hunger Relief – El Paso, TX | Summers, 2022 – Present
- Team Member, GAMBIT – VR Game Development Team | 2024 – Present

## SKILLS

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- Technical Languages: Python, C, Verilog, SystemVerilog, UVM
- Engineering Tools: Altium Designer, SolidWorks, Fusion 360, AutoCAD, LTspice, MATLAB, Synopsys HSPICE & Design Compiler
- Technical Skills: PCB layout, cleanroom fabrication, semiconductor testing
- Languages: English & Spanish (bilingual, written and oral)