**Team Contract**

Team Name: Better than Your CPU

Date: 9-22-16

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| GOALS: What are our team goals for this project?  What do we want to accomplish? What skills do we want to develop or refine? |
| * To complete the projects in full, with all functionality working correctly * To better our Verilog skills and our understanding of the MIPS CPU architecture * To improve our team skills |
| EXPECTATIONS: What do we expect of one another in regard to attendance at meetings, participation, frequency of communication, the quality of work, etc.? |
| * Every team member is expected to attend all team meetings unless there is a special condition * Every team member should participate and contribute equally * Communicate as frequent as necessary * Code comments is required for each module. |
| POLICIES & PROCEDURES: What rules can we agree on to help us meet our goals and expectations? |
| * Attend every team meeting and on time * Frequent Code Reviews(at least once a week) will help us find out any bugs before they get too problematic * Test your own modules to make sure they work properly before integrating them into the final system |
| CONSEQUENCES: How will we address non-performance in regard to these goals, expectations, policies and procedures? |
| * Talk to underperforming team members first * Try to resolve conflicts within the group. You will have up to three strikes, then we will have to involve the professor. * In the event of a communication failure, the following seem like appropriate measures:   + Including but not limited to:     - Doing/Redoing the work     - ⅓ Grade loss in this project |

We share these goals and expectations, and agree to these policies, procedures, and consequences.

Team member name:Yuxuan Huang

Team member name: Jiayu Huang

Team member name: Kenny Rader