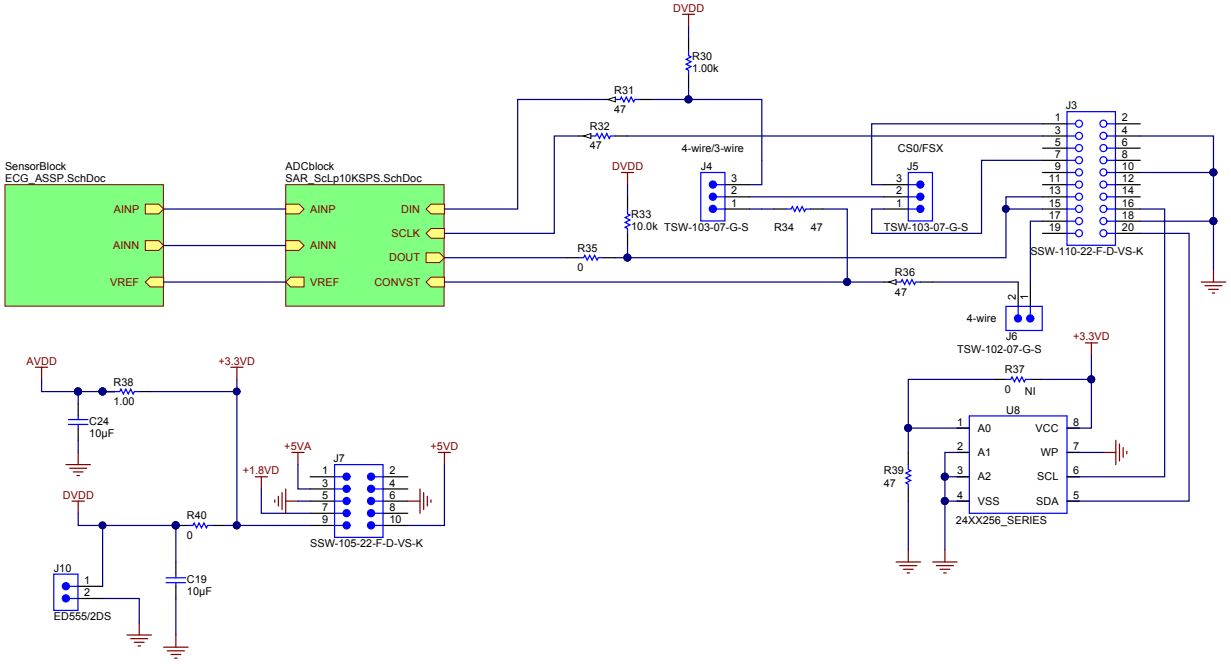
Revision History		
	Date	Notes
A	05/13/2013	Initial Release
B	05/30/2013	Minor value changes, improved layout

Ultra Low Power, High Precision ECG System



PCB
LOGO
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FID1 FID2 FID3

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DRAFTSMAN: DrawnBy	DATE: 05/30/2013	
DESIGNER: Engineer	DATE: 05/30/2013	
CHECKER: Checker	DATE: 05/30/2013	
ENGINEER: Engineer	DATE: 05/30/2013	
APPROVED: Approver	DATE: 05/30/2013	
RELEASED: B McKay	DATE: 05/30/2013	

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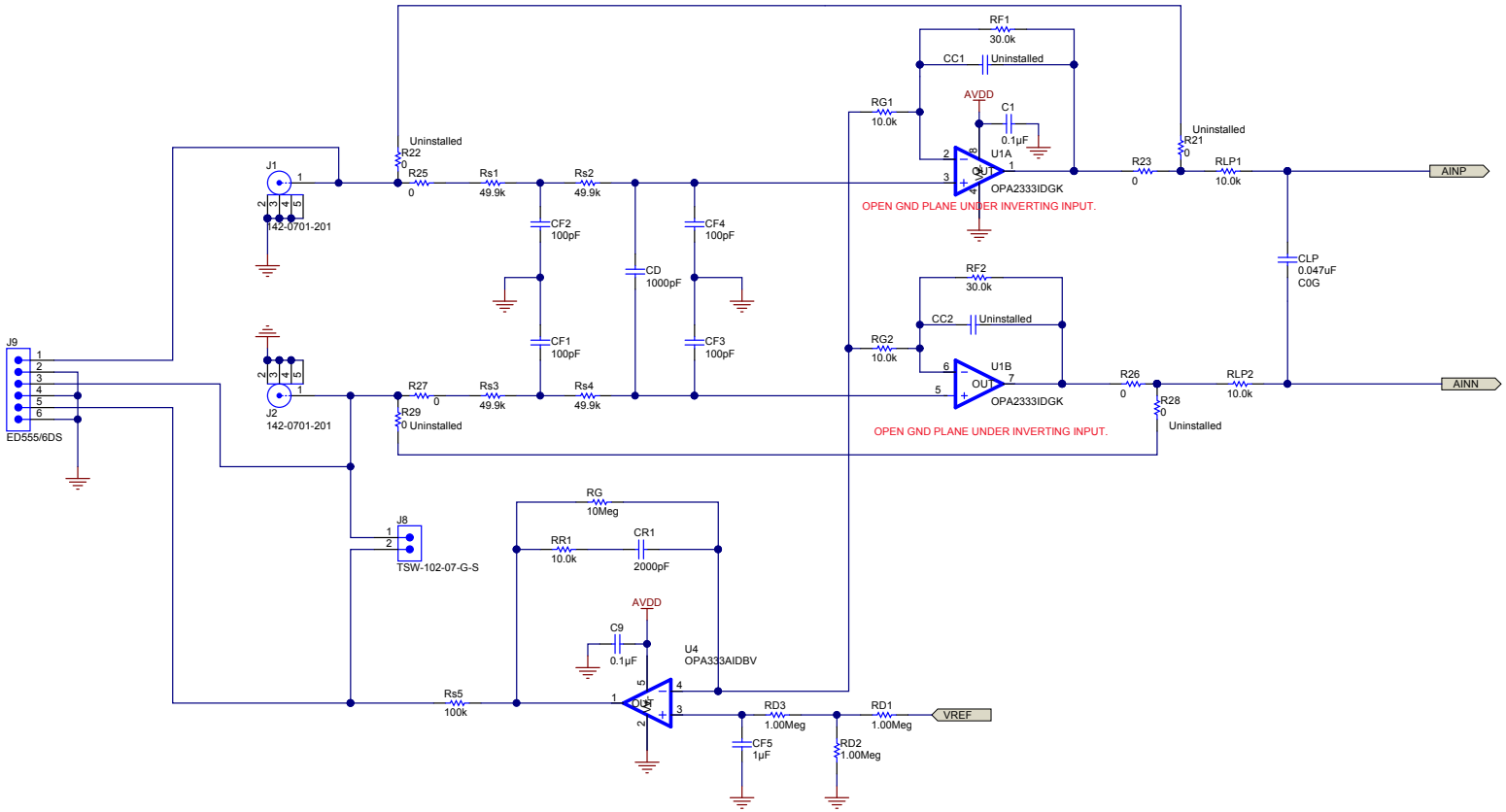
TITLE:
SCHEMATIC, EVM,
TI DESIGNS FOR
LOW POWER ECG SYSTEM

SCALE	SIZE		REV	SHEET
N	B	6571549	B	1 / 3

Ultra Low Power, Low Noise, High Zin Gain Block with Shield Drive



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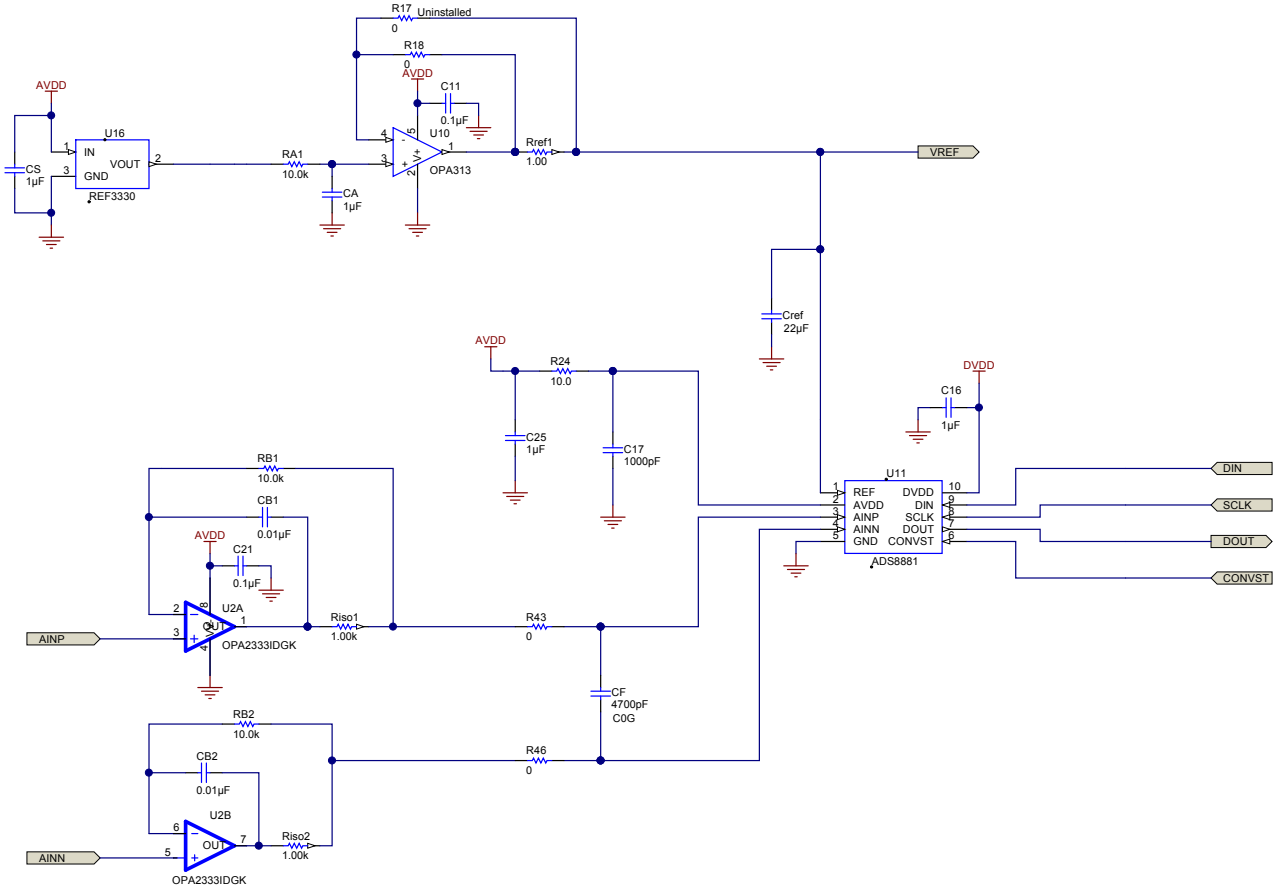
SCALE	SIZE		REV	SHEET
N	B	6571549	B	2 / 3

Ultra Low Power, 18 Bit, 10 KSPS SAR ADC Block



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N	B	6571549	B	3 / 3