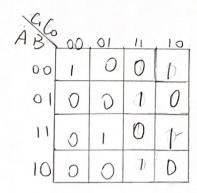
- 5. Using the block editor of Quartus Prime, draw a logic schematic to implement NAND-only logic circuit.
 - Note: Quartus Prime does not have a five input NAND gate. In case you need
 to use five-input NAND, you can use a six-input NAND gate with input pins 5
 and 6 connected to the same signal.
- 6. Print out the truth table on the next page and fill out the expected values in the output column F and the remaining output columns during your lab session.

| Inputs | | | | Outputs | | | | |
|--------|-------|---|---|---------|-----|---|-----|-----------|
| C_1 | C_0 | A | В | F | vhd | vhd DE1 | bdf | bdf DE1 |
| 0 | 0 | 0 | 0 | 21 | -1 | print the second | 1 | |
| 0 | 0 | 0 | 1 | 0 | 0 | | 0 | |
| 0 | 0 | 1 | 0 | 0 | 0 | | 0 | |
| 0 | 0 | 1 | 1 | 0 | 0 | a africana | 0 | |
| 0 | 1 | 0 | 0 | 0 | n | 4 | 0 | |
| 0 | 1 | 0 | 1 | 0 | 0 | | 0 | |
| 0 | 1 | 1 | 0 | 0 | 0 | e desperato de esta esta esta esta esta esta esta est | 0 | |
| 0 | 1 | 1 | 1 | 1 | 1 | | 1 | |
| 1 | 0 | 0 | 0 | 1 | 1 | | i | Par La |
| 1 | 0 | 0 | 1 | 0 | 0 | | 0 | |
| 1 | 0 | 1 | 0 | Ŏ | 0 | | 0 | |
| 1 | 0 | 1 | 1 | 1 | | | 1 | |
| 1 | 1 | 0 | 0 | 0 | 0 | | 0 | |
| 1 | 1 | 0 | 1 | 1 | 1 | | 1 | Barrier - |
| 1 | 1 | 1 | 0 | 1 | 1 | | 1 | |
| 1 | 1 | 1 | 1 | 0 | 0 | | 0 | |



$$F = m_0 + m_7 + m_{13} + m_{14} + m_8 + m_{11}$$

$$= (\bar{C}_1 \bar{C}_0 \bar{A} \bar{B}) + (\bar{C}_1 \bar{C}_0 \bar{A} \bar{B})$$

$$+ (\bar{C}_1 \bar{C}_0 \bar{A} \bar{B})$$