

Release Notes for AT32F403A_407 Firmware Library Drivers

V2.0.3-2023/10/26

1. Added the interrupt_flag_get to each of the IP functions
2. Optimized system clock switching procedure

V2.0.2-2023/08/04

1. Added a feature supporting modification of CRC driver polynomial and its width
2. Added the flash_spim_dummy_read and flash_spim_mass_program to Flash
3. Updated the procedure of running the adc_ordinary_channel_set and adc_preempt_channel_set
4. Updated some notes

V2.0.1-2023/02/16

1. Updated the EMAC_MAX_PACKET_LENGTH to 1524

V2.0.0-2022/11/18

1. Updated the function “xmc_ext_timing_config”
2. Updated the configuration for CRM_HICK48_NODIV to be used as default option when HICK is selected as PLL clock source