

ENGINEERING TRIPOS PART IIA

Project GF2

SOFTWARE

LOGIC SIMULATOR

First Interim Report

Name: Yi Chen Hock, Michael Stevens, Cindy Wu
College: Queens', Robinson, John's

Contents

1	Introduction	2
2	General Approach	2
3	Teamwork Planning	2
4	EBNF for Syntax	2
4.1	Example A - Simple Circuit	2
4.2	Example B - Complex Circuit	2
5	Errors	2
5.1	Semantic Error Identification	2
5.2	Error Handling	2

- 1 Introduction**
- 2 General Approach**
- 3 Teamwork Planning**
- 4 EBNF for Syntax**
 - 4.1 Example A - Simple Circuit**
 - 4.2 Example B - Complex Circuit**
- 5 Errors**
 - 5.1 Semantic Error Identification**
 - 5.2 Error Handling**