

## INMP Review Meeting

**Date: November 17, Thursday, 2016 Time: 8:30am – 4:00 pm**

**Location: Cypress Semiconductor Auditorium, Paul Allen Building, Stanford University**

### Agenda

|              |   |                                       |
|--------------|---|---------------------------------------|
| 8:00am       | Continental Breakfast   |                                       |
| <b>8:30</b>  | <b>2D Materials, Contacts and Interconnects I</b>   | <b>Chair: Yoshio Nishi</b>            |
| 8:35         | Unipolar n-Type Black Phosphorus Transistors with Scandium Contacts                                   | Fiona Wang                            |
| 8:55         | Electronic Properties and Contact Engineering of Black Phosphorus: Insight from Ab-Initio Simulations | Yi-Chia Tsai and Blanka Magyari Kope  |
| <b>9:15</b>  | <b>2D Materials, Contacts and Interconnects II</b>  | <b>Chair: Philip Wong</b>             |
| 9:20         | BEOL Compatible Graphene/Cu Composites for Better Interconnect  | Ling Li                               |
| 9:40         | Contact Engineering in n-Type MoTe <sub>2</sub> Transistors   | Michal Mleczko                        |
| 10:00        | Comparing Surface and Edge Contacts on Large Area MoS <sub>2</sub>                                    | Alvin Tang                            |
| 10:20        | <b>Break</b>  |                                       |
| <b>10:40</b> | <b>High Performance CMOS</b>  | <b>Chair: Krishna Saraswat</b>        |
| 11:00        | Low Resistance Contacts to Nanoscale MOSFETs  | Pranav Ramesh,                        |
| 11:05        | Nanoscale Ge NMOSFET  | Junkyo Suh                            |
| 11:25        | System Level Performance Analysis of Transistor and Interconnect at 5nm Node                          | Vince Lee                             |
| 11:45        | Antimonide Based III-V MOSFET Integration on Si   | Archana Kumar                         |
| 12:05pm      | <b>Lunch</b>  |                                       |
| <b>1:00</b>  | <b>Interface Defect Passivation and Dielectric Design</b>   | <b>Chair: Paul McIntyre</b>           |
| 1:05         | Hydrogen Depassivation of Defects and Reliability of High-k/InGaAs MOS                                | Kechao Tang                           |
| 1:25         | Ge-Sn Nanowires and Electrochemical Characterization of Nanowire Interface States                     | Andrew Meng                           |
| 1:45         | <b>Break for Poster Preparation</b>   |                                       |
| <b>2:00</b>  | <b>Student Posters</b>  |                                       |
|              | All students under INMP funding will have posters.  | All Students                          |
| 3:00         | AdCom and Faculty Meeting (Closed Session) in Allen 101, Linville conference room                     | <i>AdCom members and Faculty only</i> |
| 4:00         | Adjourn   |                                       |

### Poster presentations

All oral presentation speakers will have posters.