

# Cyclops

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**Cycle + Operations => Cyclops**

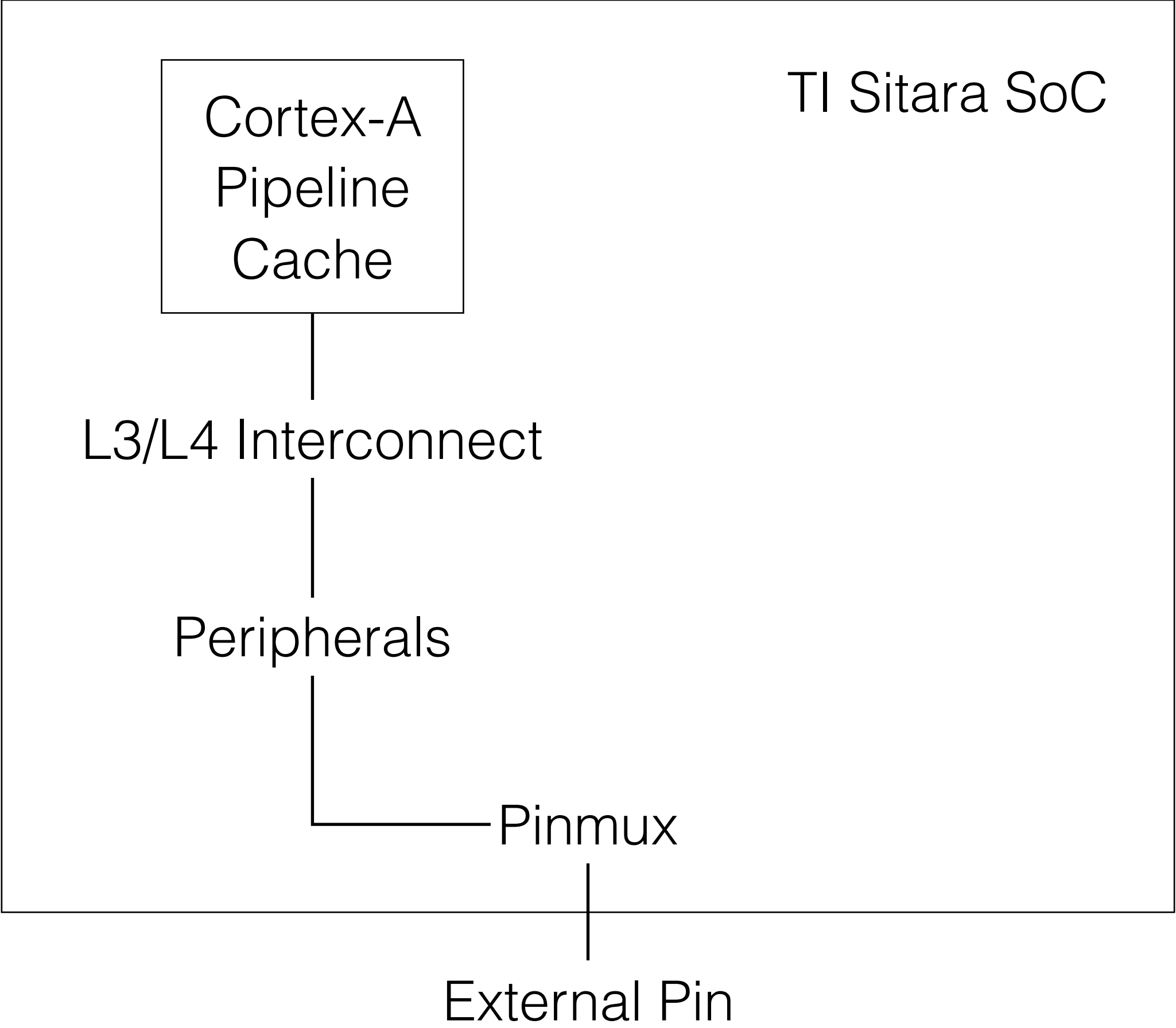
PL + Compiler + **IDE** => Cyclops

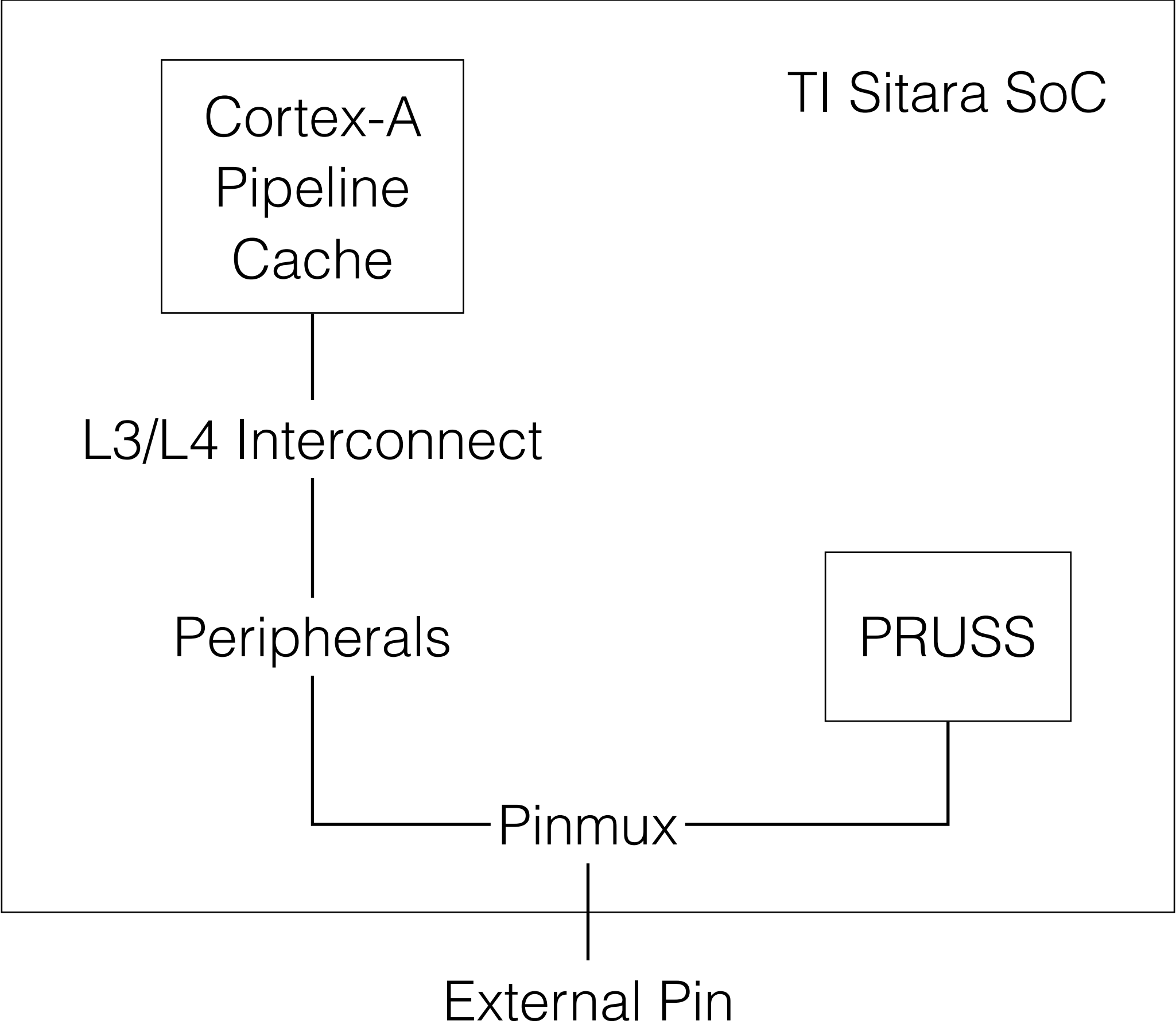
Make PRUs **easy** to use.

# Introduction

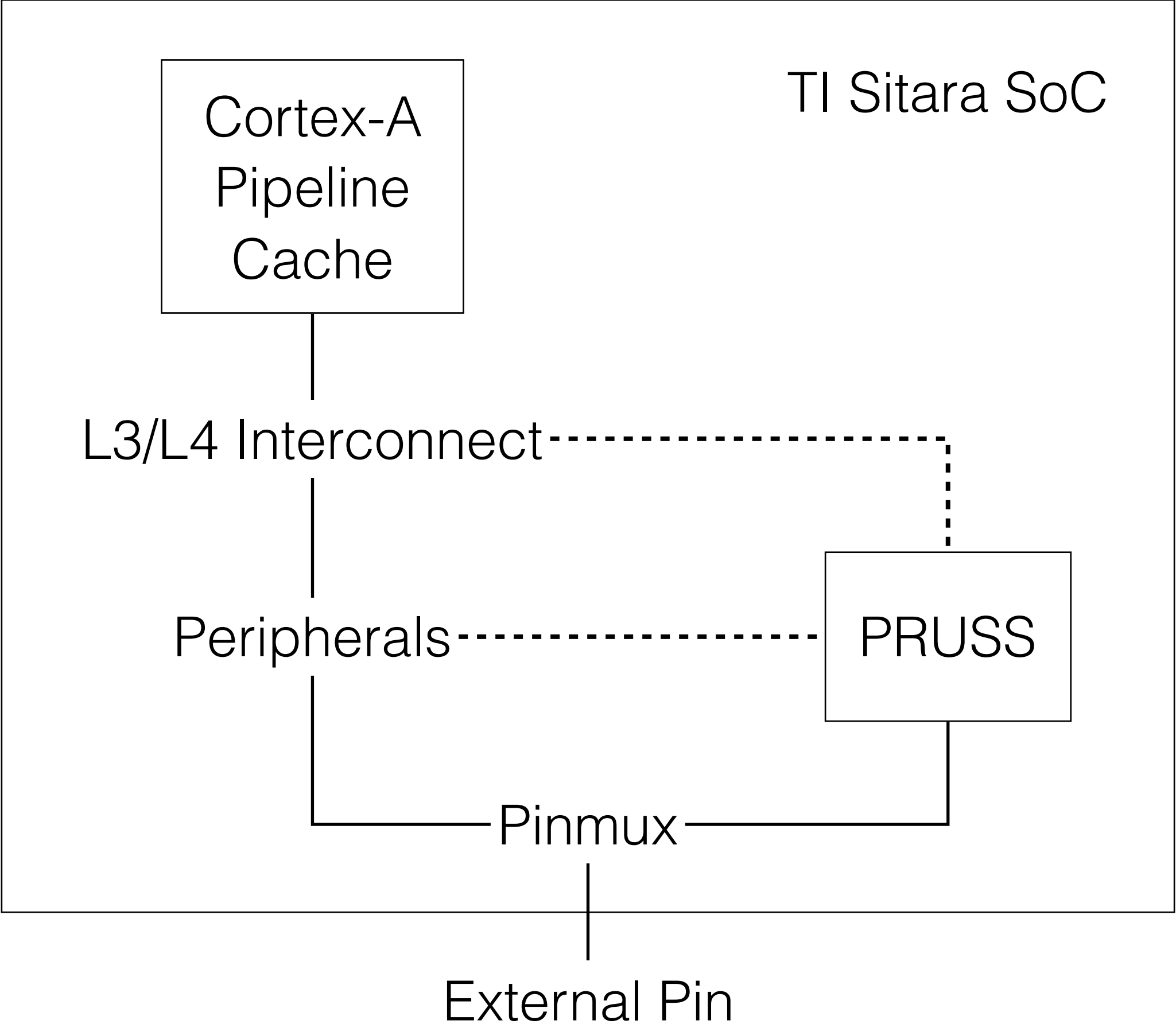
Cortex-A  
Pipeline  
Cache

TI Sitara SoC

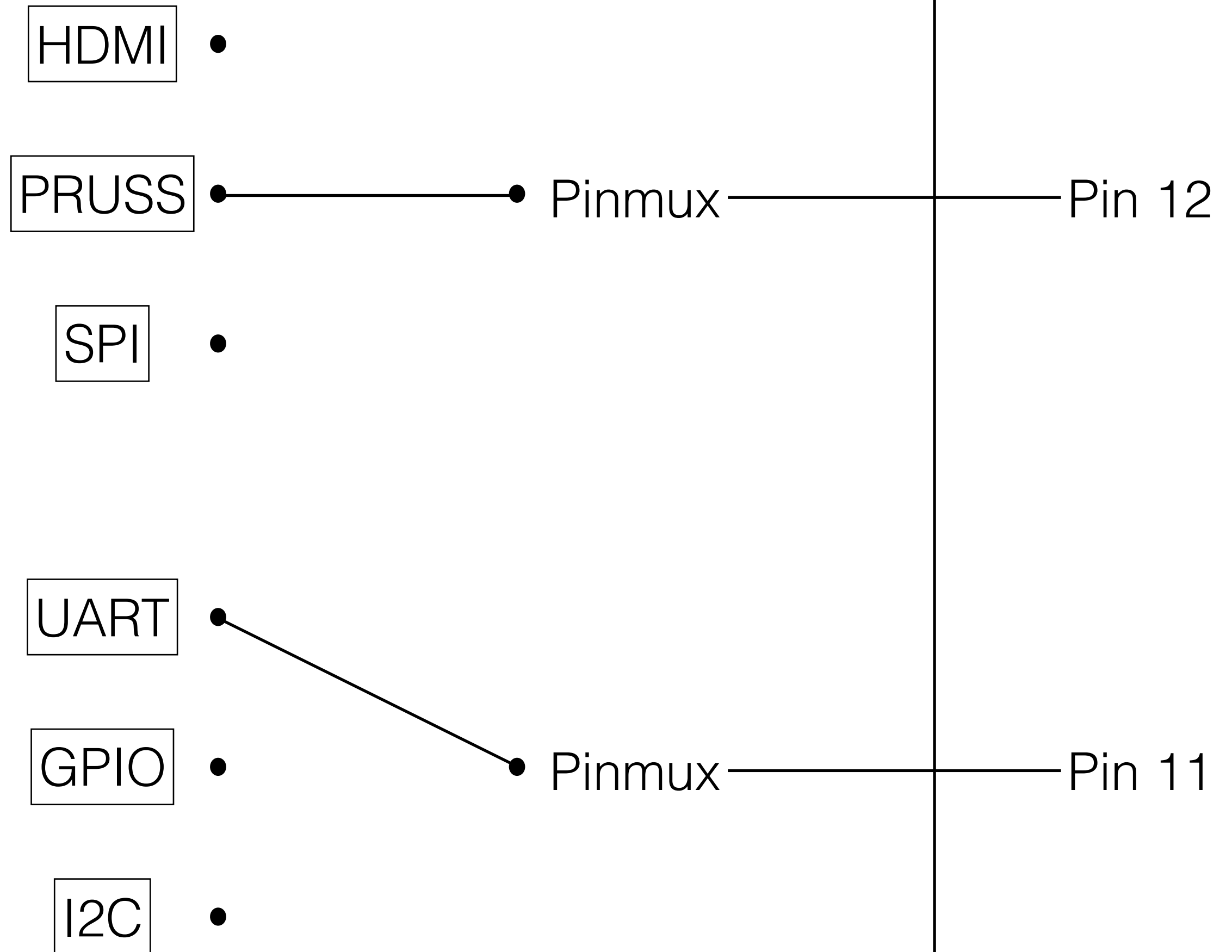




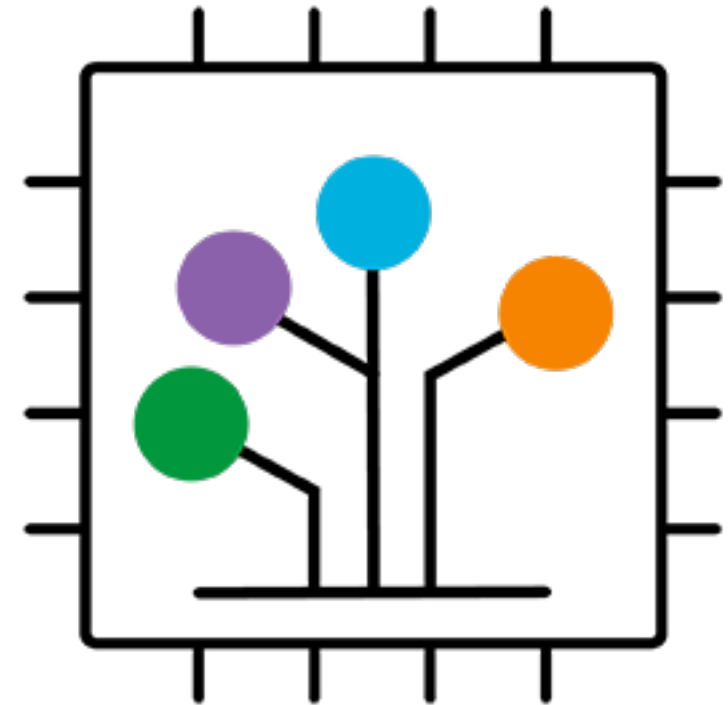




# **Problem 1. Pinmux**



- Device Tree
  - Bone Cape Manager
  - Device Tree Overlays
- Unreliable and not suitable
  - Reboot needed
  - Good for one time load otherwise kernel panics



Pinmux Controller



pin-pirate LKM



Character Device

```
echo 0x34 0x06 > /dev/pinpirate
```



## **Problem 2. Boilerplate**

Pinmux Controller

PRUSS

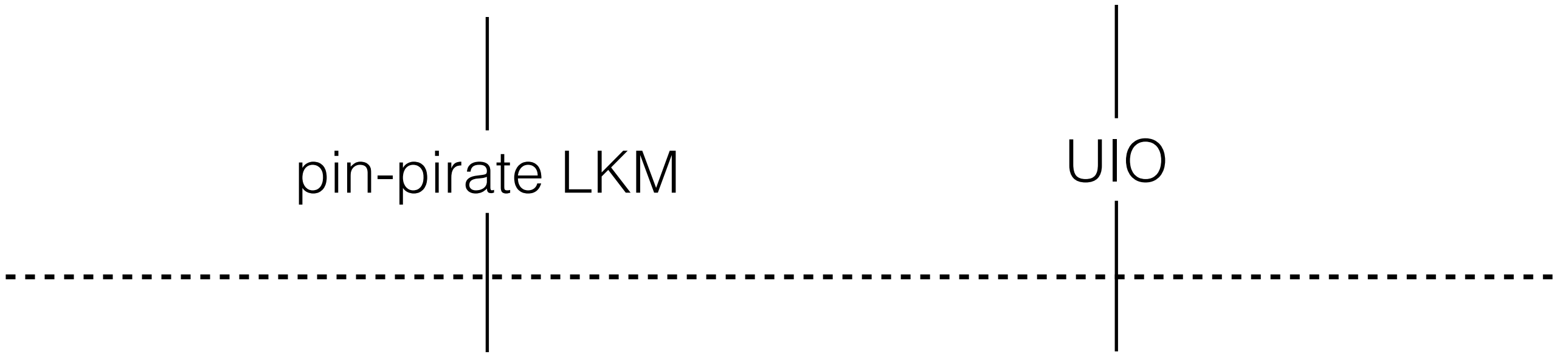
pin-pirate LKM

UIO

Character Device

libpruss + pasm

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Pinmux Controller

PRUSS

pin-pirate LKM

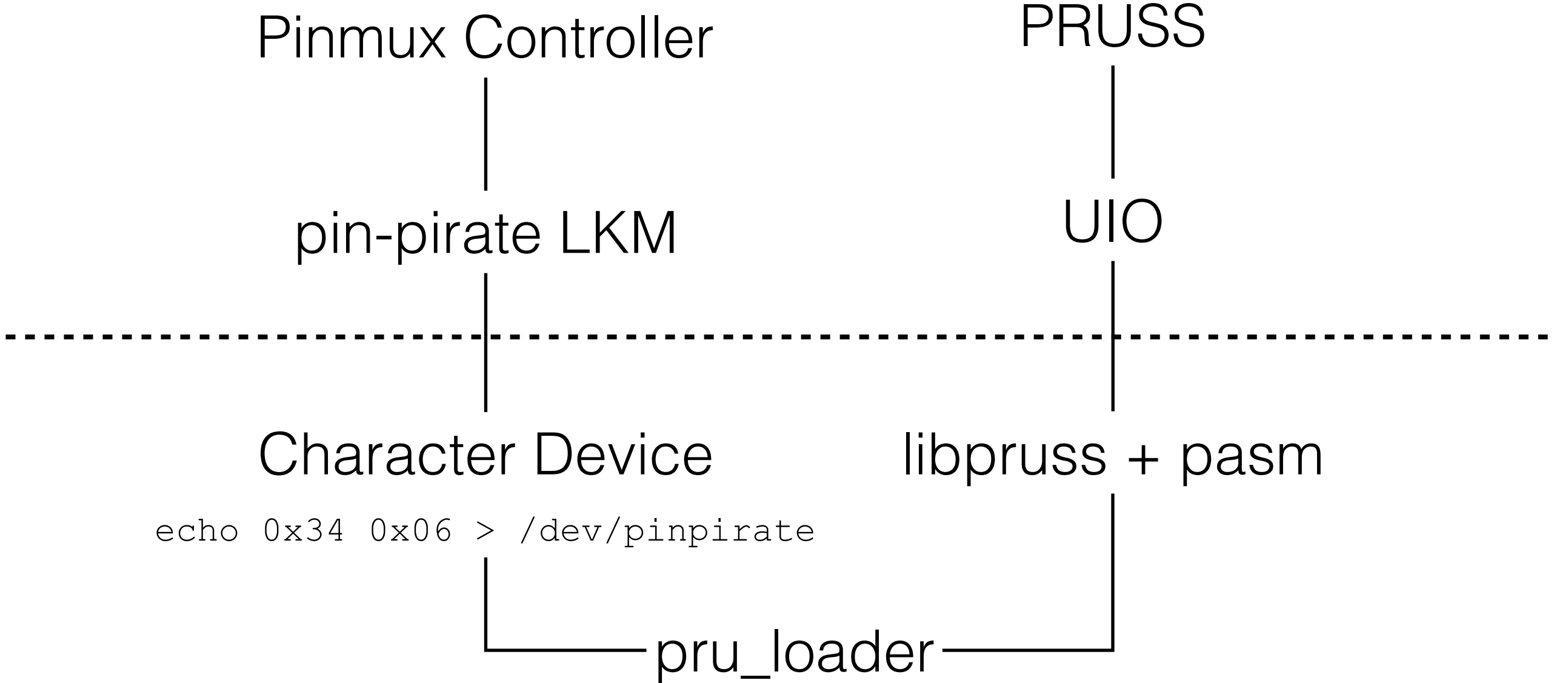
UIO

Character Device

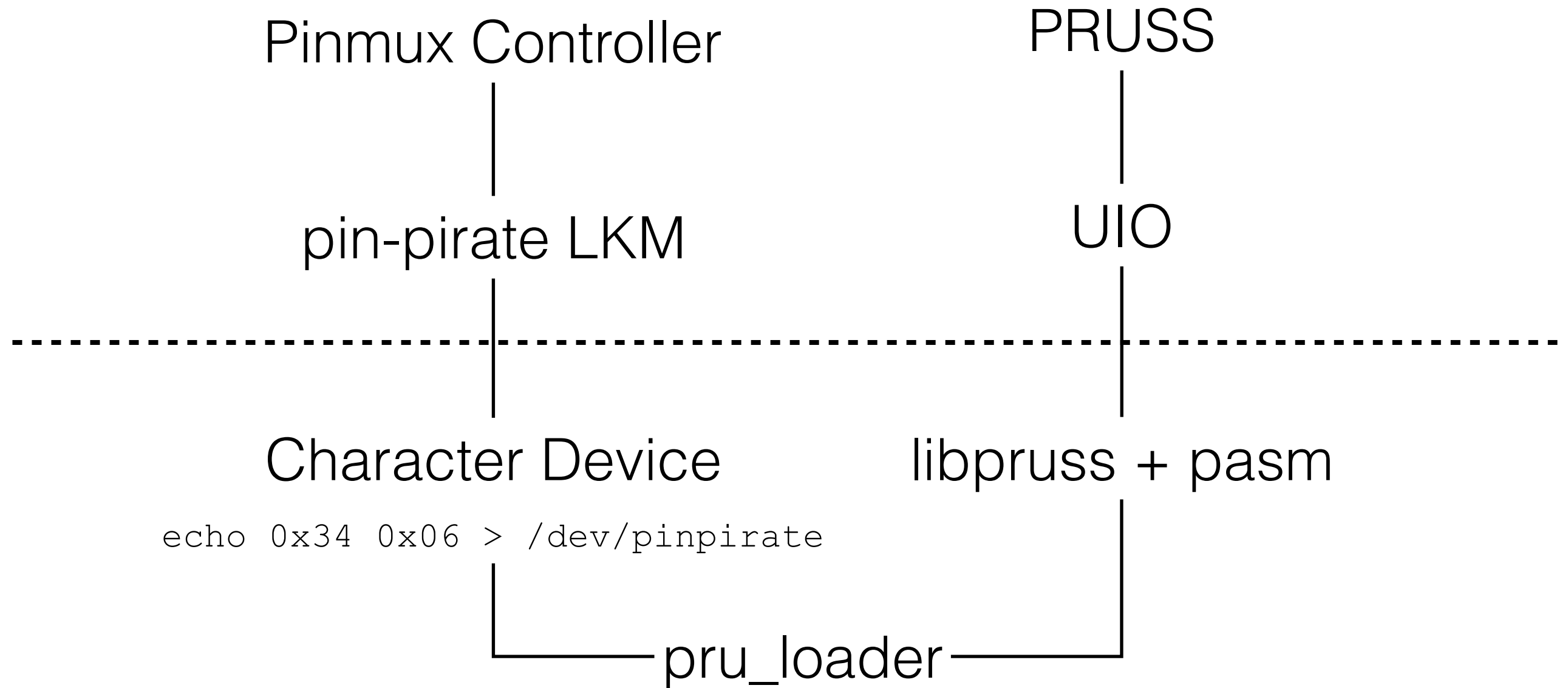
libpruss + pasm

`echo 0x34 0x06 > /dev/pinpirate`

pru\_loader







- pru\_loader + shell scripts
  - Userspace Linux application
  - Handles assembling and loading PRU binaries
  - Facilitates communication between Linux and PRU

## **Problem 3. User Experience**

## **Programming Language**

- PRUs only support assembler and C/C++
- Create a javascript style language, Cyclops, with minimal learning curve for easy prototyping

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- Cyclops => PRU assembly
- Non-optimizing compiler
- Easy to map language constructs directly to assembly code
- Implemented in javascript to run completely in the browser

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## **IDE**

- User facing part that integrates everything in a browser based IDE
- Allows writing in Cyclops or PRU assembler

Pinmux Controller

PRUSS

pin-pirate LKM

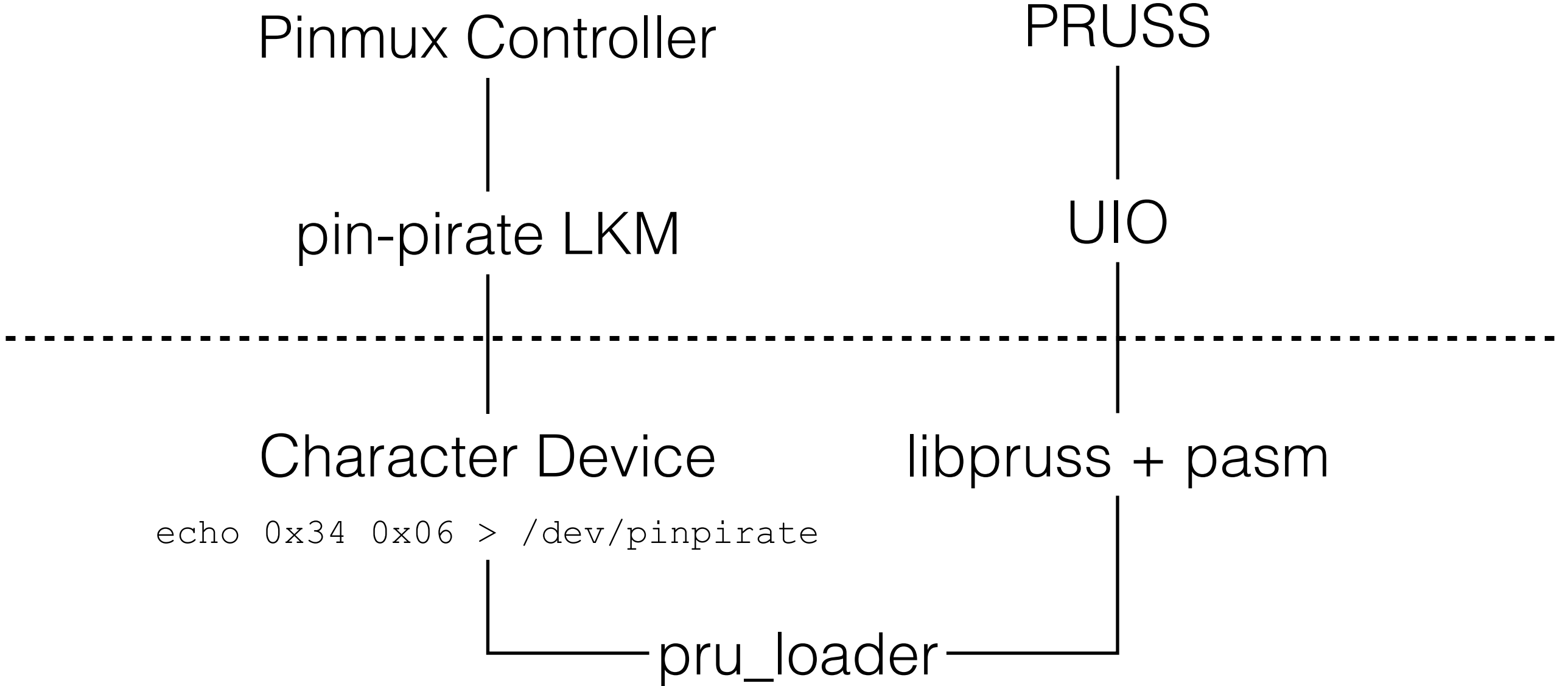
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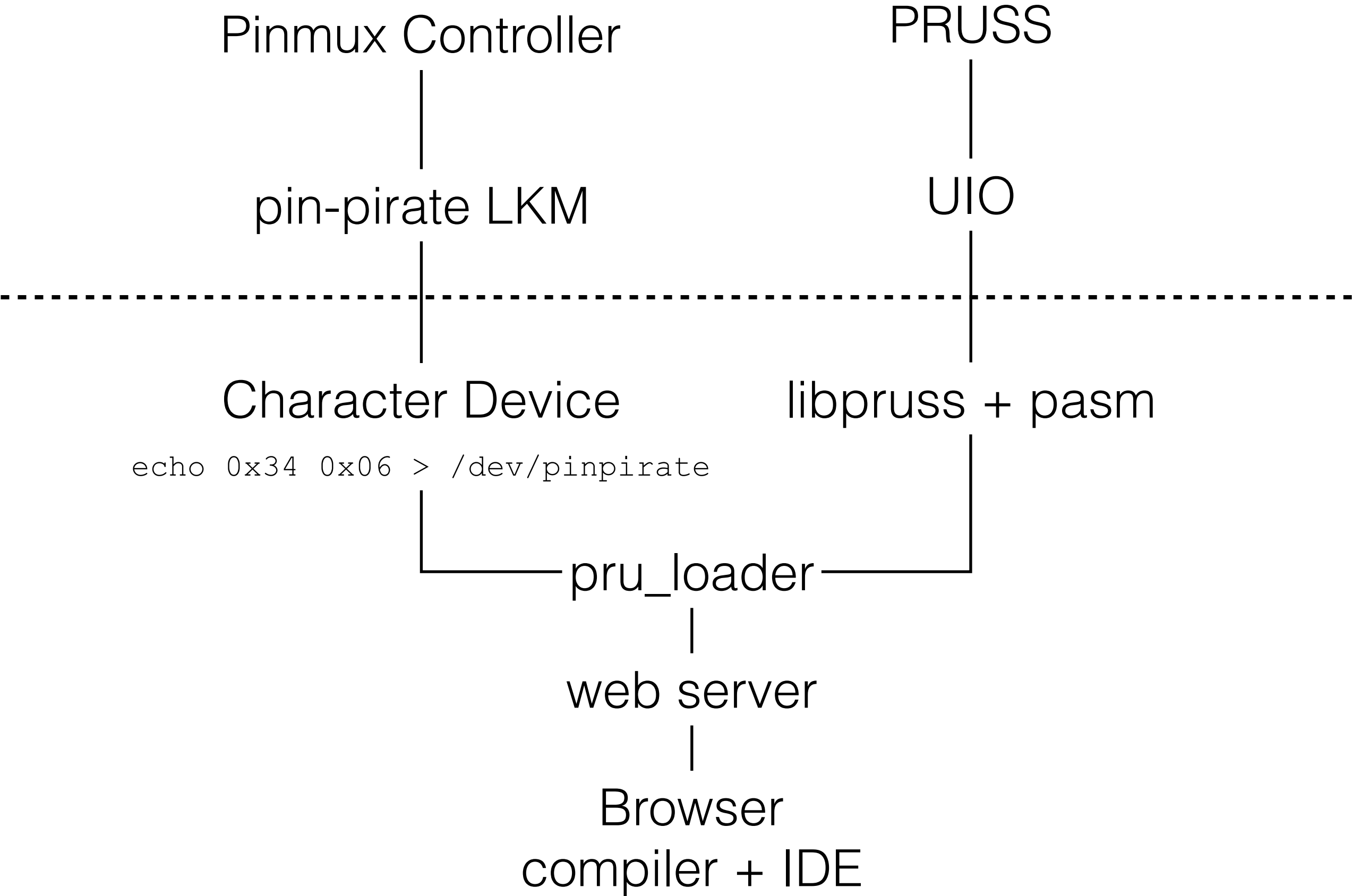
Character Device

libpruss + pasm

`echo 0x34 0x06 > /dev/pinpirate`

pru\_loader





Pinmux Controller

PRUSS

pin-pirate LKM

UIO

Character Device

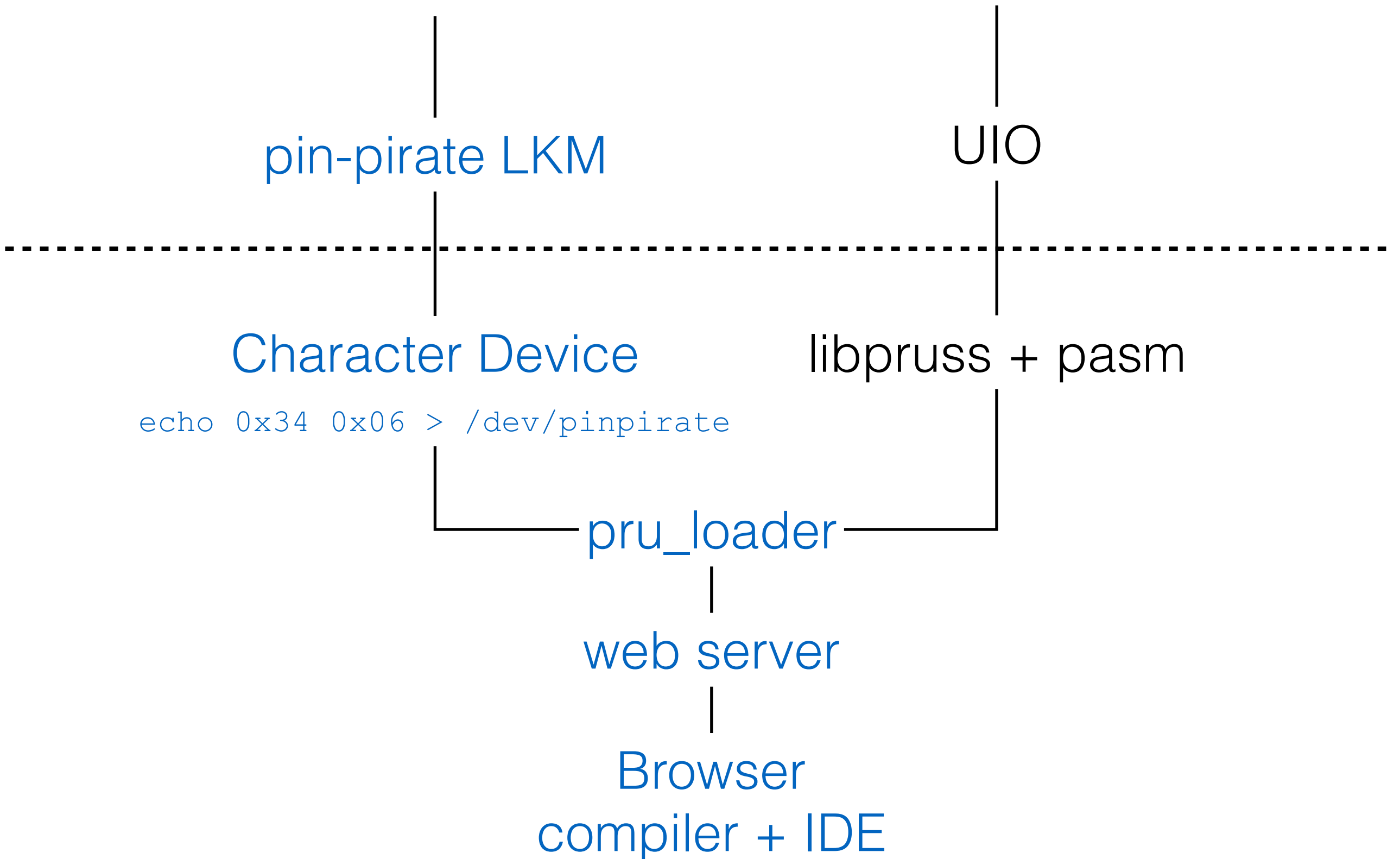
libpruss + pasm

`echo 0x34 0x06 > /dev/pinpirate`

pru\_loader

web server

Browser  
compiler + IDE





Demo

# References

- <https://www.devicetree.org>
- ELC 2015 - Enhancing Real-Time Capabilities with the PRU - Rob Birkett, Texas Instruments. <https://www.youtube.com/watch?v=pICYsbmMbmY>
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- [http://processors.wiki.ti.com/index.php/PRU\\_Assembly\\_Instructions](http://processors.wiki.ti.com/index.php/PRU_Assembly_Instructions)
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