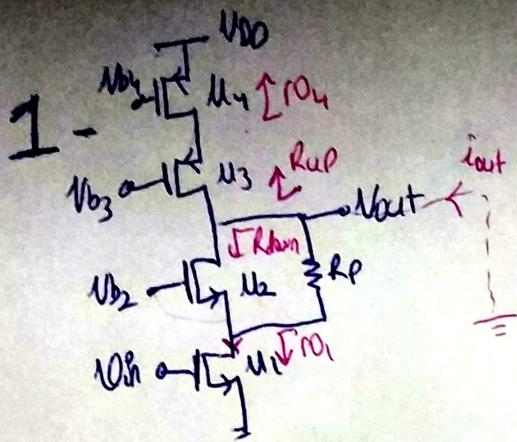


– EHB 335E – ANALOG ELECTRONICS – 11219 –



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Homework



$$6m = \frac{i_{out}}{i_{in}}$$

$$6m = \frac{i_{out}}{i_{in}} \Big|_{i_{out}=0} = gm_1$$

Ruf

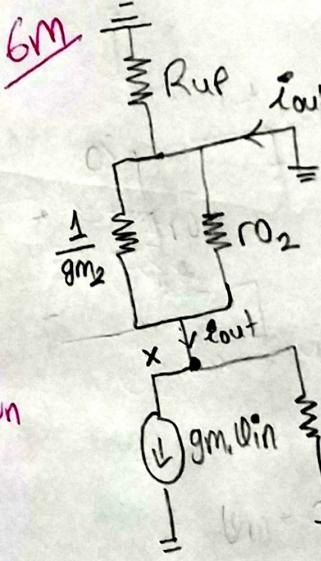
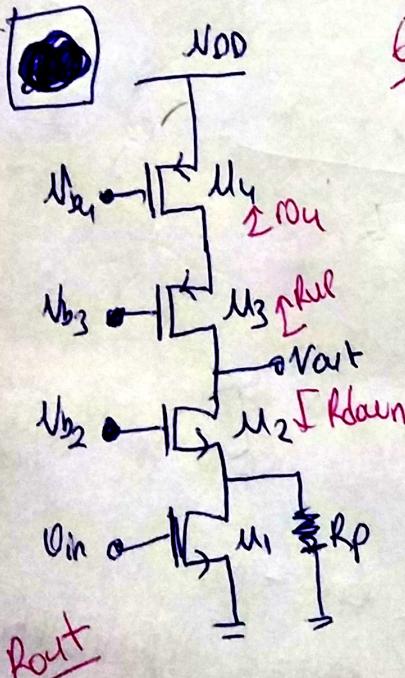
$$R_{up} = r_{O_4} + r_{O_3} + g_{m_3} r_{O_3} r_{O_4}$$

$$R_{down} = r_{O_1} + (r_{O_2} || R_p) + g_{m_2} (r_{O_2} || R_p) r_{O_1}$$

$$R_{out} = R_{up} || R_{down}$$

$$A_v = -6m R_{out} = -gm_1 \left[(r_{O_4} + r_{O_3} + g_{m_3} r_{O_3} r_{O_4}) || (r_{O_1} + (r_{O_2} || R_p) + g_{m_2} (r_{O_2} || R_p) r_{O_1}) \right]$$

$$A_v \approx -gm_1 \left[g_{m_3} r_{O_3} r_{O_4} || g_{m_2} (r_{O_2} || R_p) r_{O_1} \right]$$



$\partial X :$

$$0 - V_x = i_{out} \left(\frac{1}{gm_2} || r_{O_2} \right)$$

$$V_x = (R_p || r_{O_1}) (i_{out} - gm_1 V_{in})$$

$$-i_{out} \left(\frac{1}{gm_2} || r_{O_2} \right) = (R_p || r_{O_1}) (i_{out} - gm_1 V_{in})$$

$$6m = \frac{i_{out}}{V_{in}} \Big|_{i_{out}=0} = \frac{(R_p || r_{O_1}) gm_1}{(R_p || r_{O_1}) + (\frac{1}{gm_2} || r_{O_2})}$$

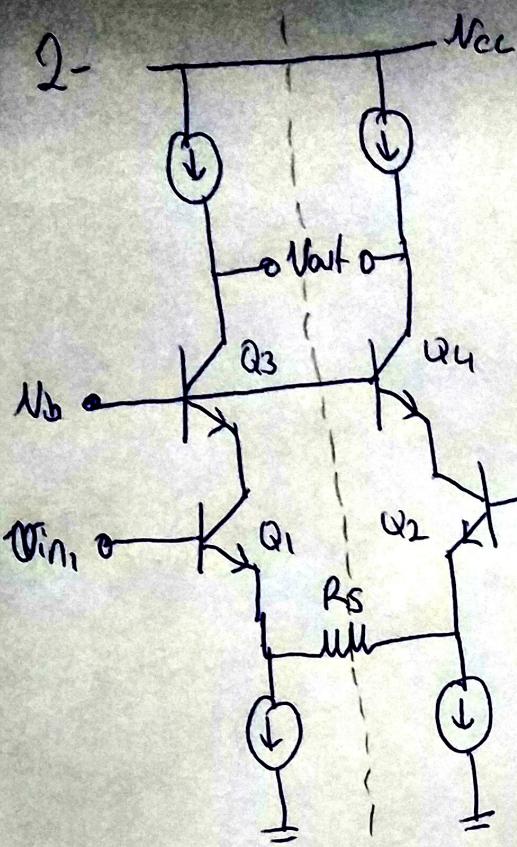
$$R_{up} = r_{O_4} + r_{O_3} + g_{m_3} r_{O_3} r_{O_4}; R_{down} = r_{O_2} + (r_{O_1} || R_p) + g_{m_2} r_{O_2} (r_{O_1} || R_p)$$

$$R_{out} = R_{up} || R_{down} = \left[r_{O_4} + r_{O_3} + g_{m_3} r_{O_3} r_{O_4} \right] || \left[r_{O_2} + (r_{O_1} || R_p) + g_{m_2} r_{O_2} (r_{O_1} || R_p) \right]$$

$$A_v = -6m R_{out} = -\frac{(R_p || r_{O_1}) gm_1}{(R_p || r_{O_1}) + (\frac{1}{gm_2} || r_{O_2})} \left\{ \left[r_{O_3} + (1 + g_{m_3} r_{O_3}) r_{O_4} \right] || \left[r_{O_2} + (1 + g_{m_2} r_{O_2}) (r_{O_1} || R_p) \right] \right\}$$

$$A_v \approx -\frac{gm_1 (R_p || r_{O_1})}{(R_p || r_{O_1}) + \frac{1}{gm_2}} \left[(g_{m_3} r_{O_3} r_{O_4}) || (g_{m_2} r_{O_2} (r_{O_1} || R_p)) \right]$$

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Lesen Sait Frei!



Let's assume $Q_3 = Q_4$ and $Q_1 = Q_2$
 Then if the input voltages are differential
 Then the line of symmetry is the virtual
 ground. (ac ground)

Half-Circuit

$$6m = \frac{i_{out}}{6in} \Big|_{Vout=0} = \frac{gm_1}{1 + gm_1 \frac{Rs}{2}}$$

$$R_{down} = r_{O1} + (1 + gm_1 r_{O1}) \left(\frac{Rs}{2} \parallel r_{T1} \right)$$

$$R_{out} = r_{O3} + (1 + gm_3 r_{O3}) (R_{down} \parallel r_{T3})$$

Line of Symmetry

$$A_{D11} = -bm R_{out}$$

$$A_{D11} = - \frac{gm_1}{1 + gm_1 \frac{Rs}{2}} \left[r_{O3} + (1 + gm_3 r_{O3}) \left(\underbrace{\left[r_{O1} + (1 + gm_1 r_{O1}) \left(\frac{Rs}{2} \parallel r_{T1} \right) \right]}_{R_{down}} \parallel r_{T3} \right) \right]$$

$$A_{D11} \approx - \frac{gm_1}{1 + gm_1 \frac{Rs}{2}} \left[gm_3 r_{O3} \left(gm_1 r_{O1} \left(\frac{Rs}{2} \parallel r_{T1} \right) \parallel r_{T3} \right) \right]$$

3-

V_{in}

V_{out}

i_{in}

i_x

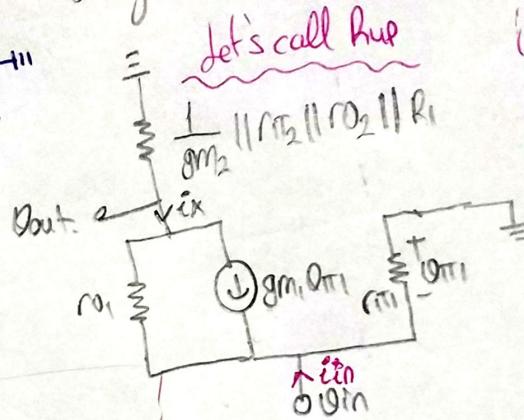
i_{out}

R_1

R_2

C_B

Assume we are operating in the midband frequency and C_B is very large. Then in the small-signal analysis C_B is short circuit.



Gain Calculation

1st way

$$V_{out} = 0 \Rightarrow V_{R_{out}} = 0$$

$$i_{out} \approx g_{m1} V_{R_{out}} ; V_{R_{out}} = -V_{in}$$

$$g_m = \frac{i_{out}}{V_{in}} \Big|_{V_{out}=0} = -g_{m1}$$

$$A_{v0} = R_{out} \parallel R_1 \quad (V_{in} = 0)$$

$$A_{v0} = -g_m R_{out}$$

$$A_{v0} = g_{m1} (R_{out} \parallel R_1)$$

$$A_{v0} = g_{m1} \left(\frac{1}{g_{m2}} \parallel r_{\pi 2} \parallel r_{\pi 1} \parallel R_1 \parallel R_{out} \right)$$

2nd way

$$V_{out} = -i_x R_{out} \Rightarrow i_x = -\frac{V_{out}}{R_{out}}$$

$$V_{out} - V_{in} = r_{\pi 1} (i_x - g_{m1} V_{R_{out}}) ; V_{R_{out}} = -V_{in}$$

$$V_{out} - V_{in} = r_{\pi 1} \left(-\frac{V_{out}}{R_{out}} + g_{m1} V_{in} \right)$$

$$(V_{out} \left(1 + \frac{r_{\pi 1}}{R_{out}} \right)) = \left(1 + g_{m1} r_{\pi 1} \right) V_{in} \Rightarrow \frac{V_{out}}{V_{in}} = A_{v0} = \frac{1 + g_{m1} r_{\pi 1}}{1 + \frac{r_{\pi 1}}{R_{out}}} = \frac{1 + g_{m1} r_{\pi 1}}{1 + r_{\pi 1} + \frac{1}{R_{out}}}$$

$$\Rightarrow A_{v0} \approx \frac{g_{m1}}{\frac{1}{r_{\pi 1}} + \frac{1}{R_{out}}} \Rightarrow A_{v0} = g_{m1} (R_{out} \parallel r_{\pi 1}) = g_{m1} \left(\frac{1}{g_{m2}} \parallel r_{\pi 2} \parallel r_{\pi 1} \parallel R_1 \parallel R_{out} \right)$$

R_{in} Calculation

$$V_{out} = g_{m1} (R_{out} \parallel r_{\pi 1}) V_{in} ; V_{R_{out}} = -V_{in}$$

$$\Rightarrow i_{in} = -\left(i_x + \frac{V_{R_{out}}}{r_{\pi 1}} \right) = \frac{V_{out}}{R_{out}} + \frac{V_{in}}{r_{\pi 1}} = \frac{1}{R_{out}} \left(g_{m1} (R_{out} \parallel r_{\pi 1}) V_{in} \right) + \frac{V_{in}}{r_{\pi 1}}$$

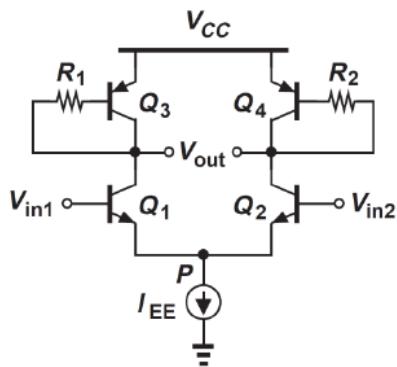
$$\Rightarrow i_{in} = V_{in} \left(\frac{g_{m1} (R_{out} \parallel r_{\pi 1})}{R_{out}} + \frac{1}{r_{\pi 1}} \right)$$

$$\Rightarrow R_{in} = \frac{V_{in}}{i_{in}} = \frac{1}{\frac{g_{m1} (R_{out} \parallel r_{\pi 1})}{R_{out}} + \frac{1}{r_{\pi 1}}} , \text{ where } R_{out} = \frac{1}{g_{m2}} \parallel r_{\pi 2} \parallel r_{\pi 1} \parallel R_1$$

$$R_{in} \text{ if } N_A \rightarrow \infty , \quad R_{in} = \frac{1}{g_{m1} + \frac{1}{r_{\pi 1}}} = \left(r_{\pi 1} \parallel \frac{1}{g_{m1}} \right) \approx \frac{1}{g_{m1}}$$

4TH QUESTION IS SOLVED BY USING LTSPICE

First of all, we examine the circuit structure. Since it is a differential pair, we can determine its gain with the half-circuit methodology.



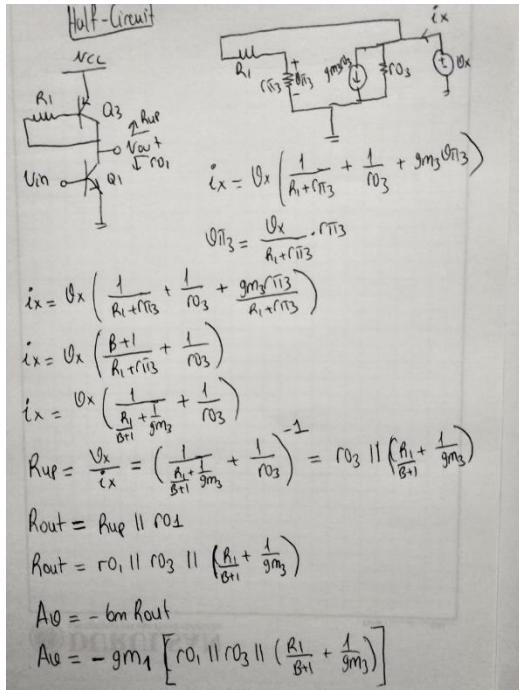
When we determine the half-circuit, we can find the gain by using the short-circuit transconductance. $A_v = -GmR_{out}$

$$Gm \approx gm_1$$

$$R_{out} = [(r_{o1} // r_{o3} // (\frac{1}{gm_3} + \frac{R_1}{\beta + 1}))]$$

$$A_v = -GmR_{out}$$

$$A_v = -gm_1 * [(r_{o1} // r_{o3} // (\frac{1}{gm_3} + \frac{R_1}{\beta + 1}))]$$



From these gain expressions and using some electronics knowledge we can optimize the circuit for the highest gain. In the differential pair, the collector current (I_c) will be $I_{ee}/2$ regardless of the the input voltages. And also, since the I_{ee} and therefore the collector currents do not change, our small signal parameters gm , r_o , and r_{pi} do not change, too.

Therefore, the only method to increase the gain is to increase the R_1 and R_2 at the same time.

But, can we increase R_1 and R_2 (let's call base resistances for simplicity) as much as we want?

Unfortunately, no!

Increasing base resistances reduces the collector voltage and if collector voltage decreases enough, the npn transistors can saturate. Pnp transistors cannot be in saturation mode because they are diode-connected.

The method I applied is:

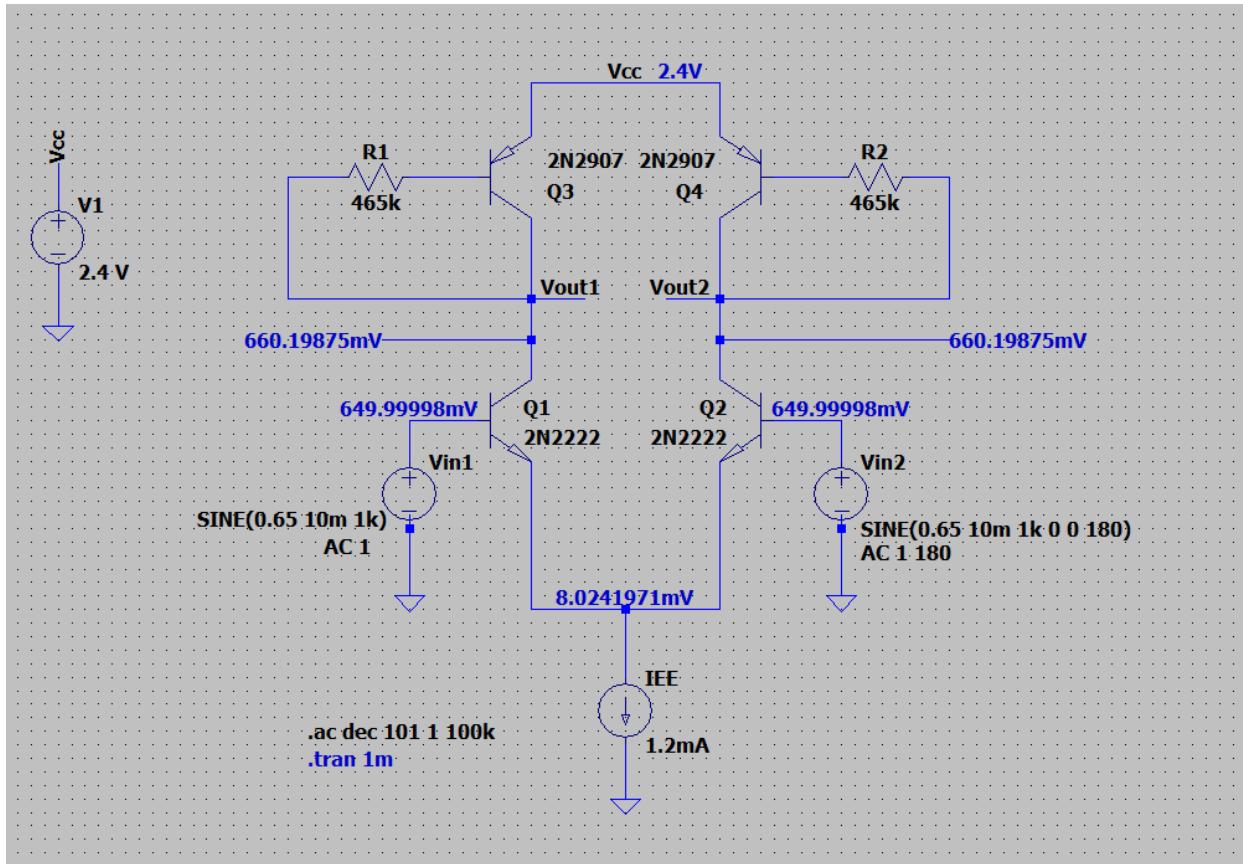
To increase the gain, I should increase the base resistances.

As I increase the base resistances, the collector voltage decreases.

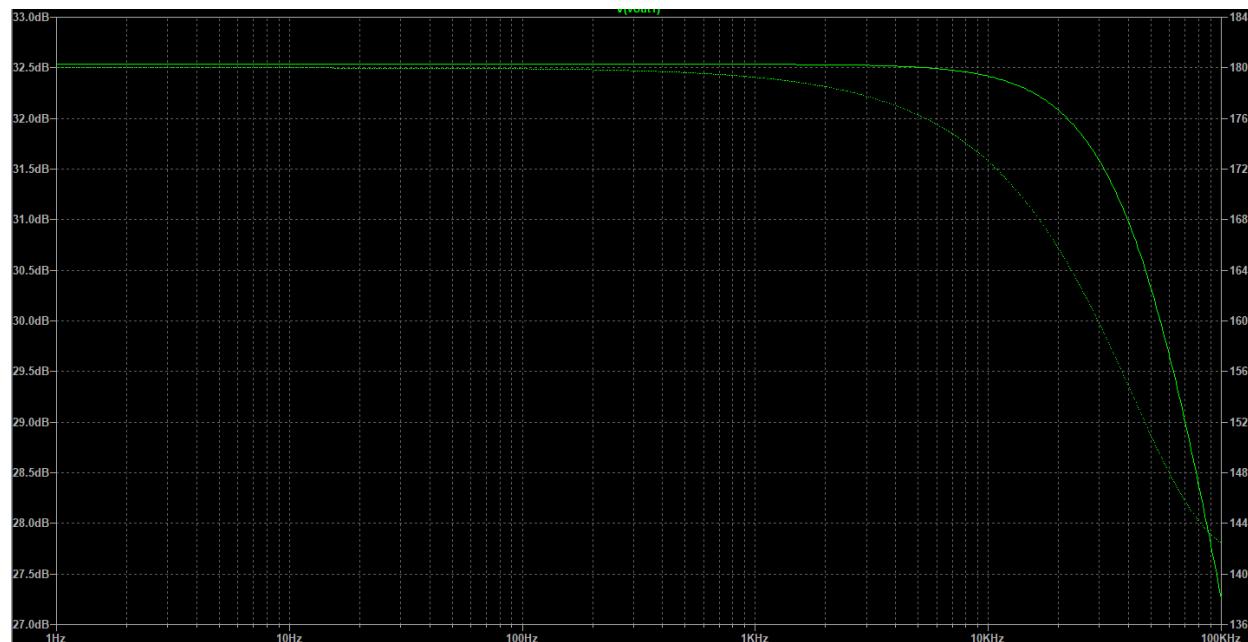
Therefore, to stay in active region input voltages should be very low.

But the voltage on the tail current should be greater than 0.

The circuit which I prepared is:

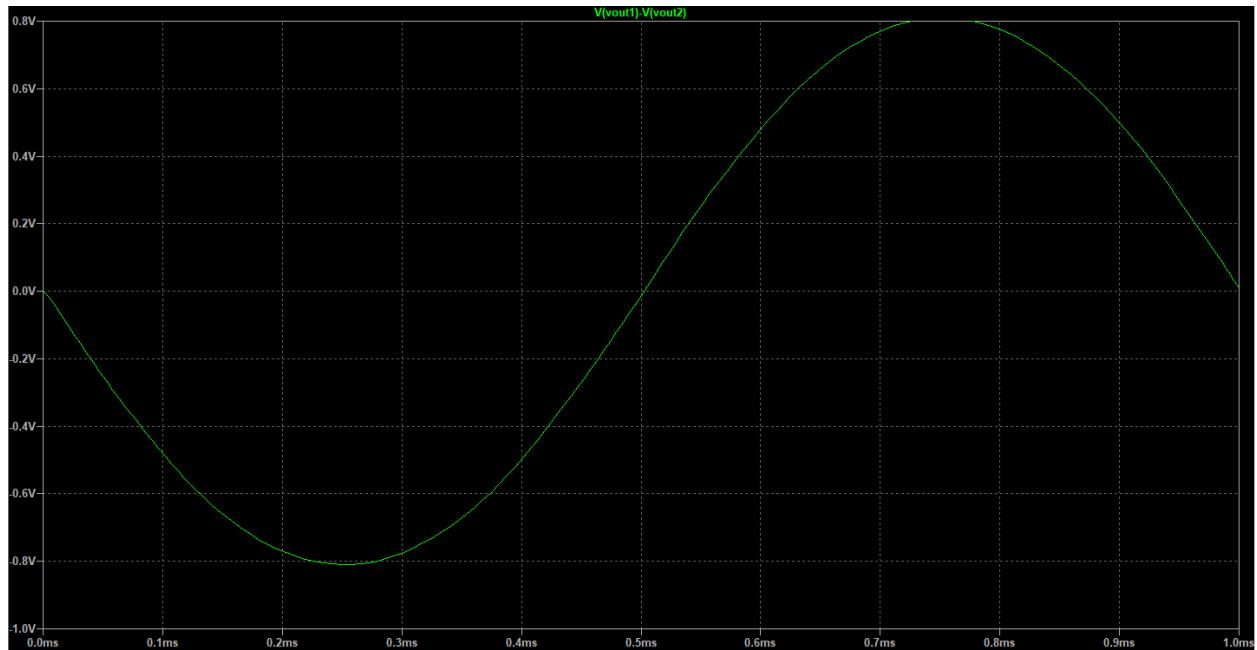


The AC gain I get is 32.53 dB

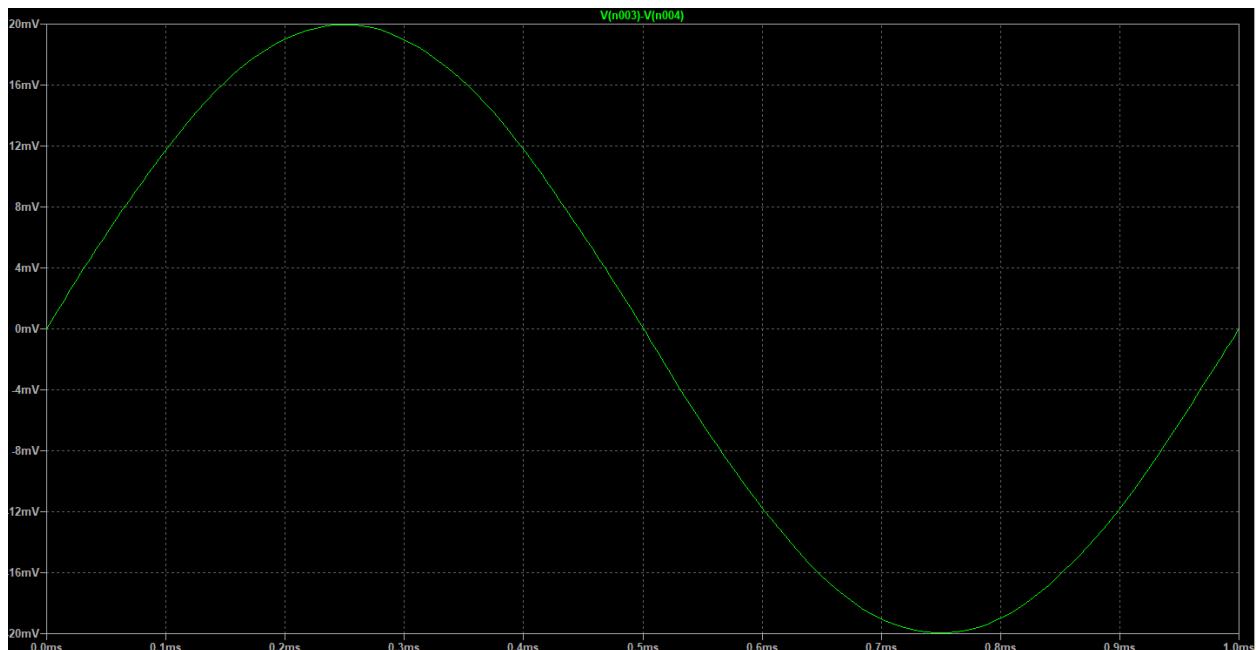


THE TRANSIENT RESPONSE OF MY CIRCUIT IS SHOWN AS:

Vout1 – Vout2: Peak voltage 809mV



Vin1 – Vin2: Peak voltage 20mV



$$\text{GAIN} : \frac{809\text{mV}}{20\text{mV}} = 40.45 \quad ; \quad 20\log(40.45) = 32.14 \text{ dB}$$

