

This document is under formal review by the DCC Working Group under topic 0504022. It has not been approved by the WG, nor reviewed & approved by the Technical Department. Send comments to reinhard@dcc-mueller.de.

This Technical Note describes background information for the

- (110) Future Expansion instructions and
- (001) Advanced Operations instructions

5 documented in Recommended Practice RP-9.2.1.

The length of the currently defined sub-instructions follows a rule depending on the CCCCC instruction code in bits 4 to 0 within the first instruction byte.

- A code of 11xxx (x= don't care) denotes a 2 byte instruction.
- A code of 00xxx (x= don't care) denotes a 3 byte instruction.

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Instructions defined in the future should follow the same rule. This allows easy instruction length recognition by the decoders.

15 Products using reserved codes for sub-instructions will be not compliant.

