



General Description

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 5P49V5901A046NLGI. Please refer to the device datasheet for further information about the device

General Configuration

Parameter	Value	Units
Device I ² C Address	Primary 0xD4	
XTAL Load Capacitor	4.5	pF
SD/OE Pin Function	Output Enable	
SD/OE Polarity	Negative	

Frequency Overview

Parameter	Configuration 0	Configuration 1	Configuration 2	Configuration 3	Units
Input	25				MHz
Output 0	25				MHz
Output 1	90		-		MHz
Output 2	50		-		MHz
Output 3	100		-		MHz
Output 4	156.25		-		MHz

Configuration 0 Parameters: SEL[1:0] = 00

Parameter	Output 0	Output 1	Output 2	Output 3	Output 4	Units
Input Frequency	25	25	25	25	25	MHz
Default Output Status	On	On	On	On	On	
VDDO Voltage		3.3	3.3	3.3	3.3	V
Output Type	LVC MOS	LVC MOSX2, 2 outputs, in phase	LVC MOSX2, 2 outputs, in phase	HCSL	LVPECL	
Frequency	25	90	50	100	156.25	MHz
Spread Spectrum		Off	Off	Off	Off	
Spread Spectrum Modulation						%
Slew Rate	1.0x nominal	1.0x nominal	1.0x nominal	1.0x nominal	1.0x nominal	
Phase Shift		0	0	0	0	Degrees

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