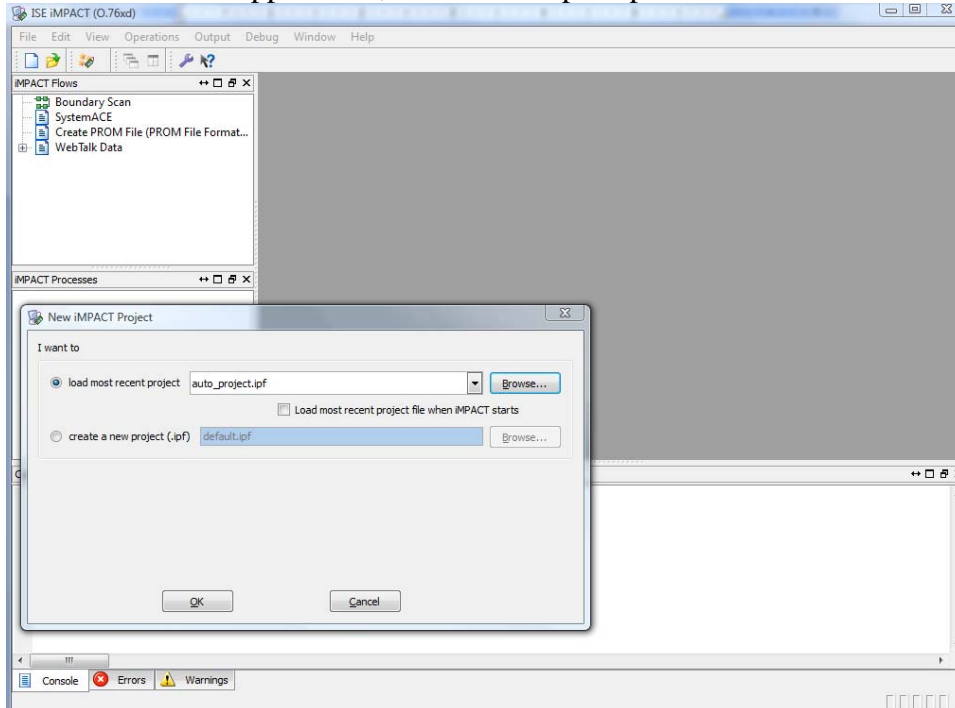# Programming D2D Summary

Requirements:
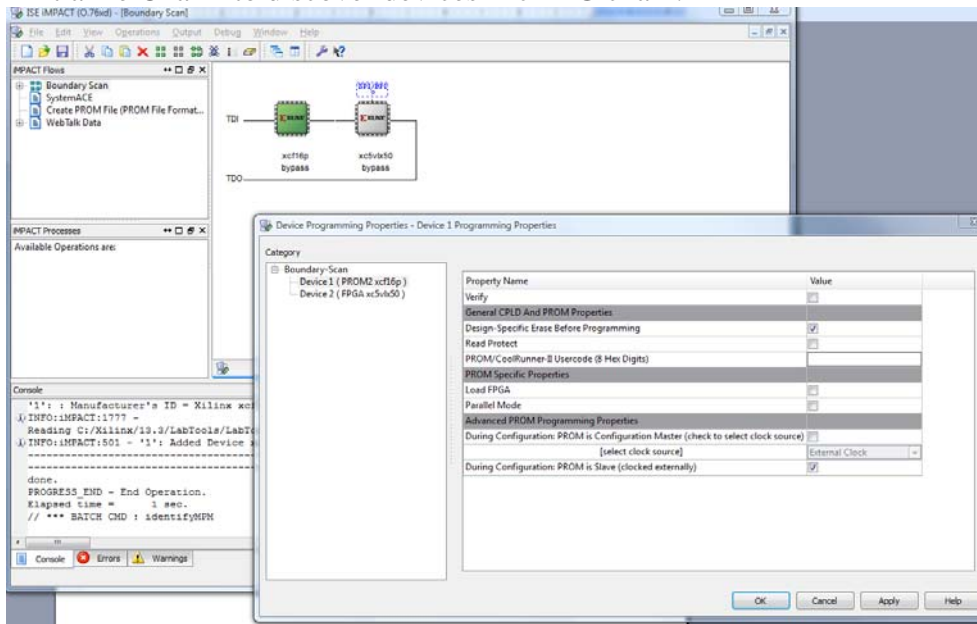iMPACT application (part of Xilinx ISE Design Suite)
Xilinx Platform Cable USB

Using the Xilinx Platform Cable USB device connected at H1:
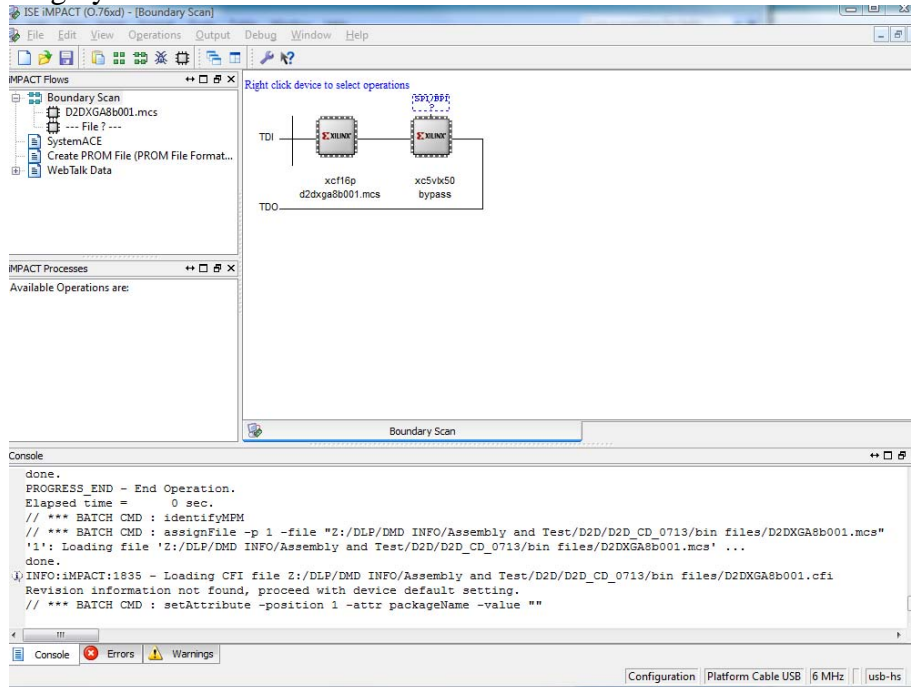Launch iMPACT application, cancel when prompted to load New iMPACT Project:

Select Boundary Scan, then right click in open space where prompted and select
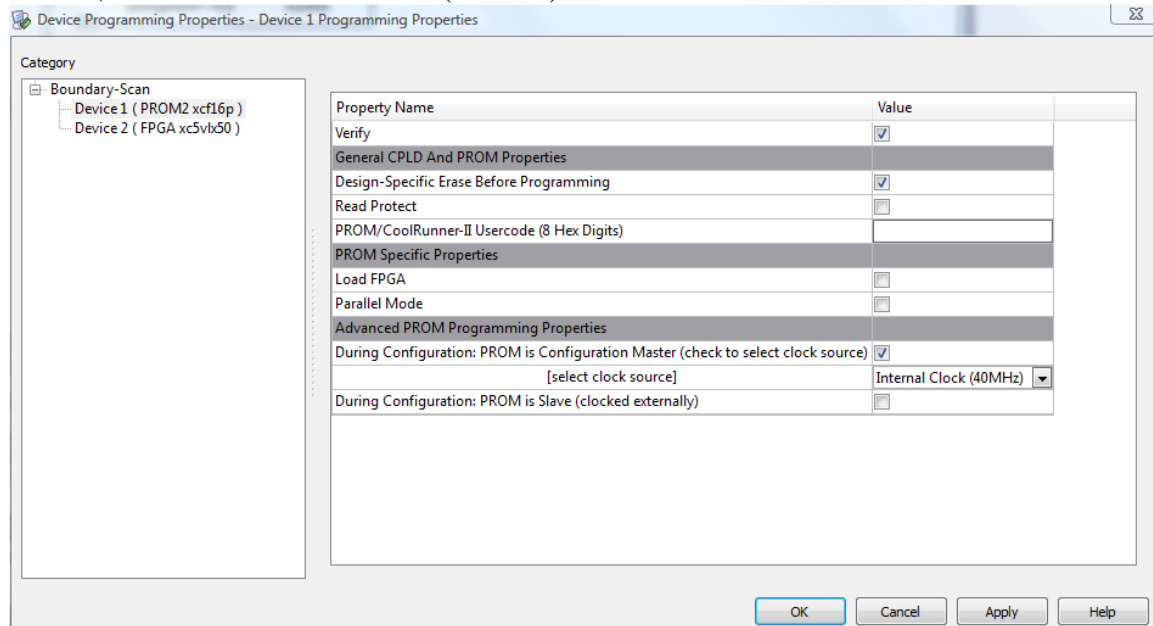'Initialize Chain' to discover devices in JTAG chain:

Select Cancel in Device Programming Properties Window.

Programming D2D Summary

Right click on the first device and select 'Assign New Configuration File' and navigate to the bin folder from the D2D CD. Select the appropriate .mcs file (for example, XGA 8 bit grayscale would be D2DXGA8b001.mcs.

Right click device again, and select Set Programming Properties. Check Verify, Design-Specific Erase before Programming, During Configuration PROM is Configuration Master, and select Internal Clock (40MHz) as clock source.

Click ok and then right click device again and select Program. Takes about 2 minutes, then cycle power and you're all set.