# PRFDN: Reparameterize and Prune Disentangled U-Net for Efficient Image Super-resolution

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## **Abstract**

Single image super-resolution (SISR) is a computer vision technique that uses deep convolution neural networks to reconstruct high-resolution images from low-resolution images. It has been used in many applications, but due to power constraints on mobile devices, efficient image super-resolution (EISR) is becoming increasingly important and researchers are designing lightweight networks through methods like network pruning, parallelism, and knowledge distillation. Efficient network design is key to achieving better performance in EISR, and many designs adopt the U-Net architecture. By deconstructing its branching structure, the main structure of the U-Net can be transformed into mutually independent branches, increasing parallelism and reducing the number of layers executed serially in the network to improve inference speed within the GPU's capacity. This paper proposed Parallel RFDN (PRFDN) based on the pre-trained RFDN, which is a typical EISR model with a backbone similar to U-Net. Our method disentangles the sequentially computed trunks in RFDN into branches and performs re-parametrization to make these branches inference in parallel on single devices. After that, we further perform pruning on the model and finetune it to achieve higher performance.

## 1. Introduction

Single image super-resolution (SISR) is a computer vision technique to reconstruct high-resolution images from low-resolution images. Deep convolution neural networks have shown impressive performance in this area and many deep learning-based methods have been proposed to address this ill-posed problem. This technique has been used in many applications (e.g. video gaming [4] and video streaming [6, 12, 18]) to achieve displays with high resolution and high refresh rates and some of the related applications have been deployed on lots of mobile devices [13, 17]. However, mobile phones have a power-constrained design with a ded-

icated power supply, which makes efficient image superresolution (EISR) a growing interest.

Efficient network design is key to achieving better performance in EISR. To make the network more efficient for resource-limited devices, researchers are designing generic lightweight networks with reduced parameters through methods like network pruning [2,8,11], parallelism [3,7,11,15] and knowledge distillation [5,10,16,19]. However, when the model size becomes sufficiently small, these methods fail to further increase the frame rate of superresolution. This is because reducing the computational scale of each layer of the model beyond the point where the GPU is fully loaded does not improve the inference speed of the model, if the length of serially executed layers cannot be reduced. Therefore, lots of EISR designs [1, 9, 13] involve careful consideration of network architecture, design, and utilization of limited features to generate more representative features for super-resolution, which can use fewer layers to achieve a good super-resolution quality.

Moreover, recent years have witnessed the wide use of U-Net [14] in the design of deep neural networks. Many designs of the EISR model also adopted the U-Net architecture, and the RFDN [10] is a representative example, which won the first prize in the AIM 2020 competition. Through the observation of the U-Net structure, we found its main structure can be transformed into mutually independent branches by deconstructing its branching structure, thereby increasing parallelism. Furthermore, multiple branches can be executed in parallel on the same GPU by reparameterization. Within the GPU's capacity, this method can reduce the number of layers that are executed serially in the network, therefore improving the inference speed.

In this paper, we proposed Parallel RFDN (PRFDN) based on the pre-trained RFDN. Our method disentangles the sequentially computed trunks in RFDN into branches and performs re-parametrization to make these branches inference in parallel on single devices. After that, we further perform pruning on the model and finetune it to achieve higher performance.

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## 1.2. Methods

We proposed Parallel RFDN (PRFDN) as is shown in figure 1. Our method consists of four stages to transform a pre-trained RFDN [10] into PRFDN.

# 1.3. Branching

To accelerate the inference, we first consider reducing the data dependency in the model to achieve higher parallelism. According to our observation, the main structure of U-Net can be transformed into mutually independent branches to increase parallelism. Our method disentangles the sequentially computed trunks of RFDN into branches. As is shown in figure 1b, after the branching, the major part of the model will consist of four independent branches that can calculate in parallel. To improve the accuracy, we also design a small SR block (SRFDB) based on the RFDB of RFDN and add them before the input of each branch. This type of block is utilized to fit the output of cascaded RFDBs, such that the input to the disentangled RFDB block closely resembles the input received in the original RFDN. This ensures model accuracy at a reduced cost.

# 1.4. Training

Since we only change the data flow but not the structure of RFDB, the pre-trained RFDN parameters can still be loaded into the major part of our branch model (only except for those SRFDBs). To benefit from the pre-training, we load the pre-trained RFDN parameters into our branch model before training our branch model. In this step, we first trained only SRFDBs for 100 epochs (on LSDIR dataset and DIV2K dataset) with freeze parameters in RFDBs, and then trained the whole model for 100 epochs.

#### 1.5. Re-parametrization

Without much data dependency, branches in our model can be computed in parallel. However, a single GPU cannot compute two or more different models in parallel. To address this issue and achieve parallel computing of multiple branches on a single GPU, we should merge the four branches into a single branch. As is shown in figure 1b, three of these four branches have exactly the same structure with only different parameters, and the only difference between the remaining branch and the other branches is the absence of an RFDBS. Therefore, we should 1) merge those four convolution layers at the beginning of the branches into

one, 2) merge those three RFDBS into one, and 3) merge those four RFDBs into one.

In our method, we create a bigger RFDB and a bigger SRFDB with bigger convolution layers and move the weight and bias from the RFDBs and SRFDBs into them. More specifically, we merge the convolution layers according to the following derivation.

Assuming Y = Conv(X,K) represents a convolution layer, where  $K \in \mathbb{R}^{c_o \times c \times h_k \times w_k}$  is the convolution kernel;  $X \in \mathbb{R}^{c \times h \times w}$  is the input of convolution layer and  $Y \in \mathbb{R}^{c_o \times h' \times w'}$  is the output; c,h,w are the channel, height, and width of the input respectively;  $c_o,h',w'$  are the channel, height, and width of the output respectively;  $h_k$  and  $w_k$  are the height and width of the kernel respectively. If we want to merge n convolution layers  $Y_i = Conv(X_i,K_i)$  into a big convolution layer  $Conv_b$ , this  $Conv_b$  can be represented as:

$$Y = Conv_b([X_1, \dots, X_n], K_1, \dots, K_n)$$

$$= [Y_1, \dots, Y_n]$$

$$= \sum_{i=1}^n [\mathbf{0}, \dots, Y_i, \dots, \mathbf{0}]$$

$$= \sum_{i=1}^n [\mathbf{0}, \dots, Conv(X_i, K_i), \dots, \mathbf{0}]$$

$$= Conv([X_1, \dots, X_n], \begin{bmatrix} K_1 & \mathbf{0} & \dots & \mathbf{0} \\ \mathbf{0} & K_2 & \dots & \mathbf{0} \\ \vdots & \vdots & \ddots & \vdots \\ \mathbf{0} & \mathbf{0} & \dots & K_n \end{bmatrix})$$

$$= Conv(X_{concat}, K_{diag})$$

$$(1)$$

Therefore, when computing the merged convolution layer  $Conv_b$ , all the input  $X_i, i \in [1, n]$  would be concatenated into  $X_{concat}$  and all the kernel  $K_i, i \in [1, n]$  would be re-parameterized into  $K_{diag}$  (in first two dimensions) before performing a normal convolution operation.

Inspired by Torch-Pruning [2], we implemented our re-parametrization algorithm as a convolution constructor function:

```
def merge_conv(convs, c, c_o, k, **kwargs):
                                                 203
  n = len(convs)
                                                 204
  conv = nn.Conv2d(c*n, c_o*n, k, **kwargs)
                                                 205
  for i, j in product(range(n), range(n)):
                                                 206
    if j == i:
                                                 207
      conv.weight[j*c_o:(j+1)*c_o,
                                                 208
                   i*c:(i+1)*c,
                                                 209
                   ...] = \
                                                 210
        convs[i].weight
                                                 211
                                                 212
    else:
      conv.weight[j*c_o:(j+1)*c_o,
                                                 213
                   i*c:(i+1)*c,
                                                 214
                   ...] = \
                                                 215
```

```
torch.zeros(convs[i].weight.shape)
for i in range(n):
    conv.bias[i*c_o:(i+1)*c_o] = \
    convs[i].bias
return conv
```

As is shown in figure 1c, we merge and re-parametrize the four convolution layer, three SRFDBs and four RFDBs into a bigger convolution layer, SRFDB and RFDB respectively. After re-parameterization, we get a bigger model that is equivalent to that of the branches of the branch model computed in parallel on a single GPU.

## 1.6. Pruning

The re-parametrized model is big. To reduce the resource consumption and further accelerate the inference, we applied selective channel-wise auto-pruning (Torch-Pruning [2]) on our re-parametrized model, as is shown in figure 1d.

Specifically, as channel-wise pruning would modify the output channel number of certain layers (especially convolution layers), channel indices of channel-wise split and concatenate operations should be adjusted accordingly. However, determining which channels are pruned during auto-pruning is difficult to implement in code. In our method, we propose to replace all channel-wise split and concatenate operations with equivalent 1x1 convolutions. This allows auto-pruning to directly prune these operations and eliminates the need to adjust their channel indices separately.

After re-parametrization, we used Torch-Pruning to prune the model. In our implementation, we prune out 90% channels from our model in 16 steps, and between each pruning step, we finetune the model by a 10-epoch training. FAQ

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Method	Frobnability
Theirs	Frumpy
Yours	Frobbly
Ours	Makes one's heart Frob

Table 1. Results. Ours is better.

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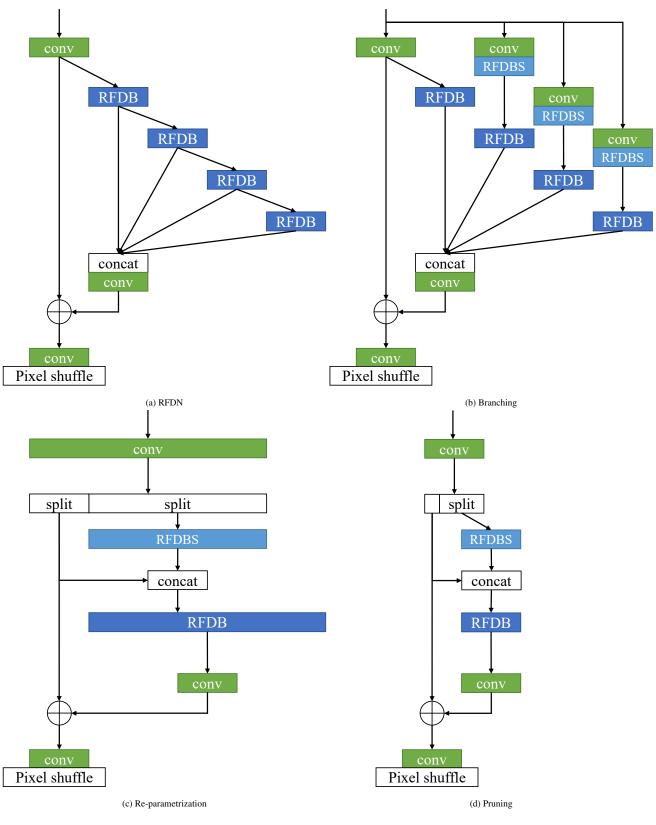


Figure 1. Transform a pre-trained RFDN into PRFDN

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