

Question 1:

cycle time has to be at least 2000 ps

$$\text{clock speed} = \frac{1}{\text{cycle time}} = \frac{1}{2 \times 10^{-3} \times 10^{-9}} = 5 \times 10^8 \text{ Hz} = 0.5 \text{ GHz}$$

CPI (cycles per instruction) = 0.5

 $\Rightarrow$  Throughput (in billion instructions per sec) = 0.5

Question 2:

cycle ~~speed~~ time has to be at least 250 ps

$$\text{clock speed} = \frac{1}{250 \text{ ps}} = 4 \text{ GHz}$$

CPI = 0.5 since there are no stalls

Each clock cycle, a new instruction is fetched  $\Rightarrow$  CPI = 0.5 $\Rightarrow$  Throughput (in billion instructions per sec) = 4

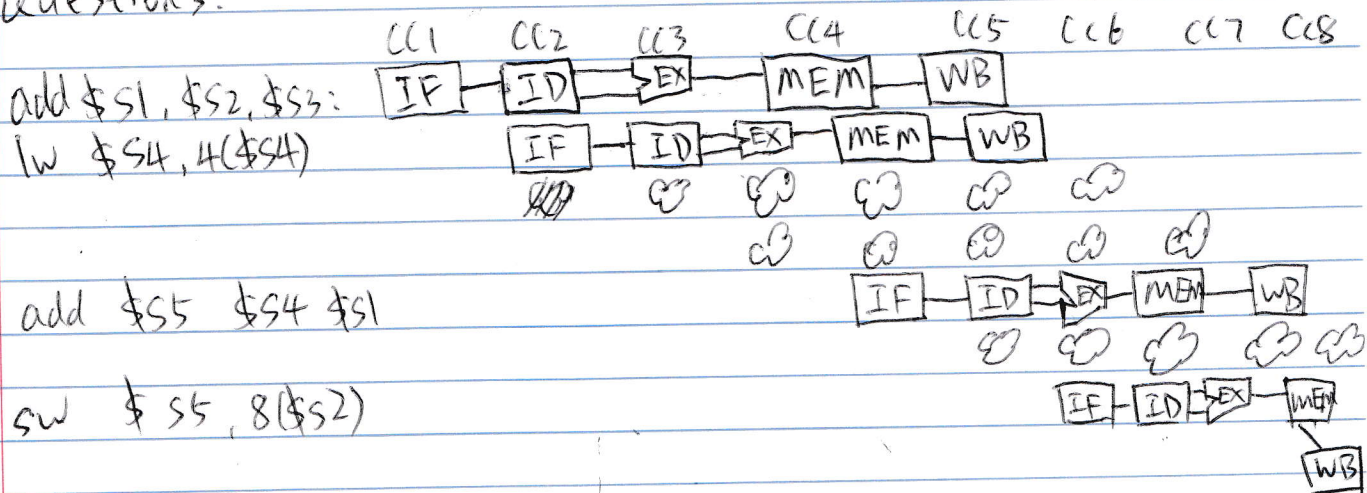
$$\text{speedup} = \frac{\text{new clock speed}}{\text{old clock speed}}$$

$$= 4 \text{ GHz} \div 0.5 \text{ GHz}$$

$$= 8$$

It is less than 10 because the cycle time was increased less than 10 as well.

Question 3:



Question 4:

add \$s1 \$s2 \$s3

lw \$s4 4(\$s4)

add \$s5 \$s4 \$s1

sw \$s5 8(\$s2)

