

CS3810 Assignment 10

Yingjie Lian
U1058784

Question 1:

Let processor is byte-addressable

a) Virtual memory = $2^{32} = 4 \text{ GB}$

b) Physical memory = $2^{36} = 64 \text{ GB}$

c) Number of Virtual pages = $2^{32} / 2^{12} = 2^{20}$

d) Number of physical pages = $2^{36} / 2^{12} = 2^{24}$

Question 2:

Request	Cache Hit/Miss	Request on the bus	who responds	State in cache 1	... in cache 2	... in cache 3
P1: Rdx	miss	Rdx	Memory	S	INV	INV
P1: Wrx	Perms miss	upgrade X	No response. other caches invalidate	M	INV	INV
P3: Rdx	Read miss	Rdx	memory	INV	m	INV
P2: Rdx	Read miss	Rdx	P1 responds	INV	is S	INV
P3: Wrx	write miss	wrx	P2 responds	INV	INV	m

~~Question 3:~~

Request	Cache Hit/Miss	Request on the bus	who responds	State in cache 1	State in cache 2	State in cache 3
P1: Rdx						

Question 3:

Request	Cache Hit/miss	Messages	Dir State	State in C ₁	...in C ₂	...in C ₃
P ₁ : Rd X	Miss	Read Request to Directory Directory responds.	X: S: 1	S	INV	INV
P ₁ : Wr X	Perms Miss	Upgrade request to directory. Grants permission to P ₁	X: M: 1	M	INV	INV
P ₂ : Rd X	Read miss	Read request to directory Directory forward request to P ₁ . P ₁ sends data to directory. memory write back. Directory sends data to P ₂ .	X: S: 2	INV	S	INV
P ₃ : Wr X	Write Miss	Write request to directory. Directory sends invalid to P ₂ . P ₂ sends ACK to Directory. Directory grants perms to P ₃	X: M: 3	INV	INV	M
P ₃ : Rd X	Read Miss	Read request to directory Directory forward request to P ₂ . P ₂ sends data to directory. memory write back. Directory send data to P ₃	X: S: 3	INV	INV INV	INV S

Question 4:

Since those footprints were left behind, the attacker was able to essentially walk through the cache and figure out the elements.

As a result, the processors can defend the meltdown attacker by squashing the illegal instructions in the reorder buffer and clean the foot prints. often.