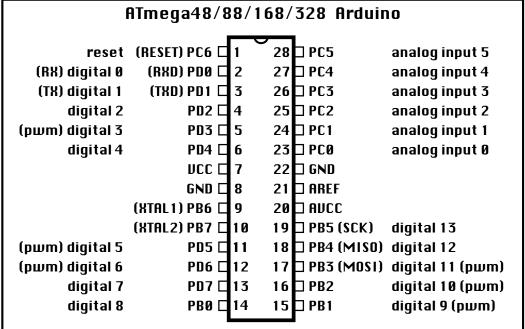
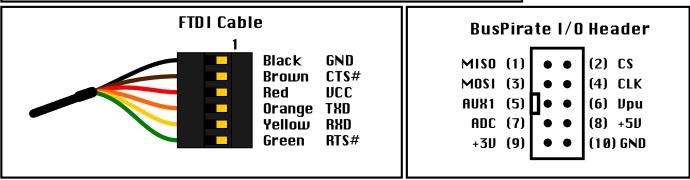
## ATtiny25/45/85 ISP Header 6-pin (RESET/ADCØ) PB5 □1 8₽ ncc MISO (1) (2) VCC (XTAL1/ADC3) PB3 **□**2 7 PB2 (SCK/ADC1) SCK (3) (4) MOSI (XTAL2/ADC2) PB4 □3 6□ PB1 (MISO) RESET (5) (6) GND GND □ 5 PB0 (MOSI) ATtiny2313 ISP Header 10-pin (RESET) PA2☐1 20 🗖 VCC **MOSI (1)** (2) UCC (RXD) PD0 **□** 2 19 PB7 (SCL) NC (3) (4) GND (TXD) PD1 ☐3 18 PB6 (MISO) RESET (5) (6) GND 17 PB5 (MOSI) (XTAL2) PA1 ☐ 4 (8) GND **SCK (7)** 16 □ PB4 (XTAL1) PA0□5 MISO (9) (10) GND PD2 □ 6 15 PB3 14 PB2 PD3 □ 7 13 PB1 PD4 □8 12 □ PB0 PD5 □ 9 11 PD6 GND ☐ 10





- +++ Microcontroller Cheat Sheet +++  $\nu$ 1.1 +++ 2010/01/24 +++ http://tinkerlog.com +++ edit Philipp Stenkamp, added BusPirate header
- +++ For Atmel datasheets visit: http://www.atmel.com/products/AVR/ +++
- +++ For FTDI datasheets visit: http://www.ftdichip.com/Documents/DataSheets.htm +++