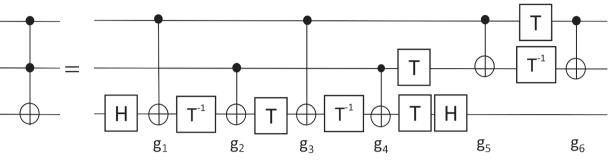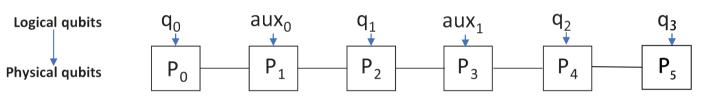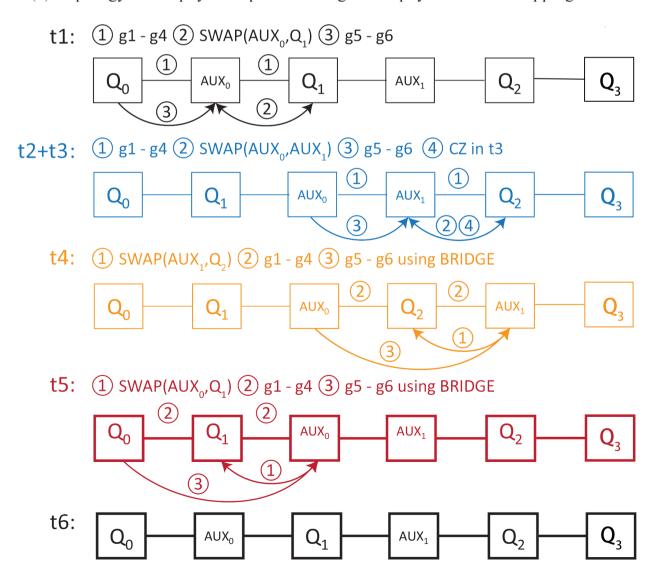(a) CCZ gate and the decomposed version of CCZ gate.



(b) CCX gate and the decomposed version of CCX gate.



(c) Topology of the physical qubits and logical-to-physical initial mapping.



(d) Qubits' status at different times from t1 - t6