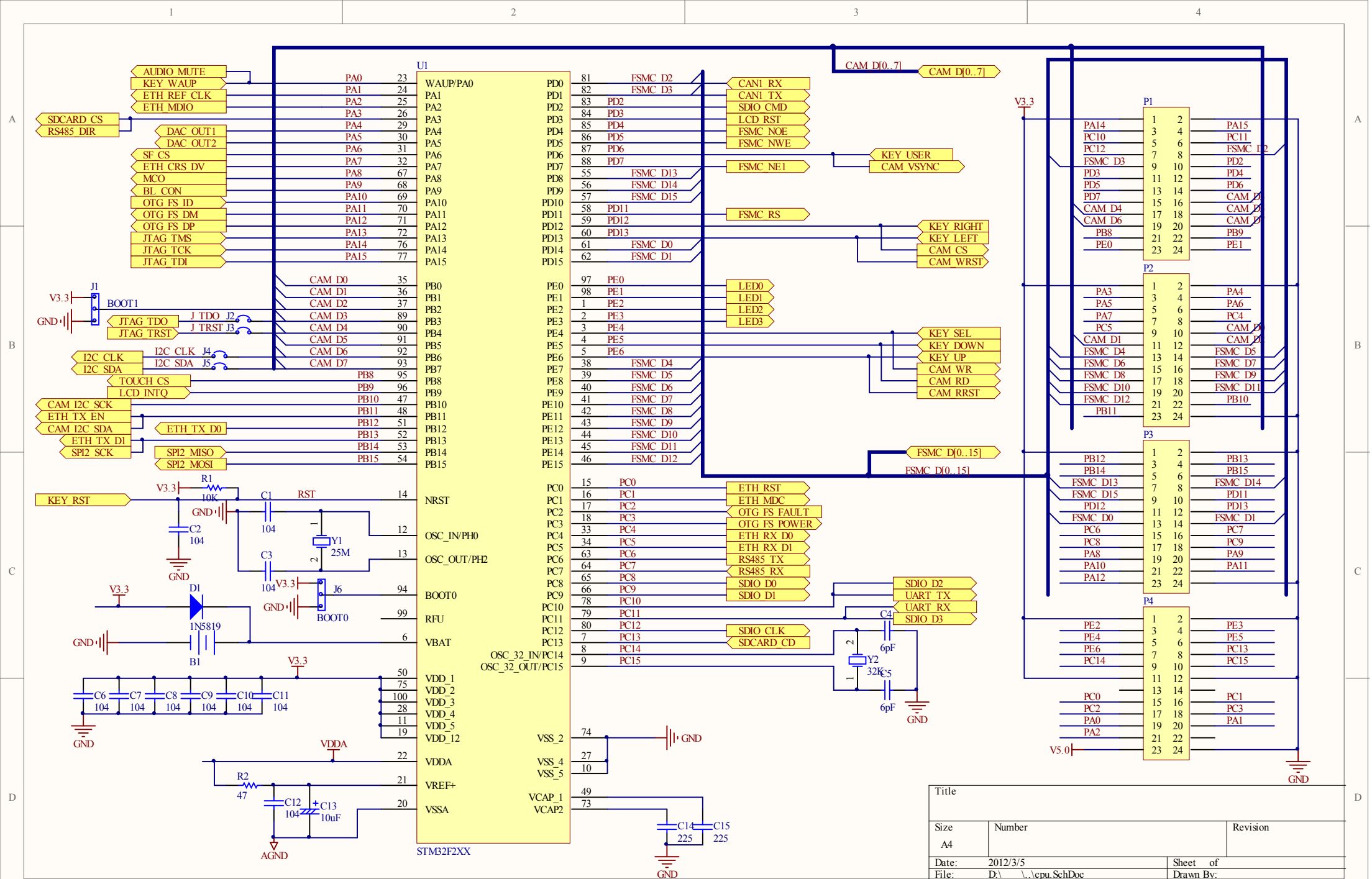
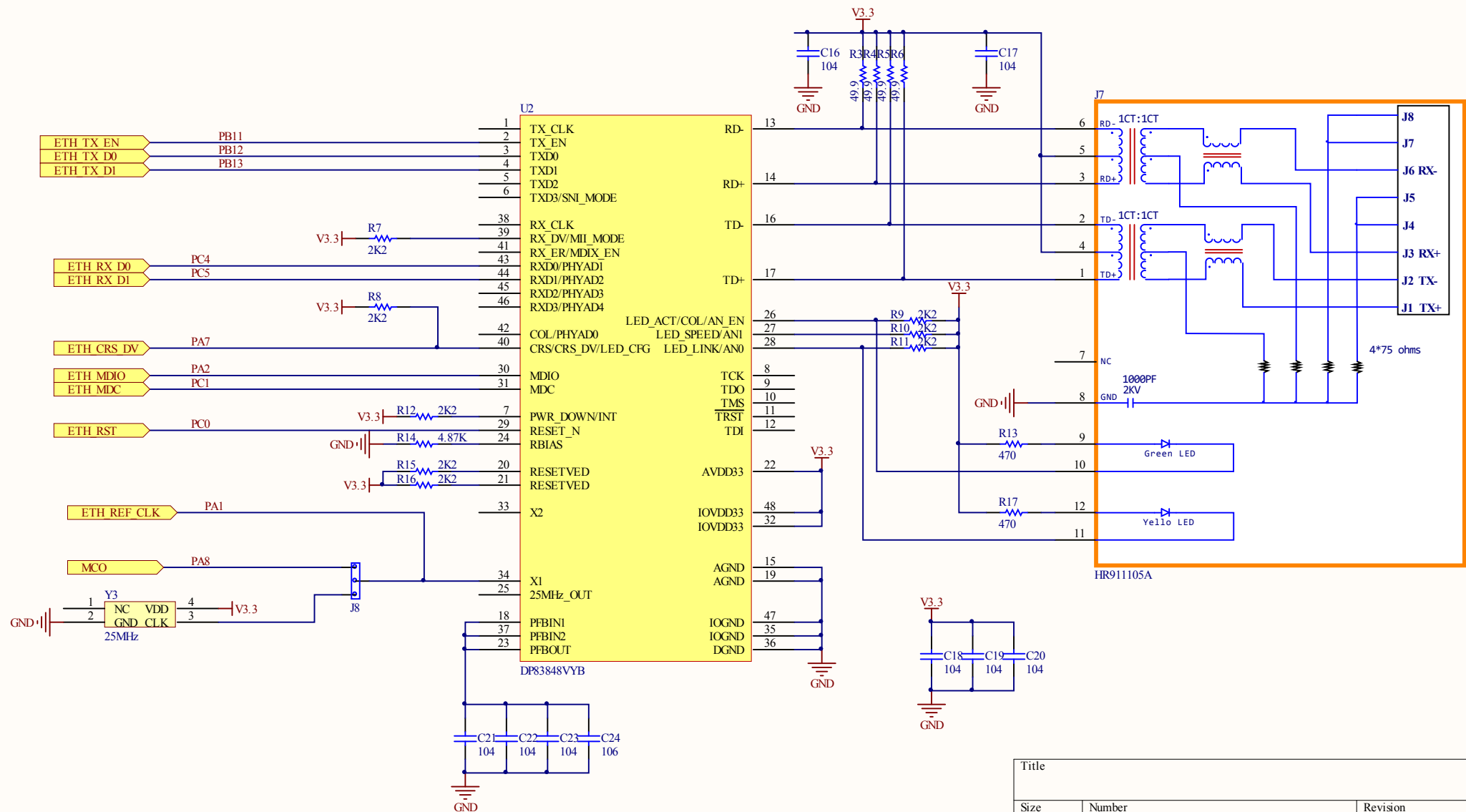
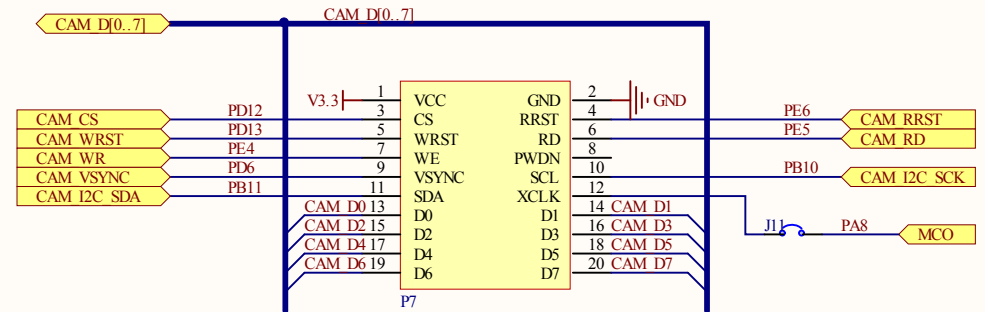
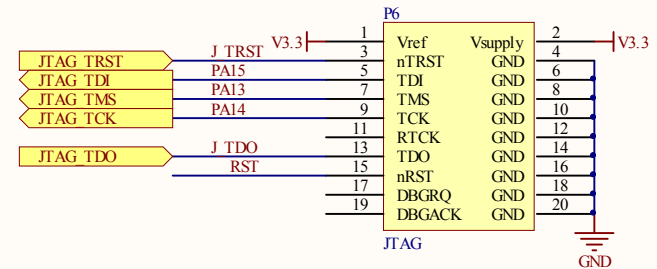
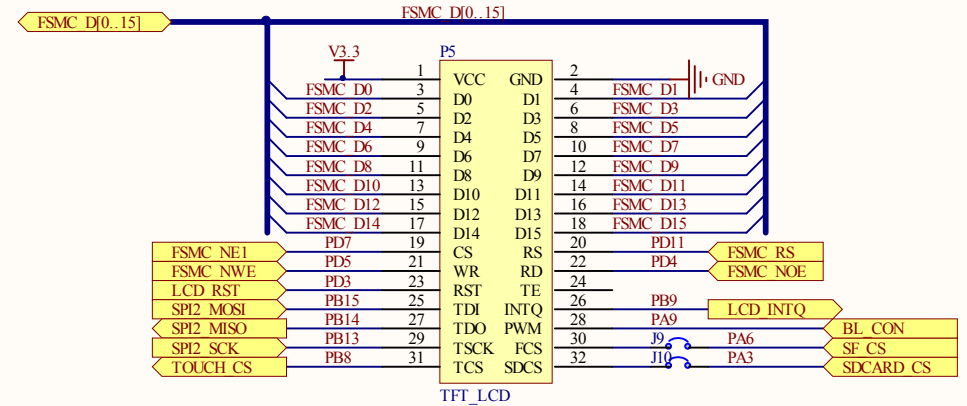
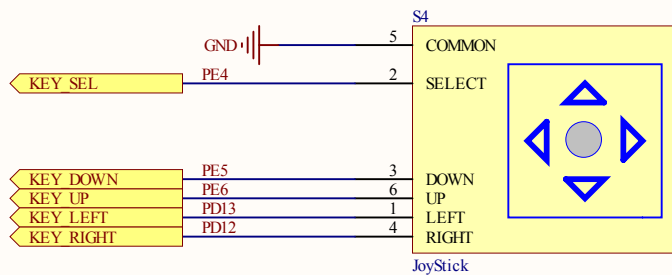
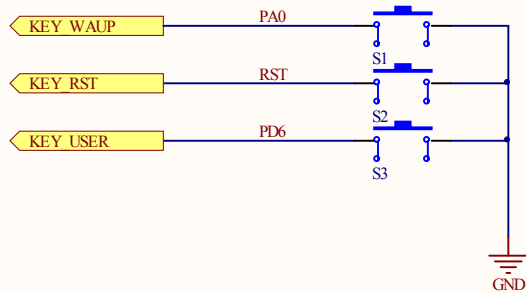
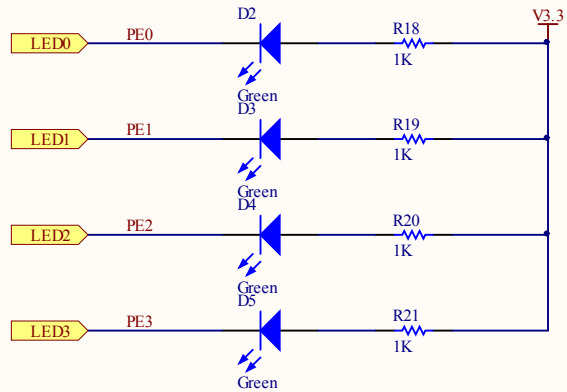


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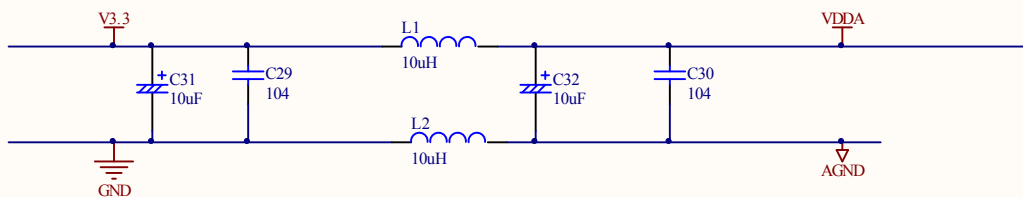
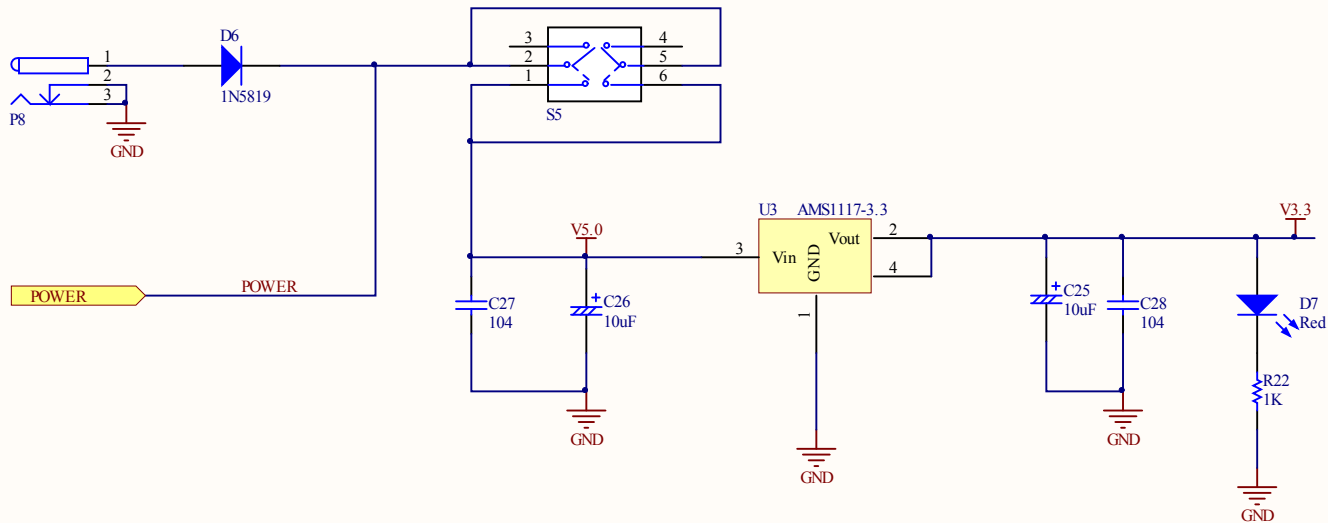


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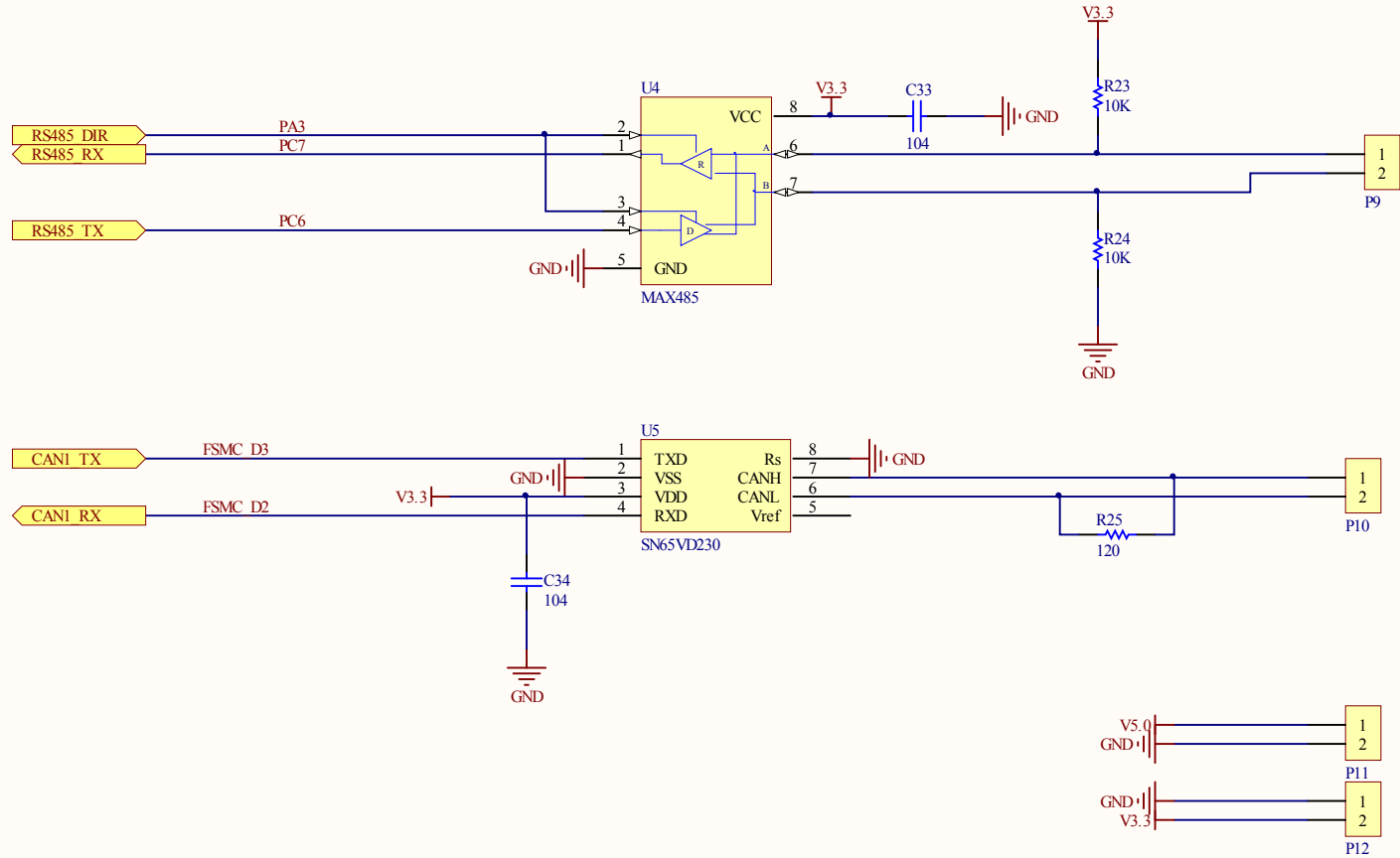




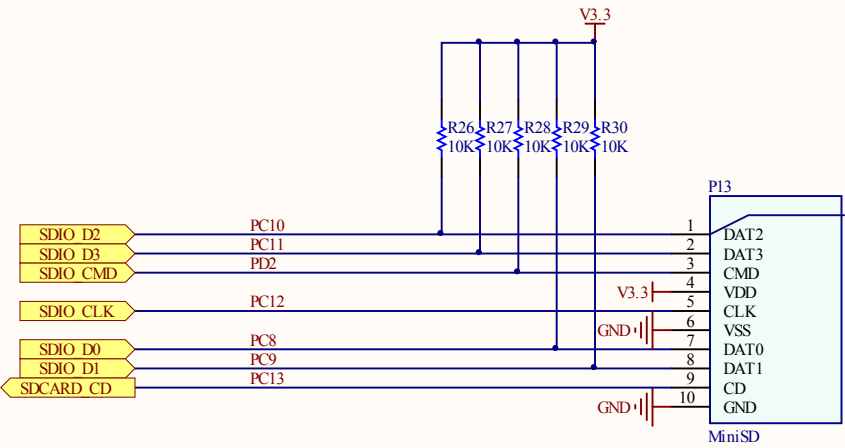
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File:	D:\ \ \power.SchDoc	Drawn By:



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File:	D:\ \ \RS485_CAN.SchDoc	Drawn By:



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File:	D:\ \ \SD_Card.SchDoc	Drawn By:

USBOTG&USBT0232
usb.SchDoc

UART_RX
UART_TX
POWER
OTG_FS_FAULT
OTG_FS_POWER
OTG_FS_DM
OTG_FS_DP
OTG_FS_ID

LCD&CAM&JTAG
interface.SchDoc

FSMC_D[0..15]
FSMC_NE1
FSMC_NWE
FSMC_NOE
FSMC_RS
LCD_RST
BL_CON
SPI2_MOSI
SPI2_MISO
SPI2_SCK
TOUCH_CS
LCD_INTQ
SDCARD_CS
SF_CS

LED0
LED1
LED2
LED3

KEY_WAUP
KEY_RST
KEY_SEL
KEY_USER

KEY_UP
KEY_DOWN
KEY_LEFT
KEY_RIGHT

JTAG_TRST
JTAG_TDI
JTAG_TMS
JTAG_TCK
JTAG_TDO

CAM_CS
CAM_WRST
CAM_RRST
CAM_WR
CAM_RD
CAM_VSYNC
CAM_I2C_SCK
CAM_I2C_SDA

CAM_D[0..7]

MCO

CPU
cpu.SchDoc

UART_RX
UART_TX
OTG_FS_FAULT
OTG_FS_POWER
OTG_FS_DM
OTG_FS_DP
OTG_FS_ID

FSMC_D[0..15]
FSMC_NE1
FSMC_NWE
FSMC_NOE
FSMC_RS
LCD_RST
BL_CON
SPI2_MOSI
SPI2_MISO
SPI2_SCK
TOUCH_CS
LCD_INTQ
SDCARD_CS
SF_CS

LED0
LED1
LED2
LED3

KEY_WAUP
KEY_RST
KEY_SEL
KEY_USER

KEY_UP
KEY_DOWN
KEY_LEFT
KEY_RIGHT

JTAG_TRST
JTAG_TDI
JTAG_TMS
JTAG_TCK
JTAG_TDO

CAM_CS
CAM_WRST
CAM_RRST
CAM_WR
CAM_RD
CAM_VSYNC
CAM_I2C_SCK
CAM_I2C_SDA

CAM_D[0..7]

MCO

SDIO_D0
SDIO_D1
SDIO_D2
SDIO_D3
SDIO_CLK
SDIO_CMD
SDCARD_CD

RS485_DIR
RS485_TX
RS485_RX
CAN1_TX
CAN1_RX

ETH_TX_EN
ETH_TX_D0
ETH_TX_D1
ETH_RX_D0
ETH_RX_D1
ETH_REF_CLK

ETH_CRS_DV
ETH_MDIO
ETH_MDC
ETH_RST

DAC_OUT1
DAC_OUT2

AUDIO_MUTE

I2C_CLK
I2C_SDA

MicroSD
SD Card.SchDoc

SDIO_D0
SDIO_D1
SDIO_D2
SDIO_D3
SDIO_CLK
SDIO_CMD
SDCARD_CD

RS485&CAN
RS485 CAN.SchDoc

RS485_DIR
RS485_TX
RS485_RX
CAN1_TX
CAN1_RX

ethernet
eth.SchDoc

ETH_TX_EN
ETH_TX_D0
ETH_TX_D1
ETH_RX_D0
ETH_RX_D1
ETH_REF_CLK

ETH_CRS_DV
ETH_MDIO
ETH_MDC
ETH_RST

MCO

AD&DA
AD DA.SchDoc

DAC_OUT1
DAC_OUT2

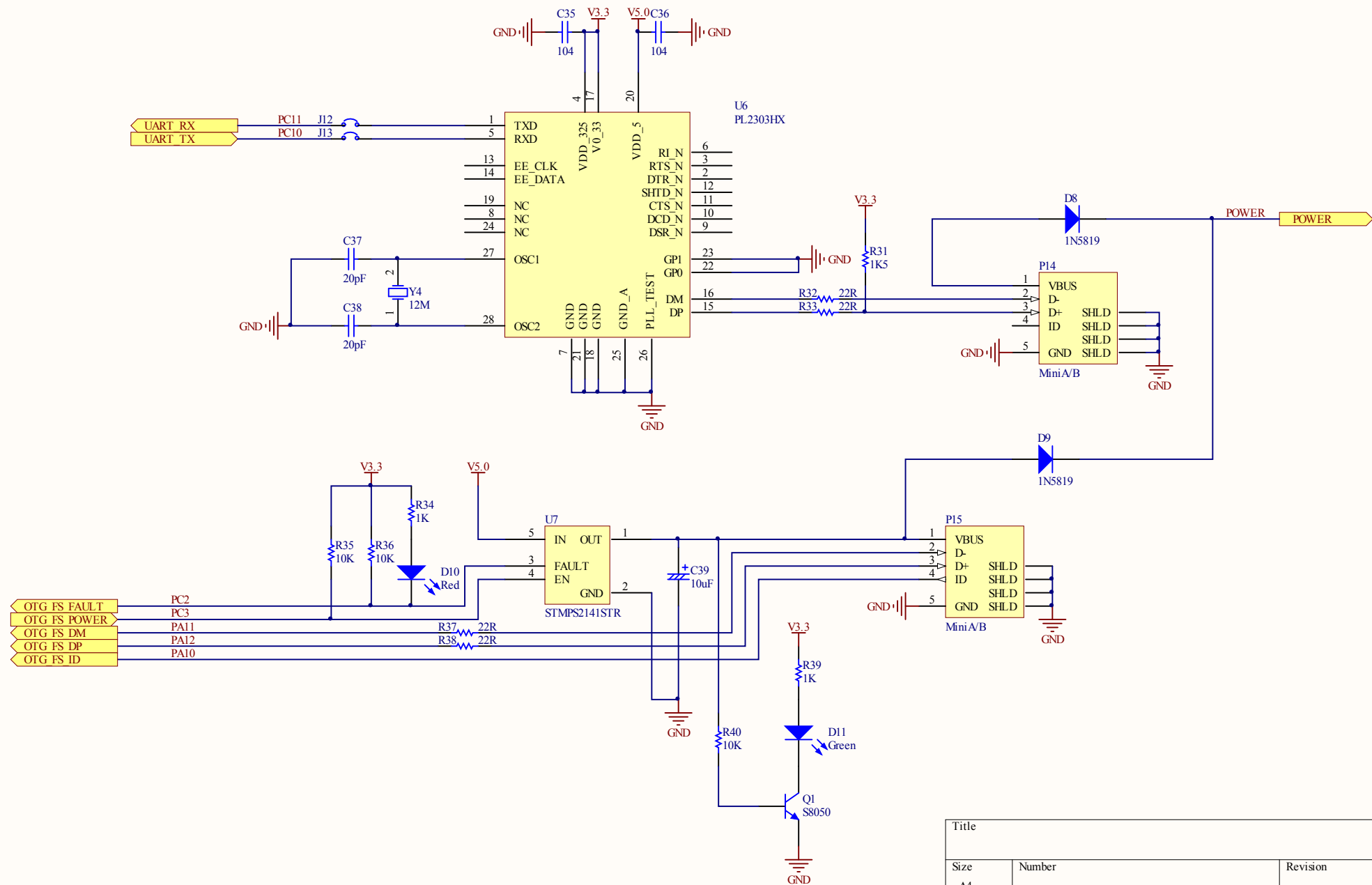
AUDIO_MUTE

I2C_CLK
I2C_SDA

Power
power.SchDoc

POWER

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Title		
Size	Number	Revision
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