Circuit Demonstration Page

Student names: _____ Yiting Wang _____

_____ _____

Instructor Signatures

Separate Full Adders _____

Two-Bit Adder _____

Adder/Subtractor _____

Inputs		Expected Results			Actual Results
A	B	B 2's comp	Sub	Dec	Sub
00	01	111	111	-1	011
00	10	110	110	-2	010
00	11	101	101	-3	001
01	01	111	000	0	100
10	01	111	001	1	101
10	00	000	010	2	110