



Accusilicon A620T, Ultra-Low Jitter IIS Clock Generator

Features

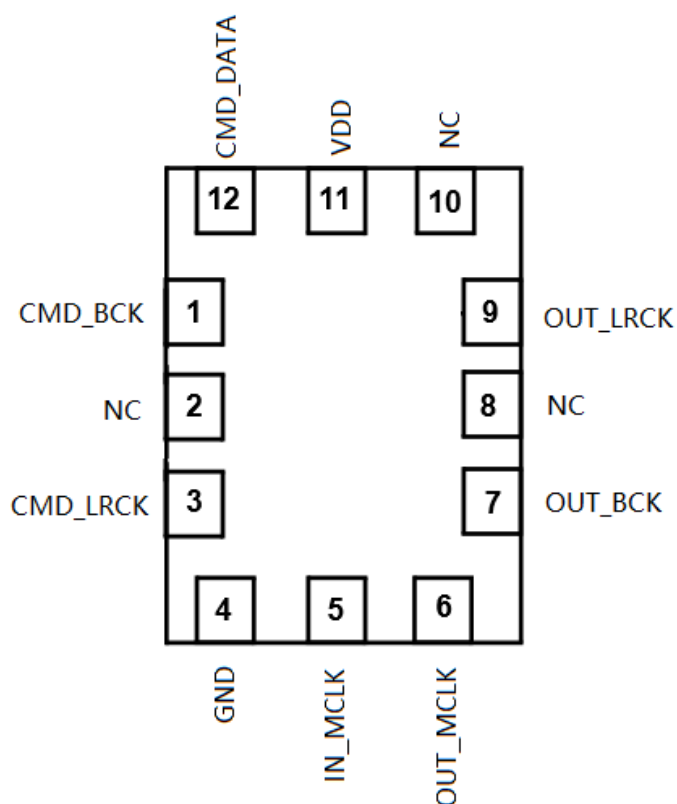
- Ultra-low jitter IIS clock output, Jitter-Free clock transmission technology inside*
 - * Does Not Degrade Phase Noise
- Jitter-Free clock buffer
- Programmable Ultra-low jitter clock divider
- Embedded control command via IIS bus
- Very low power, typical 1mW
- Support both 1.8V and 3.3V supply voltage
- 2.0mmx2.0mm 12 pin QFN Package

Application

- High Quality Audiophile Application
- Test and Measurement Instrument
- Broadcast Audio
- Audio Clock Generator
- Audio Clock Recovery



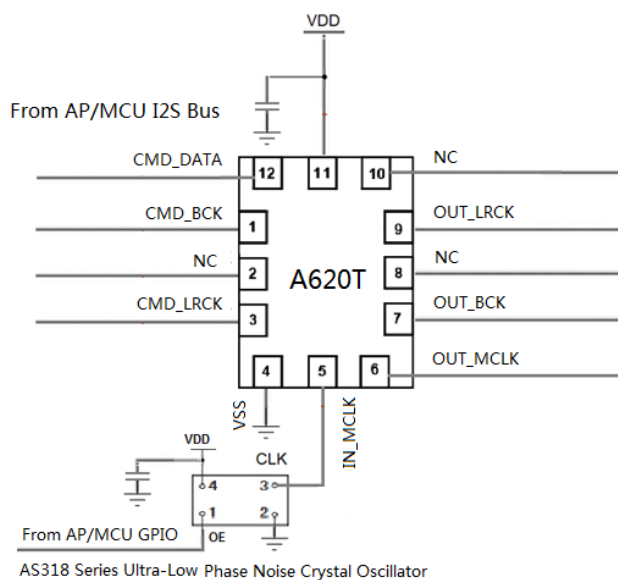
Pin Assignment-A620T



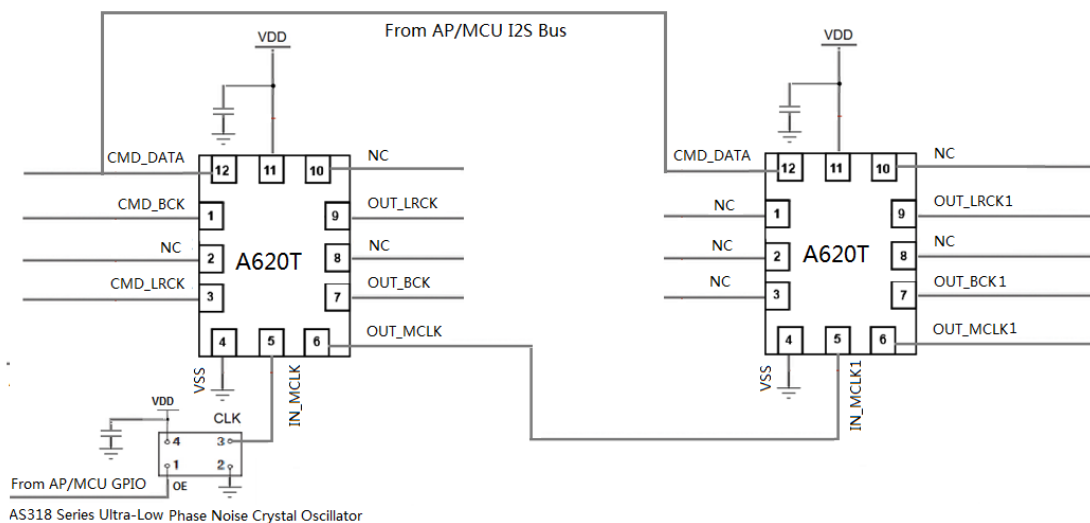
TOP VIEW

No	Symbol	Description
1	CMD_BCK	I2S bit Clock of the control command
2	NC	
3	CMD_LRCK	I2S left and right channel frame clock of the control command
4	GND	Ground
5	IN_MCLK	Master clock input
6	OUT_MCLK	Master clock output
7	OUT_BCK	I2S bit clock output
8	NC	
9	OUT_LRCK	I2S right channel frame clock output
10	NC	
11	VDD	Supply voltage, support both 1.8V and 3.3V supply voltage
12	CMD_DATA	I2S serial input data of the control command

Reference Design-A620T



Ultra-Low Jitter IIS Clock Generator with A620T



Ultra-Low Jitter IIS Clock Buffer and Generator with A620T



Electrical Specifications

Absolute Maximum Ratings

Symbol	Description	Min	Max	Unit
VDD	Supply Voltage		4.6	V
VIN	Input Voltages	-0.5	VDD+0.5	V
VOOUT	Output Voltages	-0.5	VDD+0.5	V
TA	Operating Temperature	-40	85	°C
TS	Storage Temperature	-65	150	°C
ESD	HBM ESD Protection	2000		V

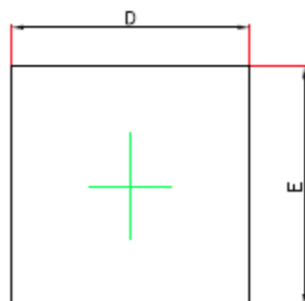
General Electrical Specifications

Symbol	Description	Conditions	Min	Typical	Max	Unit
IDD	Supply Current	VDD=3.3V		0.7		mA
Ioff	Disable Mode Current				20	uA
VDD*	Supply Voltage		1.7		3.7	V

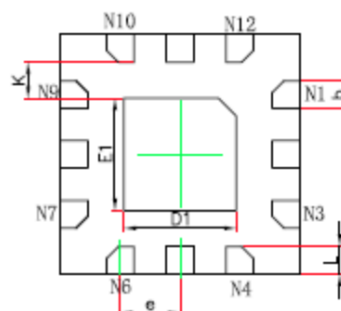
Note*: It is recommended that the power supply be filtered with a 0.1uF capacitor.

Package Outlines (2.0mm x 2.0mm) -A620T

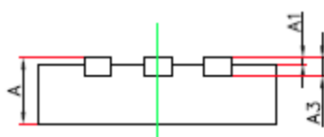
QFNWB2×2-12L-A (P0.50T0.55) PACKAGE OUTLINE DIMENSIONS



TOP VIEW



BOTTOM VIEW



SIDE VIEW

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.500	0.600	0.020	0.024
A1	0.000	0.050	0.000	0.002
A3	0.152REF.		0.006REF.	
D	1.924	2.076	0.076	0.082
E	1.924	2.076	0.076	0.082
D1	0.840	1.040	0.033	0.041
E1	0.840	1.040	0.033	0.041
k	0.200MIN.		0.008MIN.	
b	0.180	0.280	0.007	0.011
e	0.500TYP.		0.020TYP.	
L	0.154	0.306	0.006	0.012



PCB Land Pattern (2.0mm x 2.0mm) -A620T

