点阵字符显示控制器设计 (仿真实现)

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叶剑飞、刘艳杰、苏维、 张印

计思路

4. 6 赤頂

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题目要求

设计思路

HDL 源代码

壬字毕

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题目要求

设计思路

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6元字题

▶ 用 16×16 点阵的发光二极管显示字符;

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题目要求

设计思路

油壬字总

- ▶ 用 16×16 点阵的发光二极管显示字符;
- ▶ 可显示字符为 0~9 的数字字符与 A~F 英文字母;

题目要求

点阵字符显示控制器设计 (仿真实现) 叶剑飞、刘艳杰、苏维、

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题目要求

设计思路

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方示完毕

- ▶ 用 16×16 点阵的发光二极管显示字符;
- ▶ 可显示字符为 0~9 的数字字符与 A~F 英文字母;
- ▶ 输入为四位二进制矢量。

设计思路

点阵字符显示控制器设计 (仿真实现)

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设计思路

HDL 源代码

千字比

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点阵字符显示控制器设计

(仿真实现)

张印

设计思路

▶ 行共阳,列共阴。用 clk 作为时钟信号,逐行扫描显示。 用 current row 变量来储存当前显示的行,枚举每行的显 示内容即可显示。

直生现

- ► 行共阳,列共阴。用 clk 作为时钟信号,逐行扫描显示。 用 current\_row 变量来储存当前显示的行,枚举每行的显示内容即可显示。
- ▶ 四个输入按钮,分别代表四位二进制的输入信号。然后用 CASE 语句,下面 16 个 WHEN,分别处理"0000"至 "1111",外加上一个"WHEN OTHERS",即可把这几个 数字枚举转化成整型。把这个整型存在信号量 decimal 中。

设计思路

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- ▶ 行共阳,列共阴。用 clk 作为时钟信号,逐行扫描显示。 用 current\_row 变量来储存当前显示的行,枚举每行的显示内容即可显示。
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- ▶ 同样用 CASE-WHEN 枚举 16 个整数信号量,分别处理十 六个整数的显示。

设计思路

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- ▶ 行共阳,列共阴。用 clk 作为时钟信号,逐行扫描显示。 用 current\_row 变量来储存当前显示的行,枚举每行的显示内容即可显示。
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```
- 点阵字符显示控制器设计
                                                VHDI 源代码
__ 叶剑飞、刘艳杰、苏维、张印
LIBRARY IEEE;
USE IEEE.STD LOGIC 1164.ALL;
ENTITY binToHexDisplay IS
  PORT ( clk : IN STD LOGIC;
      binaryInput: IN STD LOGIC VECTOR ( 3
                       DOWNTO 0 );
      rows: OUT STD LOGIC VECTOR(0 TO 15);
      columns: OUT STD LOGIC VECTOR(15
                       DOWNTO 0 )
END ENTITY binToHexDisplay;
ARCHITECTURE show Digits OF bin To Hex Display IS Q
```

VHDL 源代码

( 仿真实现 ) 叶剑飞、刘艳杰、苏维、

```
SIGNAL decimal: INTEGER;
  SHARED VARIABLE current row : INTEGER := 0:
BEGIN
  PROCESS (binaryInput)
  BEGIN
    CASE binaryInput IS
      WHEN "0000" \Rightarrow decimal <= 0;
      WHEN "0001" \Rightarrow decimal \leq 1:
      WHEN "0010" \Rightarrow decimal <= 2;
      WHEN "0011" \Rightarrow decimal <= 3;
      WHEN "0100" \Rightarrow decimal <= 4;
      WHEN "0101" \Rightarrow decimal \leq 5;
      WHEN "0110" \Rightarrow decimal \leq 6;
      WHEN "0111" \Rightarrow decimal <= 7;
      WHEN "1000" \Rightarrow decimal \leq 8;
      WHEN "1001" \Rightarrow decimal \leq 9;
      WHEN "1010" \Rightarrow decimal \leq 10;
      WHEN "1011" =>
                          decimal \ll 11:
      WHEN "1100" \Rightarrow decimal <= 12;
      WHEN "1101" =>
                          decimal \ll 13;
      WHEN "1110" =>
                          decimal \ll 14:
                                ◆□▶ ◆圖▶ ◆差▶ ◆差▶ 差 のQ@
```

点阵字符显示控制器设计 (仿真实现)

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目要求

VHDI 源代码

有效现

```
WHEN "1111" \Rightarrow decimal \leftarrow 15;
    WHEN OTHERS => decimal <= 1000:
  END CASE:
END PROCESS:
PROCESS (decimal, clk)
BFGIN
  IF clk='1' AND clk 'EVENT THEN
    IF current row = 15 THEN
      current row := 0;
    ELSE
      current row := current_row + 1;
    END IF:
    CASE current row IS
      WHEN 0 => rows <= "01111111111111111":
      WHEN 1 => rows <= "1011111111111111";
      WHEN 2 => rows <= "1101111111111111":
      WHEN 3 => rows <= "1110111111111111";
      WHEN 4 => rows <= "1111011111111111";
      WHEN 5 => rows <= "11111011111111111":
                          4□ > 4回 > 4 = > 4 = > = 9 < 0</p>
```

(仿真实现) 叶剑飞、刘艳杰、苏维、

张印

VHDI 源代码

```
WHEN 6 => rows <= "111111101111111111":
 WHEN 7 => rows <= "111111110111111111":
                                叶剑飞、刘艳杰、苏维、
 WHEN 8 => rows <= "1111111111111111":
 WHEN 9 => rows <= "11111111111111111":
 WHEN 10 => rows <= "111111111111111111":
 VHDI 源代码
 WHEN 13 => rows <= "11111111111111111":
 WHEN OTHERS => rows<="XXXXXXXXXXXXXXXXXX":
END CASE:
CASE decimal IS
 WHEN 0 \Rightarrow
   CASE current_row IS
    WHEN 2 =>columns<="00000111111100000":
    WHEN 3 = \text{columns} < \text{"0000111001110000"};
    WHEN 4 =>columns<="0011100000011100":
    WHEN 5 =>columns<="011100000001110":
               4日 → 4周 → 4 三 → 4 三 → 9 Q P
```

(仿真实现)

张印

```
WHEN 6 =>columns<="0111000000001110"
   WHEN 7 =>columns<="011100000001110": (份真实现
   WHEN 9 =>columns<="011100000001110";
   WHEN 10=>columns<="011100000001110";
   WHEN 11=>columns<="0011100000011100" | WHEN 31=>columns
   WHEN 12=>columns<="0000111001110000":
   WHEN 13=>columns<="00000111111100000":****
   WHEN 14=>columns<="0000000000000000":
   WHEN 15=>columns<="0000000000000000":
   WHEN OTHERS =>
          END CASE:
WHEN 1
  CASE current row IS
   WHEN 0 = > columns < = "00000000000000000":
   WHEN 1 = > columns < = "0000000011110000":
   WHEN 2 =>columns<="0000000111110000":
   WHEN 3 =>columns<="0000001101110000":
   WHEN 4 = columns < 0.000011001110000":
   WHEN 5 =>columns<="000000001110000":
   WHEN 6 =>columns<="000000001110000":
                  < □ ト < 圖 ト < 直 ト < 直 ト 三 直
```

```
WHEN 7 =>columns<="000000001110000":
   WHEN 8 =>columns<="000000001110000": (份真实现
   WHEN 10=>columns<="000000001110000";
   WHEN 11=>columns<="000000001110000";
   WHEN 12=>columns<="000000001110000" | WHEN 32=>columns
   WHEN 13=>columns<="000000001110000":
   WHEN 14=>columns<="00000011111111100"
   WHEN 15=>columns<="0000000000000000":
   WHEN OTHERS =>
          END CASE:
WHEN 2 \implies
 CASE current row IS
   WHEN 0 =>columns<="000000000000000":
   WHEN 2 =>columns<="00000111111110000":
   WHEN 3 = columns < 0.001110000011100":
   WHEN 4 =>columns<="0011100000001110":
   WHEN 5 =>columns<="0011100000001110":
   WHEN 6 =>columns<="000000000011100":
   WHEN 7 =>columns<="000000000111000":
                < □ ト < 圖 ト < 直 ト < 直 ト 三 直
```

```
WHEN 9 =>columns<="0000001110000000": (份真实现
   WHEN 11=>columns<="000111000000000";
   WHEN 12=>columns<="00111111111111100";
   WHEN 14=>columns<="0000000000000000":
   WHEN 15=>columns<="0000000000000000":***
   WHEN OTHERS =>
         END CASE:
WHEN 3 \Rightarrow
 CASE current row IS
   WHEN 1 =>columns<="00011111111000000";
   WHEN 2 =>columns<="0000000001110000":
   WHEN 3 = columns < 0.000000000111000":
   WHEN 4 = \cos \mu mns \le 0.000000000001100":
   WHEN 5 =>columns<="0000000000001100":
   WHEN 6 =>columns<="0000000001110000":
   WHEN 7 = \text{columns} < \text{"0001111111000000"}:
   WHEN 8 =>columns<="000000001110000":
               イロト イ御 トイヨ トイヨ トーヨー
```

```
WHEN 9 =>columns<="0.00000000111000":
   WHEN 10=>columns<="000000000001100":
   WHEN 11=>columns<="000000000001100":
   WHEN 12=>columns<="000000000111000";
   WHEN 13=>columns<="000000001110000";
   WHEN 14=>columns<="00011111111000000"
                                     VHDL 源代码
   WHEN 15=>columns<="0000000000000000":
   WHEN OTHERS =>
          END CASE:
WHFN 4
  CASE current_row IS
   WHEN 0 = > columns < = "00000000000000000":
   WHEN 2 =>columns<="0000001110000000":
   WHEN 3 =>columns<="0000110110000000":
   WHEN 4 = > columns < = "0000110110000000":
   WHEN 5 =>columns<="00011001100000000":
   WHEN 6 =>columns<="0001100110000000":
   WHEN 7 =>columns<="0011000110000000":
   WHEN 8 =>columns<="01111111111111100":
   WHEN 9 =>columns<="11111111111111100":
```

```
WHEN 10=>columns<="0.000000110000000":
   WHEN 11=>columns<="0000000110000000":
   WHEN 12=>columns<="0000000110000000":
   WHEN 13=>columns<="0000000110000000";
   WHEN 14=>columns<="0000001111000000";
   WHEN 15=>columns<="0000000000000000"
                                      VHDL 源代码
   WHEN OTHERS =>
          END CASE:
WHFN 5
  CASE current row IS
   WHEN 0 =>columns<="000000000000000":
   WHEN 1 =>columns<="00111111111111100":
   WHEN 2 =>columns<="00111111111111100":
   WHEN 4 \Rightarrow columns <= "001110000000000":
   WHEN 5 =>columns<="0011100000000000":
   WHEN 6 =>columns<="001110000000000":
   WHEN 7 =>columns<="0011100000000000":
   WHEN 8 =>columns<="00111111111000000":
   WHEN 9 \Rightarrow columns <= "000000001110000":
   WHEN 10=>columns<="000000000111000":
```

```
WHEN 11=>columns<="0.000000000011100": ****
    WHEN 12=>columns<="0011100000011100":
    WHEN 13=>columns<="0011100000111000":
    WHEN 14=>columns<="00011111111100000";
    WHEN 15=>columns<="000000000000000";
    WHEN OTHERS =>
                                         VHDI 源代码
           END CASE:
WHFN 6
  CASE current row IS
    WHEN 0 =>columns<="0000000000000000":
    WHEN 1 =>columns<="00001111111110000":
    WHEN 2 =>columns<="0011100000011100":
    WHEN 3 = \text{columns} < = 0.011100000000011":
    WHEN 4 =>columns<="011100000000000";
    WHEN 5 =>columns<="01110000000000000":
    WHEN 6 =>columns<="111000000000000":
    WHEN 7 = \cos u m n s < = "11100000000000000":
    WHEN 8 =>columns<="11100111111100000":
    WHEN 9 =>columns<="111111111111111000":
    WHEN 10=>columns<="11110000011111100":
    WHEN 11=>columns<="1110000000011110":
```

```
WHEN 12=>columns<="011000000001111"
    WHEN 13=>columns<="011000000001100":
    WHEN 14=>columns<="0011100000111000":
   WHEN 15=>columns<="00001111111110000";
    WHEN OTHERS =>
           VHDL 源代码
  END CASE:
WHFN 7
  CASE current row IS
   WHEN 0 =>columns<="000000000000000":
    WHEN 1 =>columns<="00111111111111111":
   WHEN 2 =>columns<="01111111111111100";
   WHEN 3 =>columns<="0000000000001100":
    WHEN 4 =>columns<="000000000011000":
   WHEN 5 =>columns<="000000000110000":
   WHEN 6 =>columns<="000000001110000":
    WHEN 7 = > columns < = "0000000001110000":
   WHEN 8 =>columns<="0000000011100000":
    WHEN 9 =>columns<="0000000011100000":
    WHEN 10=>columns<="0000000111000000":
    WHEN 11=>columns<="0000000111000000":
    WHEN 12=>columns<="0000001110000000":
```

```
WHEN 13=>columns<="0.000001110000000":
   WHEN 14=>columns<="0000001110000000":
   WHEN OTHERS =>
          END CASE:
                                      VHDI 源代码
WHFN 8
  CASE current row IS
   WHEN 0 =>columns<="00001111111110000":
   WHEN 1 =>columns<="0001110000111000":
   WHEN 2 =>columns<="0011100000011100":
   WHEN 3 =>columns<="0111000000001110":
   WHEN 4 \Rightarrow columns \leq "011100000001110":
   WHEN 5 =>columns<="0011100000011100":
   WHEN 6 =>columns<="0001110000111000":
   WHEN 7 =>columns<="00000111111100000":
   WHEN 8 =>columns<="0.0000111111100000":
   WHEN 9 =>columns<="0001110000111000":
   WHEN 10=>columns<="0011100000011100":
   WHEN 11=>columns<="0111000000001110":
   WHEN 12=>columns<="011100000001110":
   WHEN 13=>columns<="0011100000011100":
```

```
WHEN 14=>columns<="0001110000111000":
    WHEN 15=>columns<="00001111111110000";
                                          叶剑飞、刘艳杰、苏维、
    WHEN OTHERS =>
                                              张印
           columns <= "XXXXXXXXXXXXXXXXX";
  END CASE:
WHFN 9
                                          VHDI 源代码
  CASE current row IS
    WHEN 0 =>columns<="00000111111100000":****
    WHEN 1 = > columns < = "0001111001111000":
    WHEN 2 =>columns<="0011100000011100":
    WHEN 3 =>columns<="011100000001110":
    WHEN 4 =>columns<="1110000000000111":
    WHEN 5 =>columns<="1110000000000111":
    WHEN 6 =>columns<="0111000000001110":
    WHEN 7 =>columns<="0.001111001111111":
    WHEN 8 =>columns<="0000011111000111":
    WHEN 9 =>columns<="0000000000001110":
    WHEN 10=>columns<="000000000011100":
    WHEN 11=>columns<="0000000001110000":
    WHEN 12=>columns<="0000000111000000":
    WHEN 13=>columns<="0000011110000000":
    WHEN 14=>columns<="0011110000000000":
```

```
WHEN OTHERS =>
          END CASE:
WHEN 10 =>
  CASE current row IS
                                      VHDI 源代码
   WHEN 0 =>columns<="0000001111000000":
   WHEN 1 =>columns<="0000011001100000":****
   WHEN 2 = columns < 0.000110000110000:
   WHEN 3 =>columns<="0000110000110000":
   WHEN 4 =>columns<="0001100000011000":
   WHEN 5 =>columns<="0001100000011000";
   WHEN 6 =>columns<="0001100000011000":
   WHEN 7 =>columns<="0011000000001100":
   WHEN 8 =>columns<="00111111111111100";
   WHEN 9 =>columns<="00111111111111100":
   WHEN 10=>columns<="011000000000110":
   WHEN 11=>columns<="0110000000000110":
   WHEN 12=>columns<="0110000000000110":
   WHEN 13=>columns<="110000000000011";
   WHEN 14=>columns<="110000000000011":
   WHEN 15=>columns<="110000000000011":
                 イロト イ御 トイヨ トイヨ トーヨー
```

```
WHEN OTHERS =>
           END CASE:
                                            张印
WHFN 11 =>
  CASE current row IS
    WHEN 0 =>columns<="11111111111111100000"; JHDI 源代码
    WHEN 1 =>columns<="1100000001111000":
    WHEN 2 =>columns<="1100000000001110"
    WHEN 3 = columns < "1100000000000111":
    WHEN 4 =>columns<="110000000000111":
    WHEN 5 =>columns<="110000000001110":
    WHEN 6 =>columns<="1100000001111000":
    WHEN 7 =>columns<="111111111111100000":
    WHEN 8 =>columns<="111111111111100000":
    WHEN 9 =>columns<="1100000001111000":
    WHEN 10=>columns<="1100000000001110":
    WHEN 11=>columns<="1100000000000111":
    WHEN 12=>columns<="110000000000111";
    WHEN 13=>columns<="110000000001110":
    WHEN 14=>columns<="1100000001111000":
    WHEN 15=>columns<="111111111111100000":
    WHEN OTHERS =>
                  4日 → 4周 → 4 三 → 4 三 → 9 Q P
```

```
END CASE:
                                                                                                                                                     叶剑飞、刘艳杰、苏维、
WHEN 12 =>
        CASE current row IS
               WHEN 0 =>columns<="00001111111110000";
               WHEN 1 =>columns<="0001110000011100" | WHEN 2 | WHEN 2 | WHEN 3 | 
              WHEN 2 =>columns<="0011100000001110":
               WHEN 3 =>columns<="011000000000111":***
               WHEN 4 \Rightarrow columns \leq "011000000000000":
              WHEN 5 =>columns<="110000000000000":
               WHEN 6 =>columns<="110000000000000":
               WHEN 7 =>columns<="1100000000000000":
              WHEN 8 =>columns<="1100000000000000":
               WHEN 9 =>columns<="1100000000000000":
               WHEN 10=>columns<="0110000000000000":
               WHEN 11=>columns<="0110000000000000":
               WHEN 12=>columns<="011000000000111":
               WHEN 13=>columns<="0011100000001110":
               WHEN 14=>columns<="0001110000011100":
               WHEN 15=>columns<="00001111111110000":
               WHEN OTHERS =>
                                         4 D > 4 A > 4 B > 4 B > B
```

```
END CASE:
                                           (仿真实现)
WHEN 13 \Rightarrow
                                         叶剑飞、刘艳杰、苏维、
  CASE current row IS
                                            张印
    WHEN 0 =>columns<="111110000000000";
    WHEN 1 =>columns<="11111111110000000";
    WHEN 2 =>columns<="1100000111000000" | WHEN 2 |
    WHEN 3 =>columns<="1100000001110000":
    WHEN 4 =>columns<="110000000011100":***
    WHEN 5 =>columns<="110000000000110":
    WHEN 6 =>columns<="110000000000011":
    WHEN 7 =>columns<="110000000000011":
    WHEN 8 =>columns<="1100000000000011":
    WHEN 9 =>columns<="1100000000000110":
    WHEN 10=>columns<="110000000000110":
    WHEN 11=>columns<="1100000000001100":
    WHEN 12=>columns<="1100000001111000":
    WHEN 13=>columns<="1100001111000000":
    WHEN 14=>columns<="11111111000000000":
    WHEN 15=>columns<="1111110000000000":
    WHEN OTHERS =>
           END CASE:
                  4日 → 4周 → 4 三 → 4 三 → 9 Q P
```

```
WHEN 14 =>
  CASE current row IS
    WHEN 0 =>columns<="1111111111111111"
   WHEN 1 =>columns<="1111111111111111":
    WHEN 2 =>columns<="110000000000000";
   WHEN 3 =>columns<="110000000000000"; HDI 源代码
   WHEN 4 =>columns<="110000000000000":
   WHEN 5 =>columns<="110000000000000":****
   WHEN 6 =>columns<="110000000000000":
   WHEN 7 =>columns<="1111111111111111":
   WHEN 8 =>columns<="1111111111111111":
   WHEN 9 =>columns<="110000000000000":
   WHEN 10=>columns<="110000000000000":
    WHEN 11=>columns<="110000000000000";
   WHEN 12=>columns<="1100000000000000":
   WHEN 13=>columns<="1100000000000000":
    WHEN 14=>columns<="1111111111111111":
   WHEN 15=>columns<="1111111111111111":
    WHEN OTHERS =>
           END CASE:
WHEN 15 =>
                  4日 → 4周 → 4 三 → 4 三 → 9 Q P
```

```
CASE current row IS
   WHEN 0 =>columns<="1111111111111111": 「何真实现」
   WHEN 1 =>columns<="11111111111111111": 中國飞、刘艳杰、苏维、
   WHEN 2 =>columns<="110000000000000";
   WHEN 3 =>columns<="110000000000000";
   WHEN 4 =>columns<="110000000000000" | WHEN 4 |
   WHEN 5 =>columns<="110000000000000":
   WHEN 6 =>columns<="110000000000000":***
   WHEN 7 =>columns<="11111111111111111":
   WHEN 8 =>columns<="1111111111111111":
   WHEN 9 =>columns<="110000000000000":
   WHEN 10=>columns<="1100000000000000":
   WHEN 11=>columns<="1100000000000000":
   WHEN 12=>columns<="110000000000000":
   WHEN 13=>columns<="1100000000000000":
   WHEN 14=>columns<="1100000000000000":
   WHEN 15=>columns<="110000000000000":
   WHEN OTHERS =>
          END CASE:
WHEN OTHERS =>
```

```
END CASE;
END IF;
END PROCESS;
```

#### END ARCHITECTURE showDigits;

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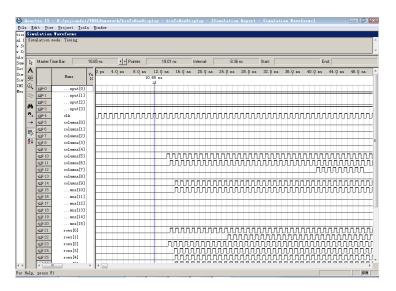
延日女仆

设计思路

VHDL 源代码 分音空现

示完毕

# 仿真实现



#### 点阵字符显示控制器设计 (仿真实现)

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題目要求

设计思路

仿真实现

寅示完与

# 演示完毕!

#### 点阵字符显示控制器设计 (仿真实现)

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设计思路

演示完毕