

Topics

- Instruction set architecture
- Assembly language programming
- Computer arithmetic
- Datapath and control
- Pipelining
- Interrupts, exceptions, process management
- I/O: memory-mapped, polling, interrupts
- Performance evaluation
- Logic design
- Etc.

RISC and CISC

- The two major schools of computer architecture design: RISC and CISC
- **RISC** = Reduced Instruction Set Computing/Computer
 - MIPS
 - ARM
 - Check out the websites of MIPS and ARM
- **CISC** = Complex Instruction Set Computing/Computer
 - Intel and AMD use CISC architecture.
- We'll use MIPS as our primary example.