

# SoC Robot Brain Board – FPGA

*System Design Innovation & Application Research Center*

- I. FPGA Device
- II. SoC Robot Project
- III. Compilation
- IV. FPGA Download



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and Bio/Health!



## FPGA Device

- **Company Name:** Altera
- **Device Family:** Cyclone IV **Device:** EP4CE75U19I7
- **Package:** 324Pins
- **LEs:** 75408
- **User I/O:** 293
- **Generated Simulation Language:** Verilog HDL

## FPGA PROM

- **Device Name:** EPCS64
- **Package:** 16pins SOIC
- **Configuration:** Serial

## Design Software

- **Altera Quartus II**

## Design Software

- **Altera USB Blaster**





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- SoC 로봇워 홈페이지 => 자료실
- AMAZON2 SoC 두뇌보드, FPGA 소스 첨부파일 다운로드

AMAZON2 Board, FPGA Source Code

자료실

작성자 최고관리자 (143.248.146.153) 16-12-28 14:38 조회 149회 댓글 0건

SoC Brain.zip (7.5M)

105회 다운로드 DATE : 2016-12-28 14:38:47

이전글

다음글

수정

삭제

복사

이동

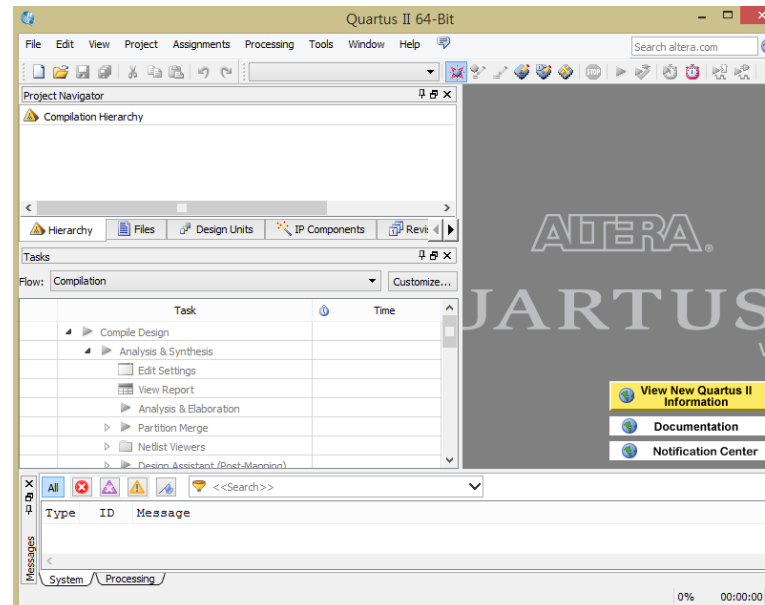
목록

답변

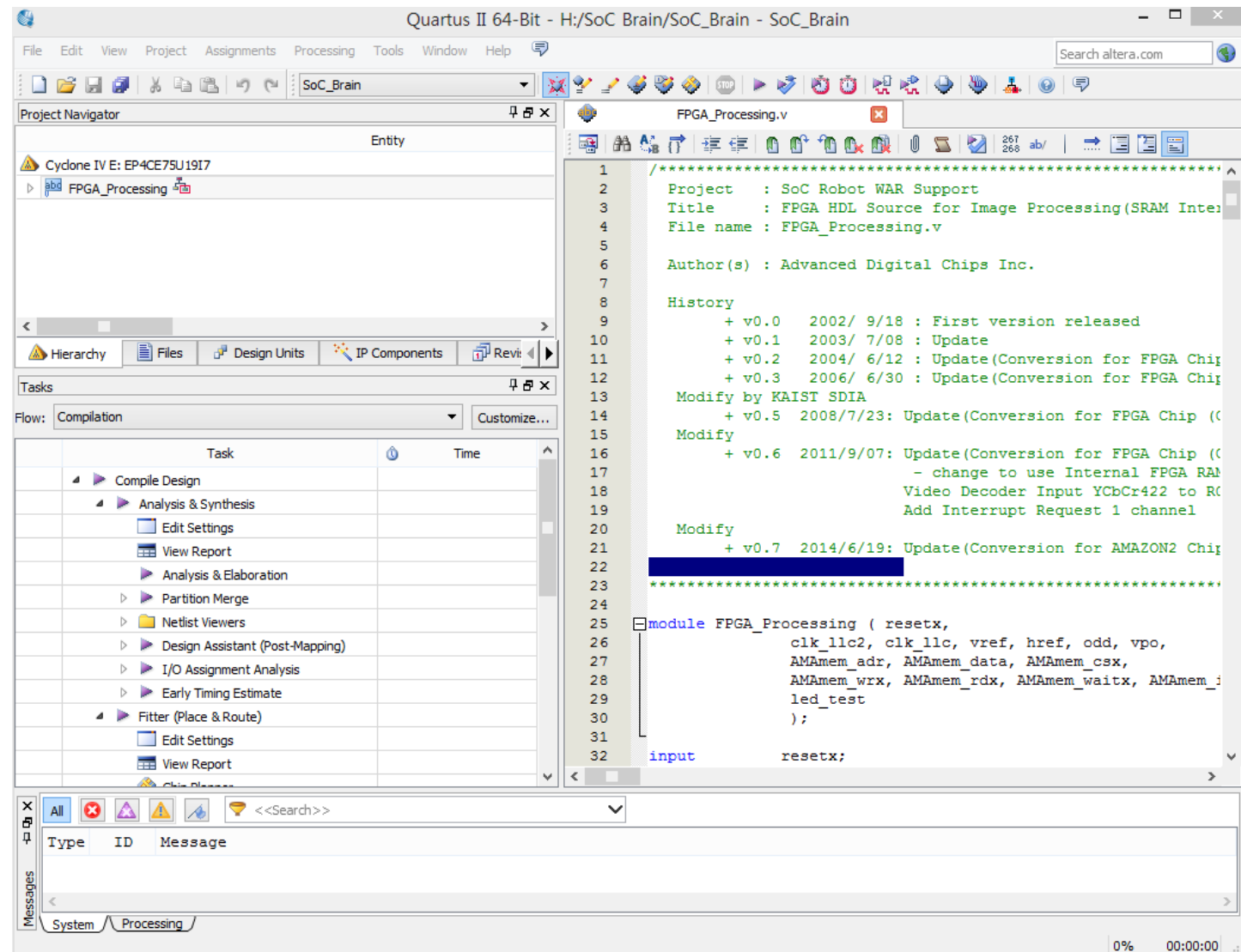
글쓰기

2016년 AMAZON2 Board, FPGA Source Code 입니다.  
Verilog로 작성되었습니다.

- SoC Brain.zip 압축풀기
- Quatus 실행



- 프로젝트 Open
  - => File => Open Project
  - => SoC\_Brain.qpf 파일 선택, Open



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ROBOTWAR  
Intelligent SoC Robot War

uxfactory

adc  
intel

ROBOTIS

MINI ROBOT  
(주)미니로봇

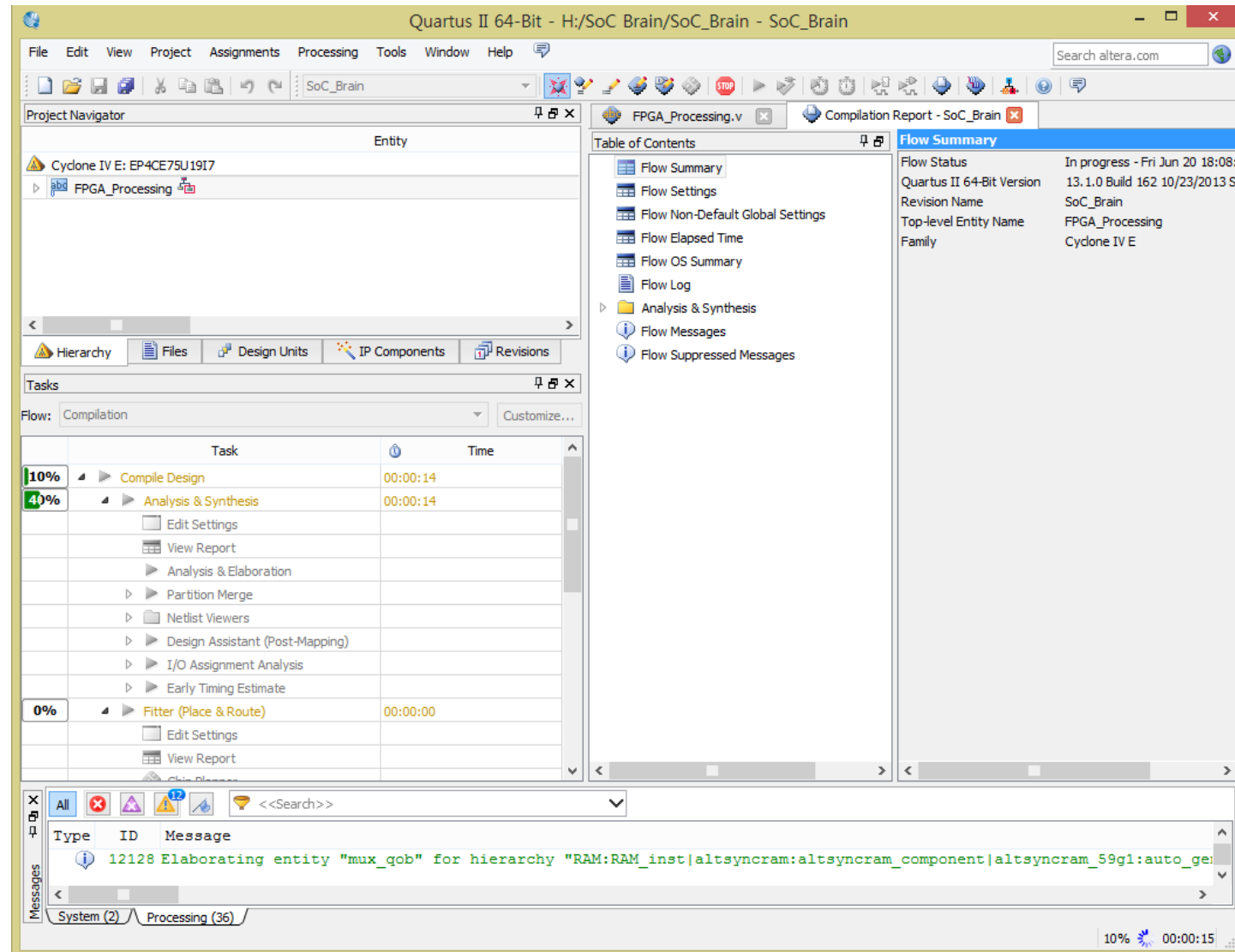
DSP Robot  
다이스티로봇

INTERBOARD

반도체설계교육센터  
IC DESIGN EDUCATION CENTER

KIRIA 한국로봇산업진흥원  
KOREA INSTITUTE FOR ROBOT INDUSTRY ADVANCEMENT

- Compile  
=> Processing => Start Compilation



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**ROBOTIS**

**MINI ROBOT**  
(주)미니로봇

**DSP Robot**  
다이스티로봇

**INTERBOARD**

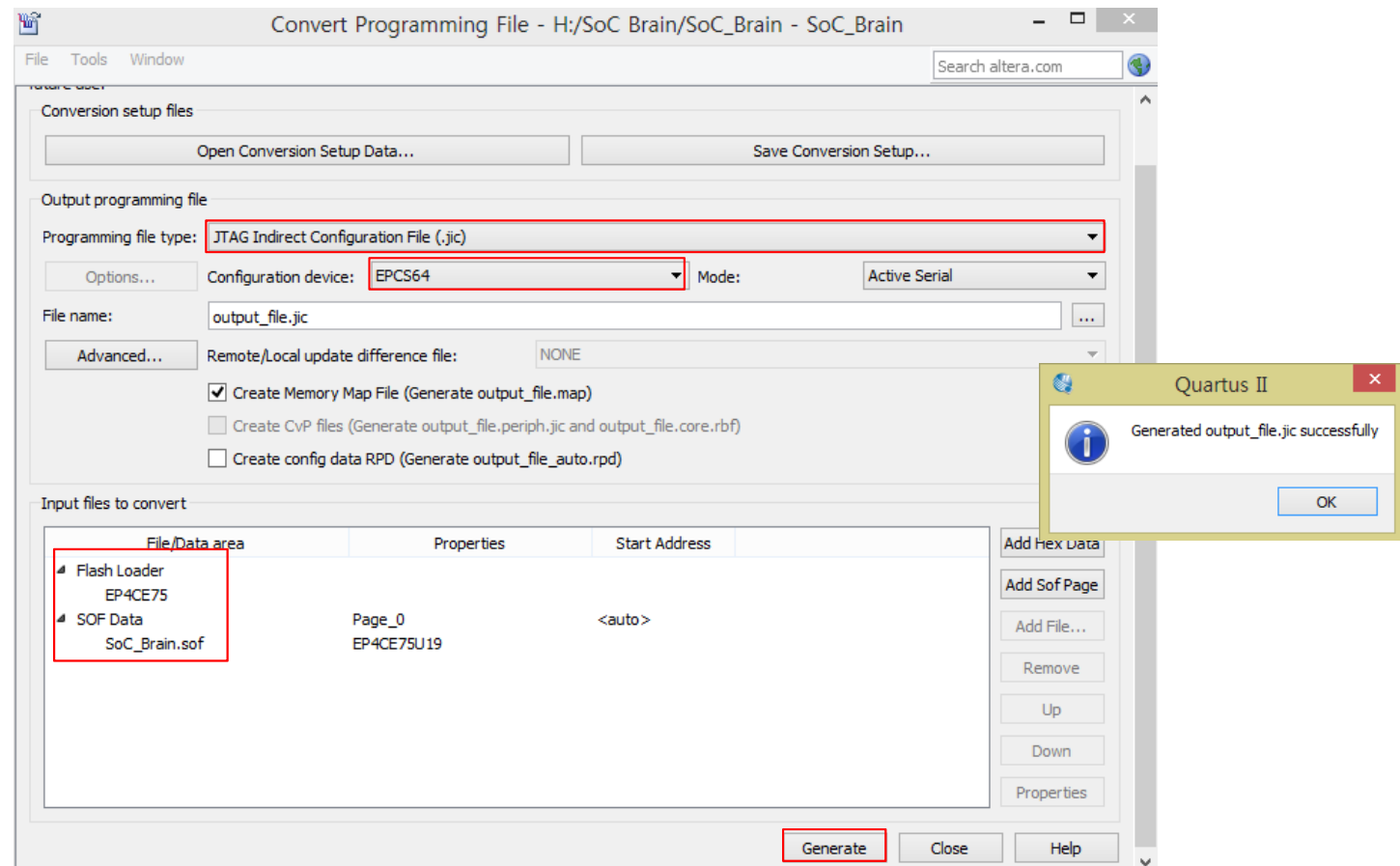
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IC DESIGN EDUCATION CENTER

**KIRIA** 한국로봇산업진흥원  
KOREA INSTITUTE FOR ROBOT INDUSTRY ADVANCEMENT

- JIC(JTAG indirect Configuration file) 파일 생성

=> File => Convert Programming Files

- Programming file type: JTAG Indirect Configuration File(.jic)
- Configuration device: EPCS64
- Flash Loader 선택 => Add Device... => Cyclone IV E / EP4CE75 체크
- SOF Data 선택 => Add File... => output\_files 폴더 선택 => SoC\_Brain.sof Open
- Generate 선택



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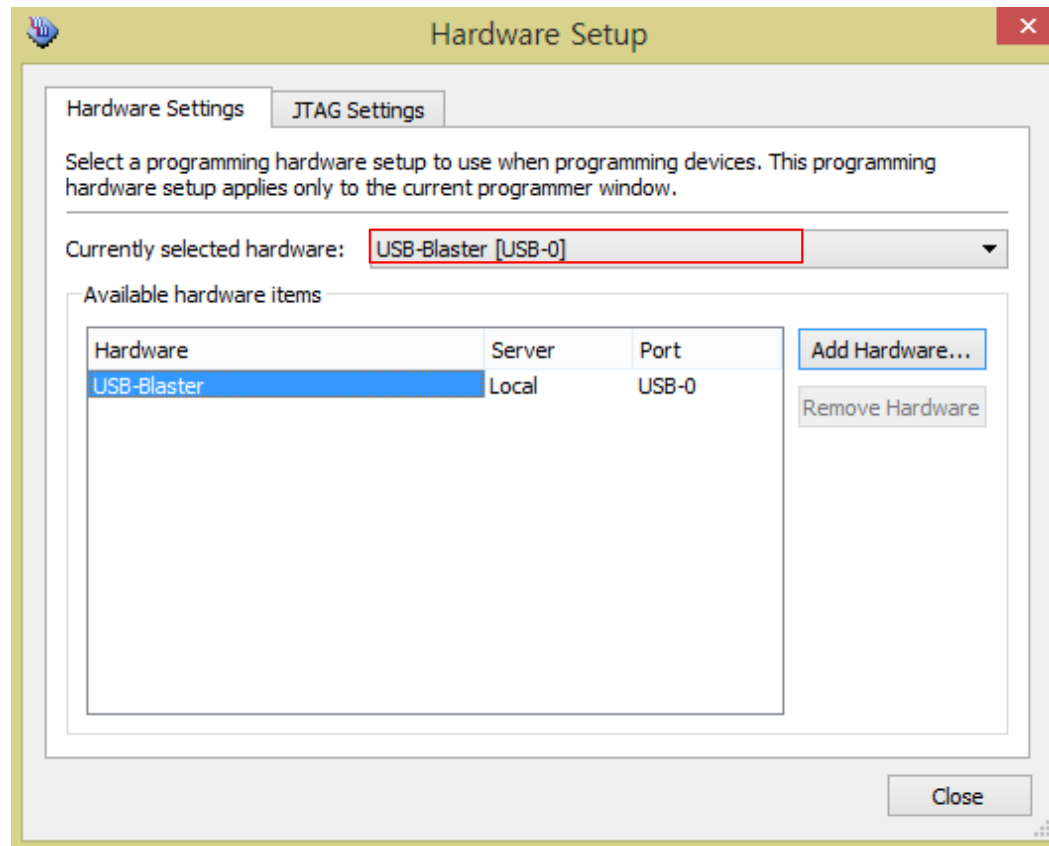


- FPGA Programming

=> tools => Programmer

- Hardware Setup

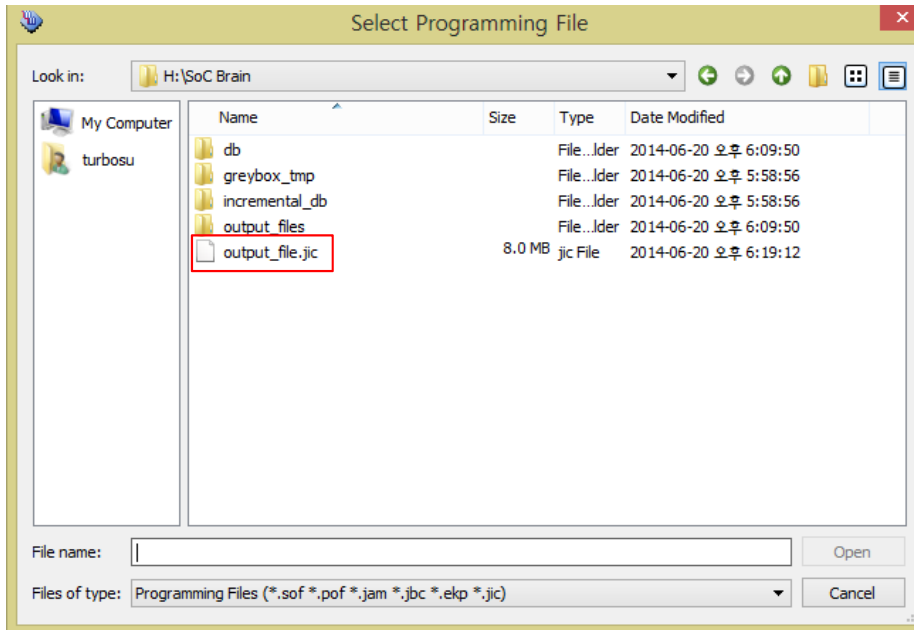
=> USB Blaster 연결 후 드라이버 설치 => USB-Blaster 선택





## • JIC 파일 추가

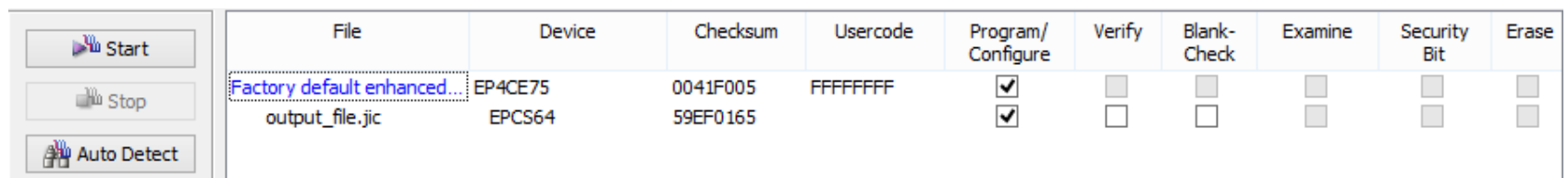
=> Add File... => Programmer => output\_file.jic 선택



## • Programming

=> Program/Configure 체크

=> SoC Brain Board 전원 ON => Start



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