SoC Robot Brain Board – FPGA

System Design Innovation & Application Research Center

I. FPGA Device
II. SoC Robot Project
III. Compilation
IV. FPGA Download

FPGA Device

- Company Name: Altera
- Device Family: Cyclone IV Device: EP4CE75U19I7
- Package: 324Pins
- LEs: 75408
- User I/O: 293
- Generated Simulation Language: Verilog HDL

FPGA PROM

- Device Name: EPCS64
- Package: 16pins SOIC
- Configuration: Serial

Design Software

- Altera Quartus II

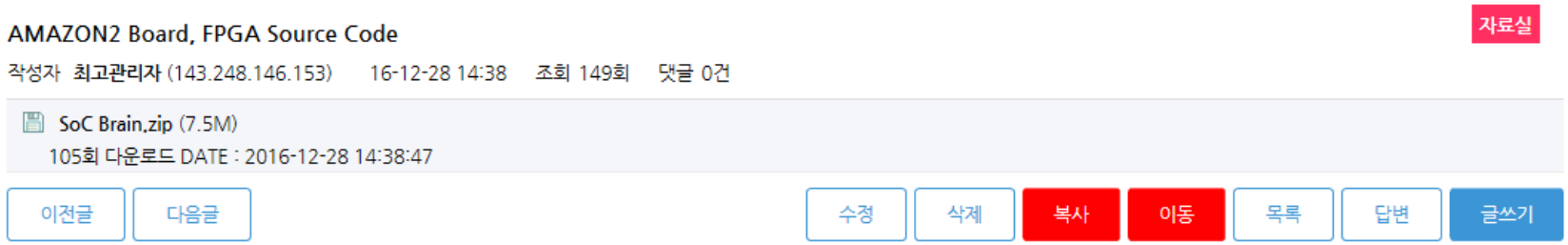
Design Software

- Altera USB Blaster

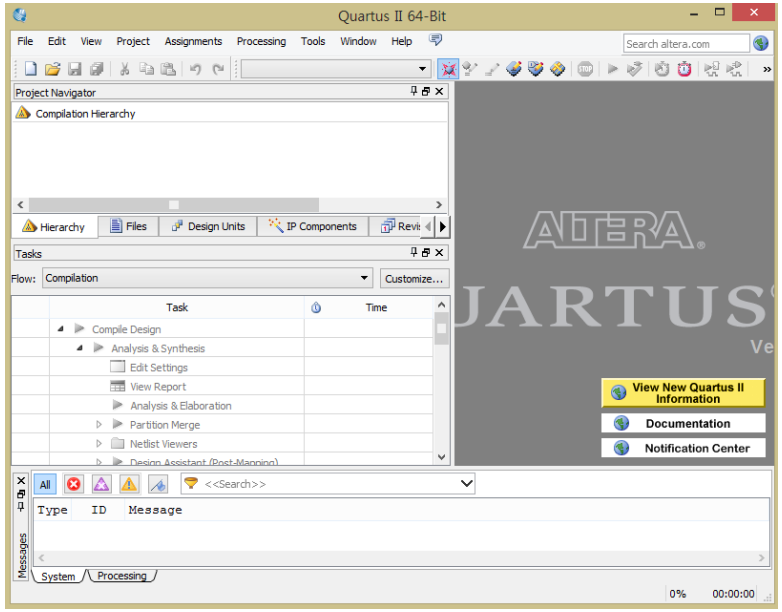
FPGA Code Download

Excellence in Intelligent Robot, Wearable Computer, and Bio/Health!

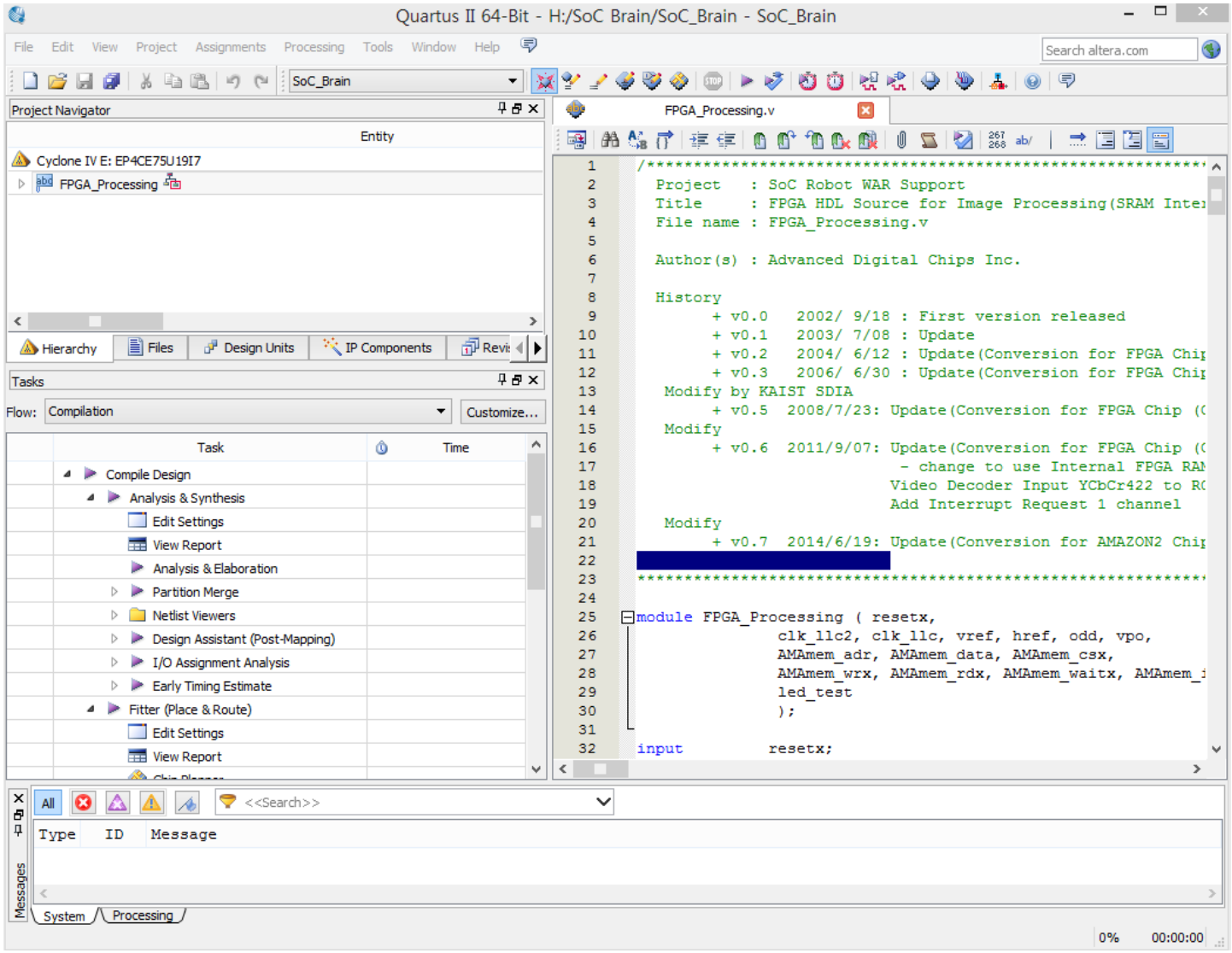
- SoC 로봇워 홈페이지 => 자료실
- AMAZON2 SoC 두뇌보드, FPGA 소스 첨부파일 다운로드



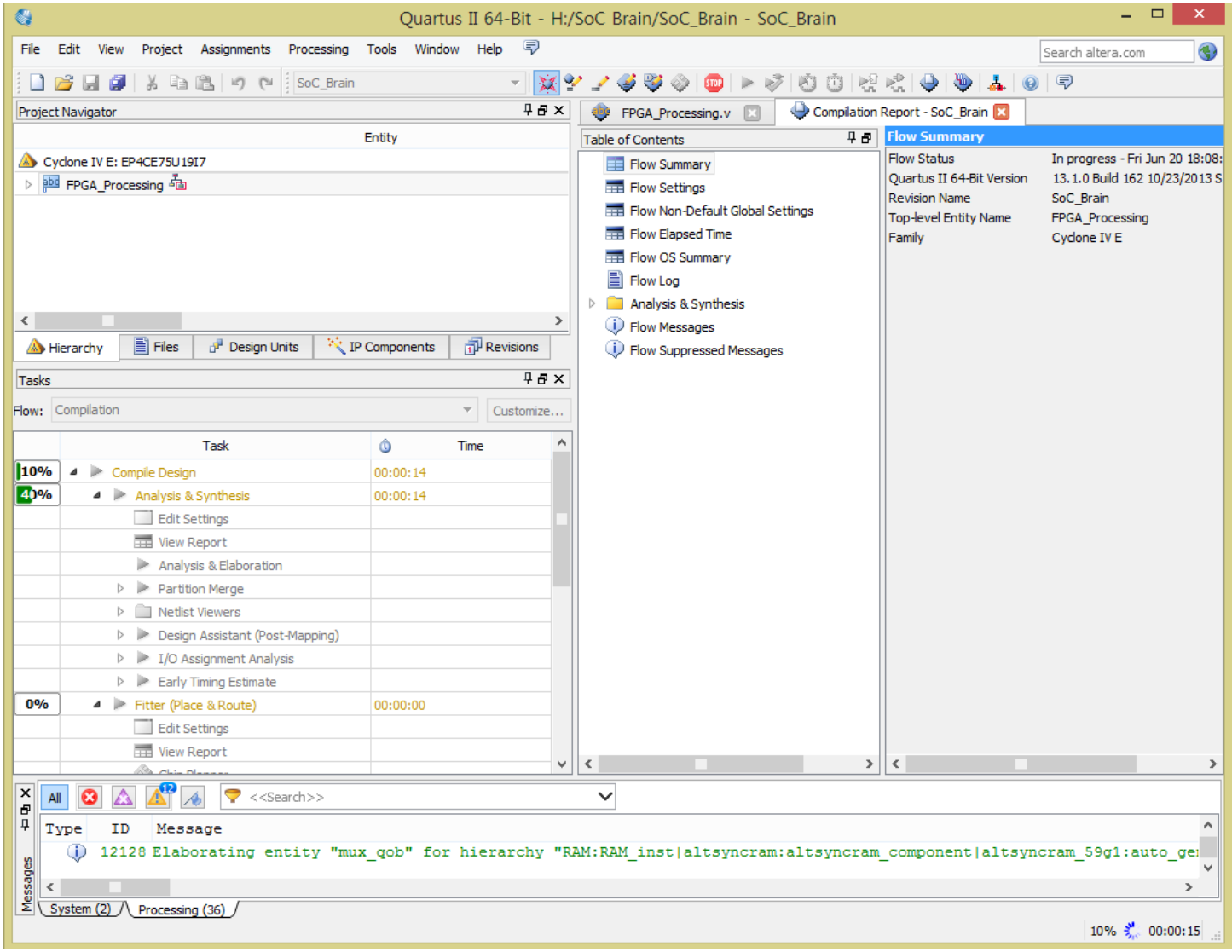
- SoC Brain.zip 압축풀기
- Quatus 실행



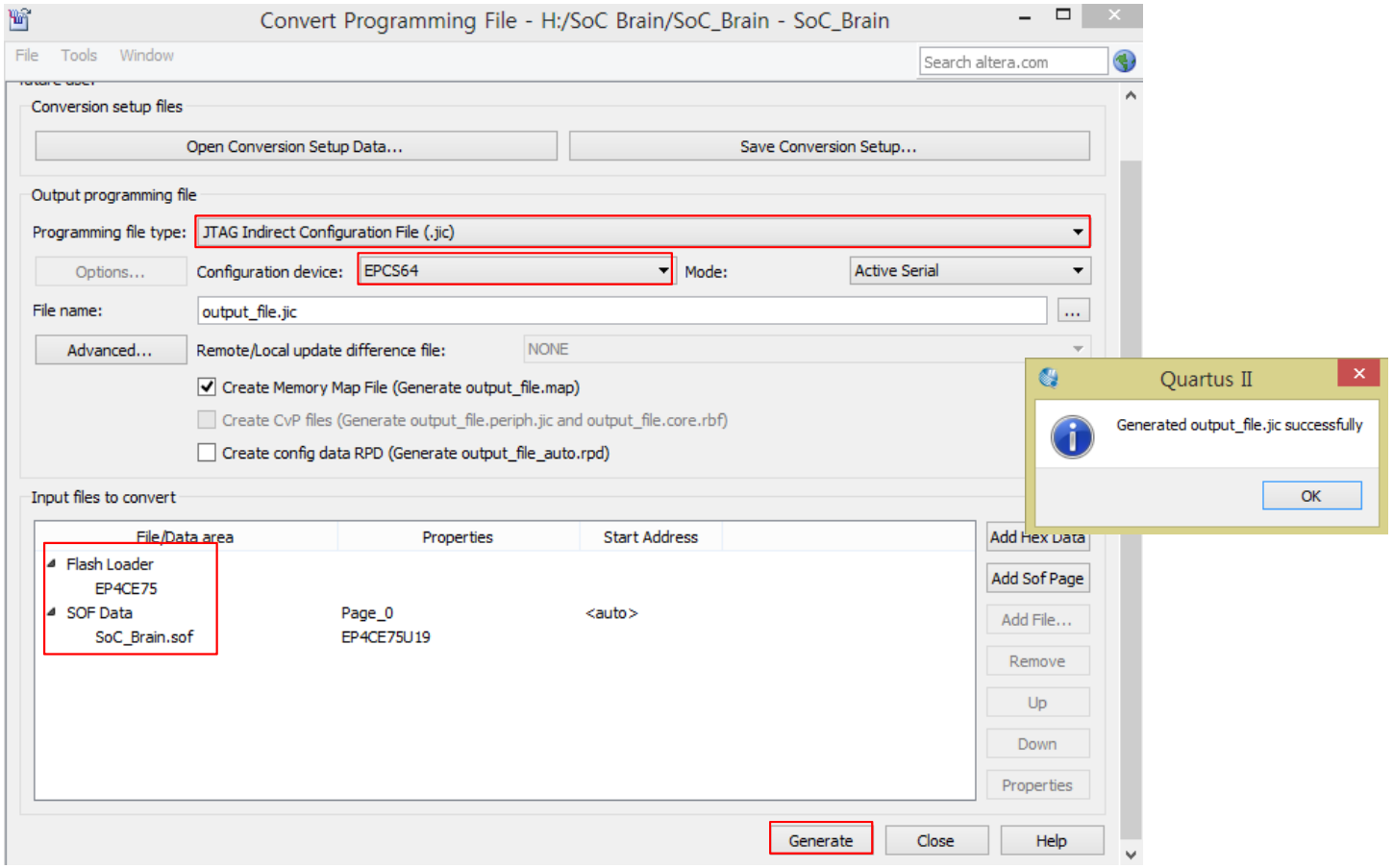
- **프로젝트 Open**
  => File => Open Project
  => SoC_Brain.qpf 파일 선택, Open



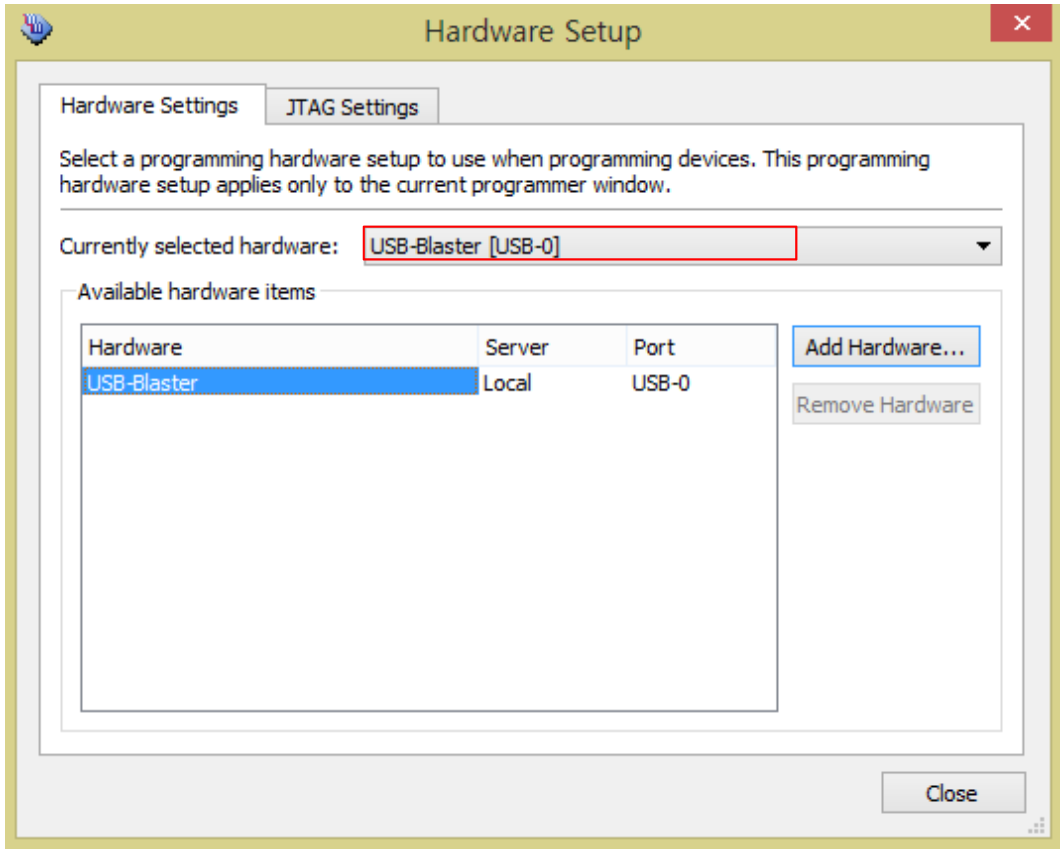
- **Compile**
  => Processing => Start Compilation



- JIC(JTAG indirect Configuration file 파일 생성
  => File => Convert Programming Files
  - Programming file type: JTAG Indirect Configuration File(.jic)
  - Configuration device: EPCS64
  - Flash Loader 선택 => Add Device… => Cyclone IV E / EP4CE75 체크
  - SOF Data 선택 => Add File… => output_files 폴더 선택 => SoC_Brain.sof Open
  - Generate 선택

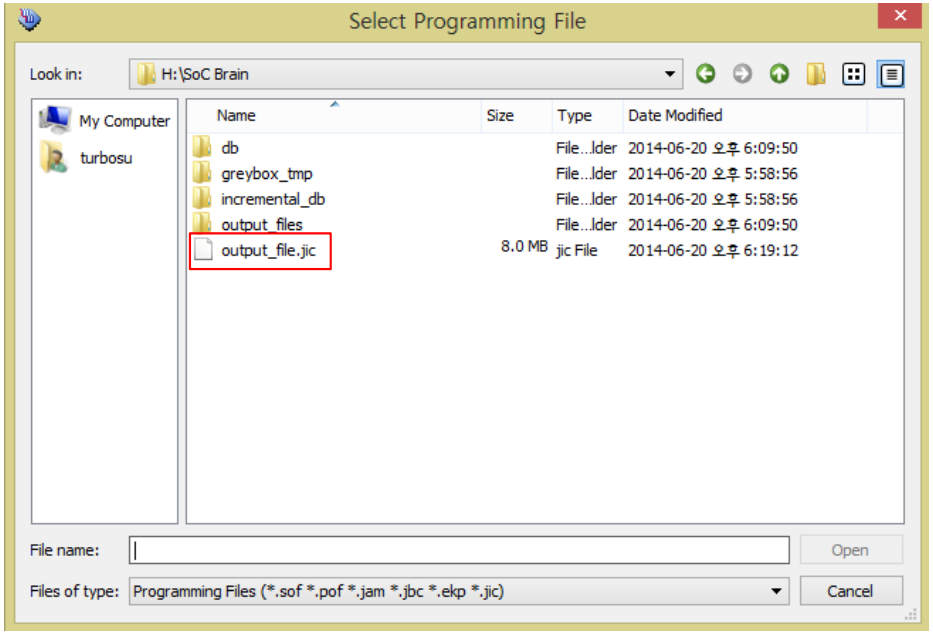


- **FPGA Programming**
  => tools => Programmer
- **Hardware Setup**
  => USB Blaster 연결 후 드라이버 설치 => USB-Blaster 선택



- JIC 파일 추가

  => Add File… => Programmer => output_file.jic 선택



- Programming

  => Program/Configure 체크

  => SoC Brain Board 전원 ON => Start