Curriculum Vitae

Personal Details

Name and Surname:
Place and Date of Birth:
Marital Status:
Citizenship and Identity Card Number:
Permanent Address:
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Education & Qualifications

2020-2025: completed M.sc degree in AI and robotics with excellence (Average 91.2) at the Technion Electrical and Computers' Engineering faculty.

2001-2006: completed B.sc computers engineering degree with excellence (Average 88.6) at the Technion Computers' Science faculty.

2019-2020: RL specialization (4 courses specialization) by The University of Alberta, Deep learning specialization by deeplearning.ai and Machine Learning by Stanford University on Coursera.

Work Experience

2018-Now - Intel Israel Haifa - SATG (SW Advanced Technologies Group) FLEX - Staff Senior SW engineer:

- Mobileye validation team enabled network stack in RT embedded environment.
- Intel Labs group POC of optical flow 3D feel for video conferencing over AWS services (EC2, Kubernetes). Designed, implemented and tested cloud C++ SW for uploading several cameras streams, trigger OF algo and stream to clients.
- Real Sense POC of RGB+DEPTH video streams over Ethernet using DDS protocols and standard (used by ROS2 topics) in Real Time embedded environment w/ FreeRTOS.

2015-2018 - Intel Israel Haifa - in the IPU HW simulator team as a Senior SW engineer on:

- Developed HW kernels models for simulating HW as SDK (for SW/FW development on top). Exceeded expectations, promoted and was rewarded on execution with urgency.
- Established new IPUSim infrastructure and MOW in the team.

2012-2015 - Intel Israel Haifa - working as a Real Time embedded FW engineer in the Skylake SOC Camera subsystem

- Developed FW controlling the numerous different image processing kernels in the pipe. And also, DSP w/ SIMD simple kernels.
- Worked with multiple sites as a virtual team to bring the code to production. Played a key role in training and ramping other engineers that joined the project.
- Integrated and debugged the FW on Windows OS environment (with the driver team) and root caused major blocking bugs.

2006-2012 – Zoran corporation – Worked as a Real Time embedded system (ThreadX RTOS) FW engineer at the Digital Still/Video Cameras FW group:

- Developed the codec (encoding and decoding) of Mpeg4/1 video standards for movie record/play modes.
- Implemented the algorithm for digital video stabilization (DVS).
- Lens distortion correction, image mix and image correction HW blocks drivers' development for several generations.

Military Service

1996-2000 – Tanks' Platoon commander in the IDF armored forces, Company commander's deputy. In the Army reserves as Captain, mortars' Platoon commander in between 2000-2014.

<u>Skills</u>

Languages: Hebrew – native, English – next to native,

Script & Programming Languages: C/C++11/14 - full control 17 trained but no experience (due to lack of support in past projects), Python. Also worked w/ C#, Java, Perl, Matlab, Simulink, Python ML packages like TensorFlow and Keras. Worked with OpenAI packages for RL studies assignments (Technion and Alberta course on Coursera).

Environments: Testing and running simulations in Unix/Linux/Windows OS environments.

Interests & Activities

Very interested and enthusiastic about deep learning (specializing in NN, CNN and RNN). Very eager to learn and improve. Would like to face new challenges to expand my knowledge and experience.

For recommendations I can give the names of my managers/colleges. Also, grades sheets of relevant courses can be provided.