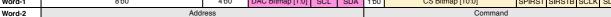
SFP+0 Words (32-bit) Forth byte [31:24] Third byte [23:16] Second byte [15:8] First byte [7:0] BCR BCID [11:0] Comma = 0xbc Word-0 0x0 1'b0 4'b0 DAC Bitmap [1:0] CS Bitmap [10:0] SPIRST SIRSTB SCLK SDI Word-1 8'b0 4'b0 1'b0



Data

SL Full board ID

FPGA TP pattern ID

Footer = 0x0e0d

8'b0

Word-3

Word-4