SFP+ 0

Words (32-bit)	Forth byte [31:24]	Third byte [23:16]	Second byte [15:8]	First byte [7:0]
Word-0	0x0 / Soft Reset / TPT / BCR / 1'b0	4'b0 / BCID [11:0]	Comma = 0xbc	
Word-1	8'b0	4'b0 / DAC Bitmap [1:0] / SCL / SDA	1'b0 / CS Bitmap [10:0]	SPIRST / SIRSTB / SCLK / SDI
Word-2	Address		Command	
Word-3	8'b0	FPGA TP pattern ID	Data	
Word-4	Footer = 0x0e0d		SL Full board ID	