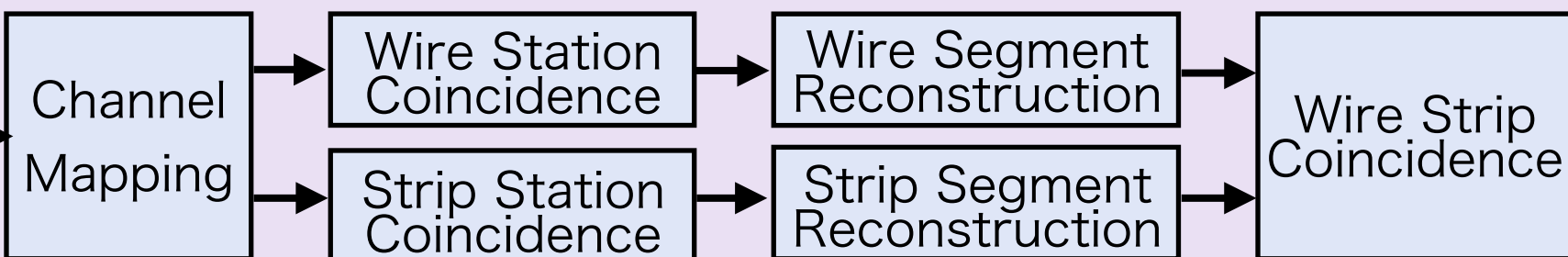


PS boards  
(Endcap  $\phi 0$ )

### TGC BW Coincidence



Readout

L0 Buffer

Derandomizer

Zero Suppressor  
& Serializer

SLR0

NSWTP  
Barrel SL  
Tile TDAQi  
TGC EI

Inner  
Coincidence

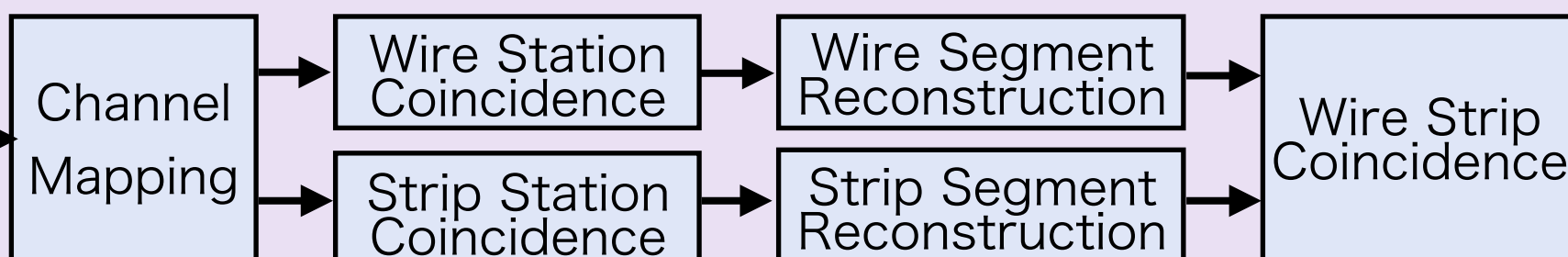
Track  
Selector

MDTTP

MUCTPI

SLR1

### TGC BW Coincidence



Readout

L0 Buffer

Derandomizer

Zero Suppressor  
& Serializer

SLR2

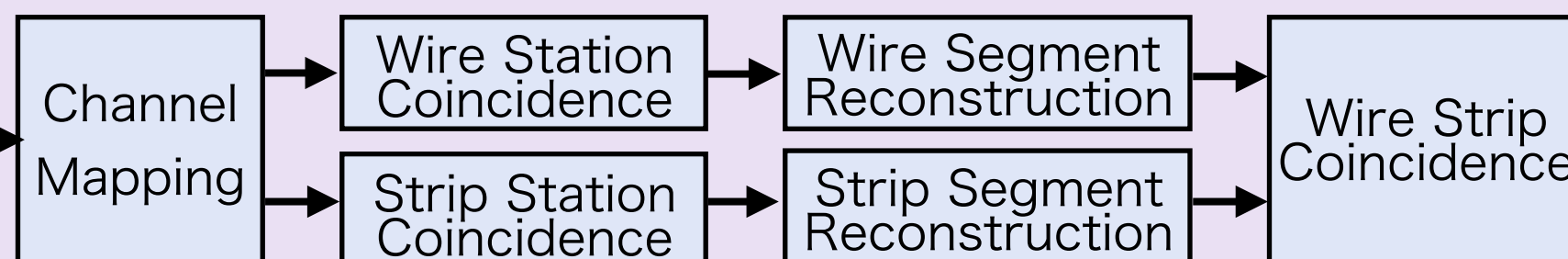
Control

AXI  
Translator

各SLRへ

MPSoC

### TGC BW Coincidence



Readout

L0 Buffer

Derandomizer

Zero Suppressor  
& Serializer

Event  
Builder

MUCTPI

SLR3

PS boards  
(Forward )