

```

<program> ==> <: <stmtList :>
==> <stmtList> ==> <stmt>
==> <degiskenler> ==> tam <tutac> .
==> tam x.
==> <stmtList> ==> <stmt>
==> <ata> ==> ata( <tutac> ve <expr> ).
==> ata(x ve <expr>).
==> ata(x ve <tamSayi>).
==> ata(x ve 10).
==> <stmtList> ==> <stmt>
==> <iseStmt> ==> ise( <logicExpr> ) <: <stmtList> :>
==> ise( <expr> < <expr> ) <: <stmtList> :>
==> ise( <tutac> < <expr> ) <: <stmtList> :>
==> ise( x < <expr> ) <: <stmtList> :>
==> ise( x < <tamSayi> ) <: <stmtList> :>
==> ise( x < 10 ) <: <stmtList> :>
==> ise( x < 10 ) <: <stmt> :>
==> ise( x < 10 ) <: <ata> :>
==> ise( x < 10 ) <: ata( <tutac> ve <expr> ). :>
==> ise( x < 10 ) <: ata( x ve <expr> ). :>
==> ise( x < 10 ) <: ata( x ve <tamSayi> ). :>
==> ise( x < 10 ) <: ata( x ve 7 ). :>
==> <yada> ==> yada( <logicExpr> ) <: <stmtList> :>
==> yada( <expr> == <expr> ) <: <stmtList> :>
==> yada( <tutac> == <expr> ) <: <stmtList> :>
==> yada( x == <expr> ) <: <stmtList> :>
==> yada( x == <tamSayi> ) <: <stmtList> :>
==> yada( x == 10 ) <: <stmtList> :>
==> yada( x == 10 ) <: <stmt> :>
==> yada( x == 10 ) <: <ata> :>
==> yada( x == 10 ) <: ata( <tutac> ve <expr> ). :>
==> yada( x == 10 ) <: ata( x ve <expr> ). :>
==> yada( x == 10 ) <: ata( x ve <tamSayi> ). :>
==> yada( x == 10 ) <: ata( x ve 4 ). :>
==> <degilse> ==> degilse <: <stmtList> :>
==> degilse <: <stmt> :>
==> degilse <: <ata> :>
==> degilse <: ata( <tutac> ve <expr> ). :>
==> degilse <: ata( x ve <expr> ). :>
==> degilse <: ata( x ve <tamSayi> ). :>
==> degilse <: ata( x ve 1 ). :>

```