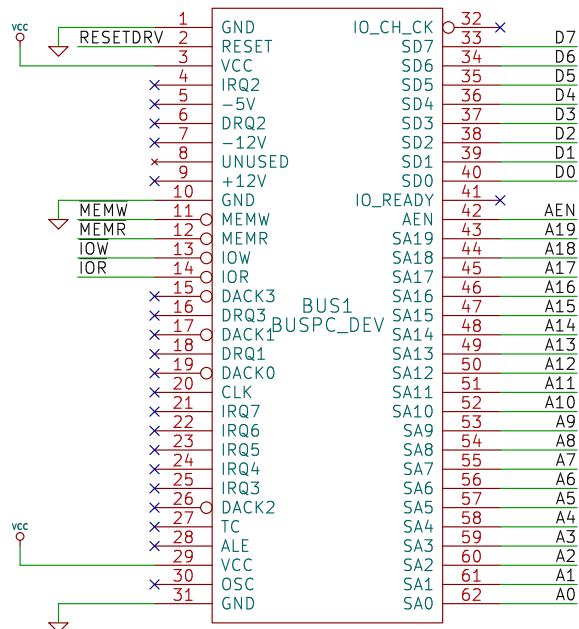
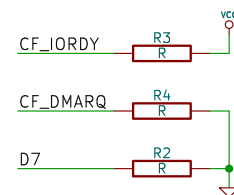
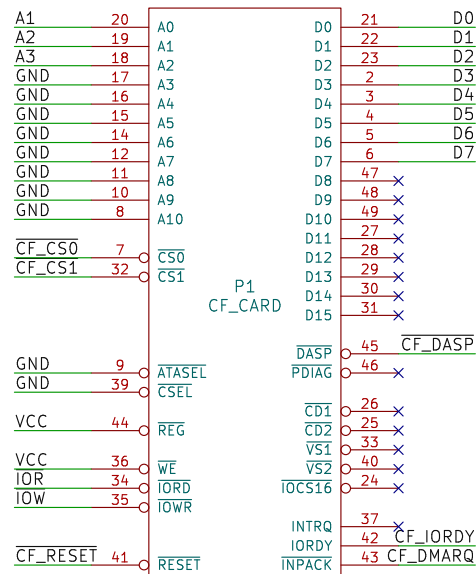


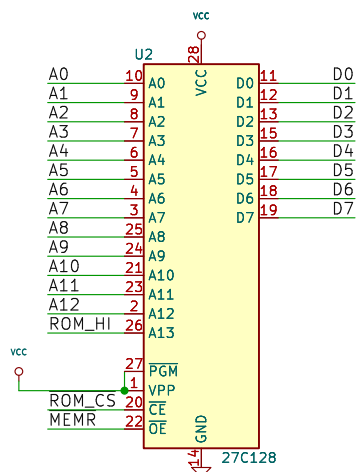
## ISA Bus



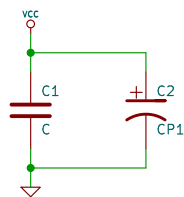
## Compact Flash Interface



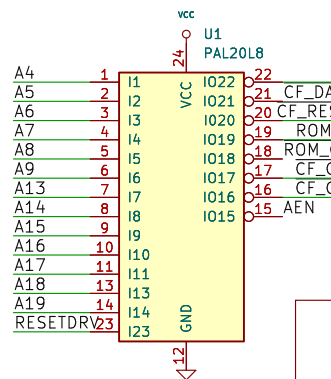
## EPROM (optional)



## Bypass Capacitors



## I/O Ports and ADDRESSES Chip Select Logic



Based on <https://github.com/skiselev/xt-cf-lite-v4> from Sergey Kiselev

Sheet: /

File: xt-cf.kicad\_sch

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Size: A4 Date: 25 mar 2025

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Rev:

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