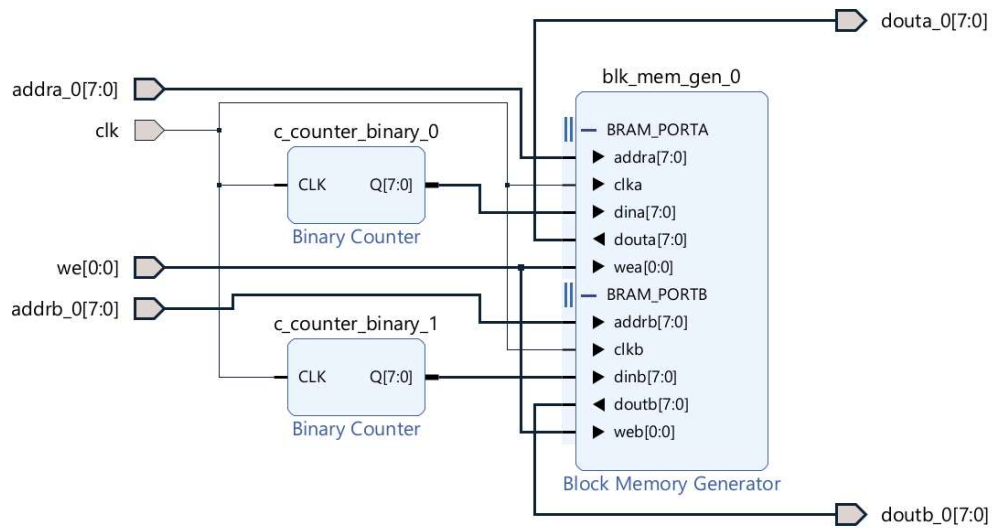


Assignment 22: Design Dual Port RAM using Block Memory Generator IP

Block Diagram



Behavioral Simulation

