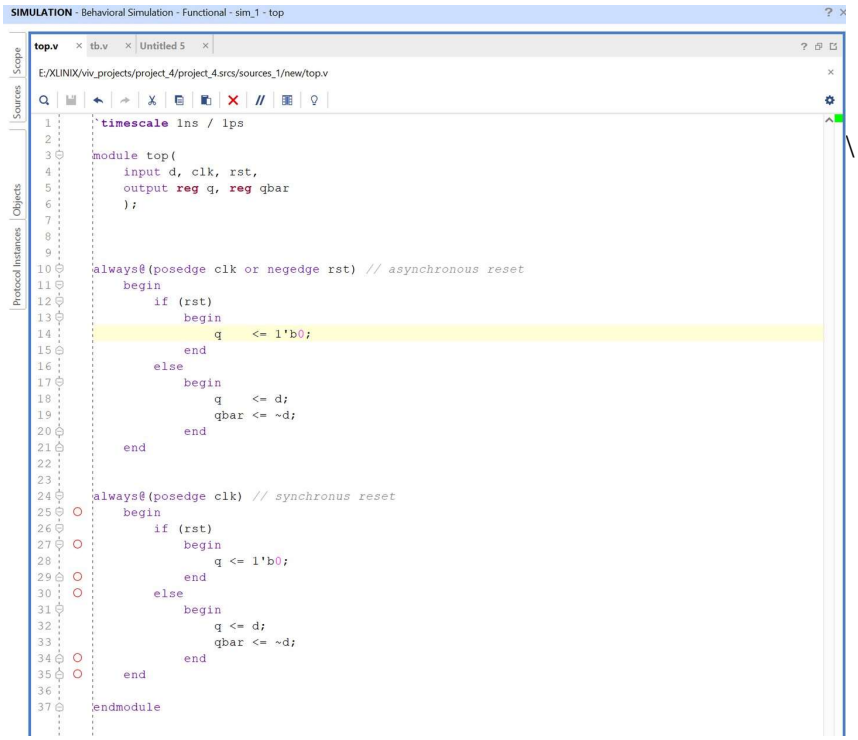
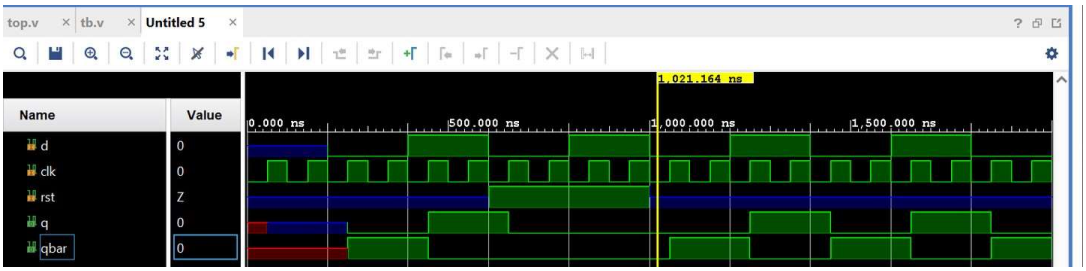
Assignment_11:

Implement D-Flipflop with Synchronous and Asynchronous Reset. Assume both True and complemented output pins are available in the D-Flipflop and it should send a value of '0' on the true output pin when the user applies Active High reset onto Flipflop.

code



Synchronous simulation



asynchronos simulation