

Cycle	1	2	3	4	5	6	7	8	9	10	11	12	13
Backend Stall													
Alloc Slot 0													
Alloc Slot 1													
Alloc Slot 2													
Frontend Bound													
Backend Bound													
Retiring													
Bad Speculation													

_____ Bad Speculation times _____ Frontend Bound times

_____ Retiring times _____ Backend Bound times

Pdst	instruction	R1	R2	R3	addr	data	src1	src2	T alloc		T src1 ready	T src2 ready	T exe	T addr ready	T data ready	block code	T commit
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	

