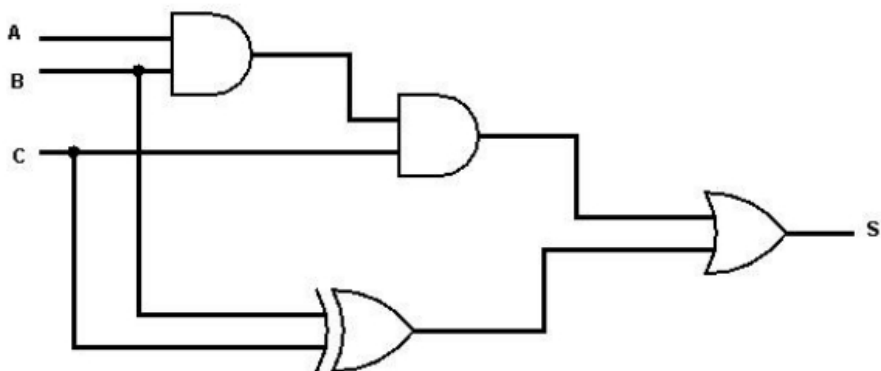
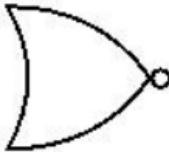
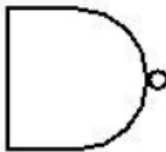


Question	Answer	Marks
5(a)	<p>1 mark per bullet point to max 2</p> <ul style="list-style-type: none"> Definition: Microprocessor/microcontroller within a larger system // microprocessor/microcontroller that performs one specific task Example: e.g. Embedded system in washing machine only controls the programs for the washing cycle // it is part of the washing machine but does not perform any other function within it 	2
5(b)	<p>1 mark for RAM, 1 mark for ROM</p> <p>RAM:</p> <ul style="list-style-type: none"> Store the choices/wash program the user has entered // stores the data read from the sensors // stores the time left in the program // by example <p>ROM:</p> <ul style="list-style-type: none"> Store the start-up instructions (for the washing cycles) 	2
5(c)	<p>1 mark per bullet point</p> <ul style="list-style-type: none"> The system uses feedback The system causes the temperature to change // produces an action 	2

Question	Answer						Marks
8	1 mark per correct row						3
	Statement	AND	NAND	NOR	XOR	OR	
	The output is 1 only when both inputs are 1	✓					
	The output is 1 only when both inputs are different				✓		
	The output is 1 only when both inputs are 0			✓			

Question	Answer	Marks																																													
3(a)	<p>1 mark for each correct gate, with correct inputs</p> 	4																																													
3(b)	<p>1 mark for each half (shaded)</p> <table><tr><th>A</th><th>B</th><th>C</th><th>Working space</th><th>S</th></tr><tr><td>0</td><td>0</td><td>0</td><td></td><td>0</td></tr><tr><td>0</td><td>0</td><td>1</td><td></td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td><td></td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td><td></td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td><td></td><td>0</td></tr><tr><td>1</td><td>0</td><td>1</td><td></td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td><td></td><td>1</td></tr><tr><td>1</td><td>1</td><td>1</td><td></td><td>1</td></tr></table>	A	B	C	Working space	S	0	0	0		0	0	0	1		1	0	1	0		1	0	1	1		0	1	0	0		0	1	0	1		1	1	1	0		1	1	1	1		1	2
A	B	C	Working space	S																																											
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Question	Answer	Marks
3(a)	<p>1 mark per bullet point</p> <ul style="list-style-type: none"> • A AND B ... • ... XOR C ... • ... OR NOT B <p>((A AND B) XOR C) OR NOT B</p>	3

Question	Answer	Marks																																													
3(b)	<p>1 mark for each set of 4 rows (shaded)</p> <table><tr><th>A</th><th>B</th><th>C</th><th>Working space</th><th>X</th></tr><tr><td>0</td><td>0</td><td>0</td><td></td><td>1</td></tr><tr><td>0</td><td>0</td><td>1</td><td></td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td><td></td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td><td></td><td>1</td></tr><tr><td>1</td><td>0</td><td>0</td><td></td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td><td></td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td><td></td><td>1</td></tr><tr><td>1</td><td>1</td><td>1</td><td></td><td>0</td></tr></table>	A	B	C	Working space	X	0	0	0		1	0	0	1		1	0	1	0		0	0	1	1		1	1	0	0		1	1	0	1		1	1	1	0		1	1	1	1		0	2
A	B	C	Working space	X																																											
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3(c)	<p>1 mark for gate, 1 mark for matching symbol, 1 mark for matching truth table</p> <p>NOR</p> <div></div> <table><tr><th>A</th><th>B</th><th>OUTPUT</th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table> <p>NAND</p> <div></div> <table><tr><th>A</th><th>B</th><th>OUTPUT</th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	A	B	OUTPUT	0	0	1	0	1	0	1	0	0	1	1	0	A	B	OUTPUT	0	0	1	0	1	1	1	0	1	1	1	0	3															
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Question	Answer	Marks
2(a)(i)	<p>1 mark for each correct term.</p> <p>Random Access Memory (RAM) and Read Only Memory (ROM) are both examples of primary memory.</p> <p>One item that is stored in RAM is currently running software/data/part of OS.</p> <p>One item that is stored in ROM is the start-up/boot-up instructions/BIOS.</p> <p>RAM can be either Static RAM (SRAM) or Dynamic RAM (DRAM). SRAM uses transistors arranged as flip-flops/latches. DRAM uses transistors and capacitors.</p>	5
2(a)(ii)	<p>1 mark per bullet point to max 3</p> <ul style="list-style-type: none"> PROM can be set once, EPROM and EEPROM can be overwritten multiple times. EPROM needs to be removed from device EEPROM can be erased in situ. EPROM and can be erased using UV light, EEPROM can be erased using voltage // is flash storage . EPROM must be entirely erased before rewriting, EEPROM does not have to be entirely erased before rewriting. 	3

Question	Answer	Marks															
2(b)	<p>1 mark per bullet point to max 5</p> <ul style="list-style-type: none"> • The hard disk has (one or more) platter/plate/disk • Each surface of the platter/disk is (ferrous oxide which is) capable of being magnetised • The platters/disks are mounted on a (central) spindle • The entire mechanism is contained inside a sealed (aluminium) box. • The disks are rotated (at high-speed) • (Each surface of the disk) has a read/write head mounted on an arm (positioned just above the surface) • Electronic circuits control the movement of the arm (and hence the heads) • The surface of the platter/disk is divided into <u>concentric</u> tracks / circles • The surface of the platter/disk is divided into sectors • One track in one sector is the basic unit of storage called a block • The data is encoded as a magnetic pattern for each block • When writing to disk, a variation in the current in the head produces a variation in magnetic field on the disk • When reading from disk, a variation in magnetic field produces a variation in current through the head 	5															
2(c)(i)	<p>1 mark for each correct row.</p> <table border="1"> <thead> <tr> <th>Gate</th><th>Input 1</th><th>Input 2</th></tr> </thead> <tbody> <tr> <td>AND</td><td>1</td><td>1</td></tr> <tr> <td>NAND</td><td>0 0 1</td><td>0 1 0</td></tr> <tr> <td>XOR</td><td>0 1</td><td>1 0</td></tr> <tr> <td>NOR</td><td>0</td><td>0</td></tr> </tbody> </table>	Gate	Input 1	Input 2	AND	1	1	NAND	0 0 1	0 1 0	XOR	0 1	1 0	NOR	0	0	4
Gate	Input 1	Input 2															
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2(c)(ii)	<p>1 mark per bullet point</p> <ul style="list-style-type: none"> • A NAND B • B XOR C • OR <p>(A NAND B) OR (B XOR C)</p>	3															

Question	Answer	Marks																																													
4(a)	<p>1 mark per pair of outputs (shaded)</p> <table><tr><th>A</th><th>B</th><th>C</th><th>Working space</th><th>X</th></tr><tr><td>0</td><td>0</td><td>0</td><td></td><td>0</td></tr><tr><td>0</td><td>0</td><td>1</td><td></td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td><td></td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td><td></td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td><td></td><td>0</td></tr><tr><td>1</td><td>0</td><td>1</td><td></td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td><td></td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td><td></td><td>0</td></tr></table>	A	B	C	Working space	X	0	0	0		0	0	0	1		1	0	1	0		0	0	1	1		0	1	0	0		0	1	0	1		1	1	1	0		0	1	1	1		0	4
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4(b)	<p>1 mark for all three gates:</p> <p>OR XOR NOT</p>	1																																													



Question	Answer	Marks
4(a)	<p>One mark per bullet point to max 2</p> <p>E.g.</p> <ul style="list-style-type: none"> • Optical Disc Drive // CD/DVD Drive/Writer • Solid State Drive // USB Flash drive 	2

Question	Answer	Marks																						
4(b)	<p>One mark per bullet point to max 4</p> <ul style="list-style-type: none">• Main component of a scanner is a CCD (Charge Couple Device) array• CCD is a collection of light sensitive diodes• Laser beam / light shines onto the source document/barcode• The scanned image reaches the CCD through mirrors and lenses• Sensors detect levels of reflected light• Brighter light results in greater electrical charge• Light intensity is converted (by software) to a digital value	4																						
4(c)	<p>One mark for correct lines from each type of RAM</p> <table><thead><tr><th>Type of RAM</th><th>Description</th></tr></thead><tbody><tr><td>SRAM</td><td>Is less expensive to manufacture</td></tr><tr><td>SRAM</td><td>Needs to be refreshed</td></tr><tr><td>SRAM</td><td>Has more complex circuitry</td></tr><tr><td>SRAM</td><td>Is often used as cache</td></tr><tr><td>SRAM</td><td>Has faster access time</td></tr><tr><td>DRAM</td><td>Is less expensive to manufacture</td></tr><tr><td>DRAM</td><td>Needs to be refreshed</td></tr><tr><td>DRAM</td><td>Has more complex circuitry</td></tr><tr><td>DRAM</td><td>Is often used as cache</td></tr><tr><td>DRAM</td><td>Has faster access time</td></tr></tbody></table>	Type of RAM	Description	SRAM	Is less expensive to manufacture	SRAM	Needs to be refreshed	SRAM	Has more complex circuitry	SRAM	Is often used as cache	SRAM	Has faster access time	DRAM	Is less expensive to manufacture	DRAM	Needs to be refreshed	DRAM	Has more complex circuitry	DRAM	Is often used as cache	DRAM	Has faster access time	2
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Question	Answer	Marks															
2(a)	<p>1 mark for first three rows, 1 mark for the touchscreen being both.</p> <table border="1" data-bbox="300 1014 887 1344"> <thead> <tr> <th data-bbox="300 1014 550 1077">Device</th><th data-bbox="555 1014 715 1077">Input</th><th data-bbox="719 1014 887 1077">Output</th></tr> </thead> <tbody> <tr> <td data-bbox="300 1084 550 1146">LCD Monitor</td><td data-bbox="555 1084 715 1146"></td><td data-bbox="719 1084 887 1146">✓</td></tr> <tr> <td data-bbox="300 1153 550 1216">Microphone</td><td data-bbox="555 1153 715 1216">✓</td><td data-bbox="719 1153 887 1216"></td></tr> <tr> <td data-bbox="300 1223 550 1285">Keyboard</td><td data-bbox="555 1223 715 1285">✓</td><td data-bbox="719 1223 887 1285"></td></tr> <tr> <td data-bbox="300 1292 550 1344">Touchscreen</td><td data-bbox="555 1292 715 1344">✓</td><td data-bbox="719 1292 887 1344">✓</td></tr> </tbody> </table>	Device	Input	Output	LCD Monitor		✓	Microphone	✓		Keyboard	✓		Touchscreen	✓	✓	2
Device	Input	Output															
LCD Monitor		✓															
Microphone	✓																
Keyboard	✓																
Touchscreen	✓	✓															

Question	Answer	Marks
2(b)(i)	<p>1 mark for 1 correct entry 2 marks for 2 correct entries 3 marks for 3 correct entries 4 marks for 5 correct entries</p> <ol style="list-style-type: none"> The object is designed using Computer Aided Design (CAD) software C (The software splits the object into slices) E (The data about the slices is sent to the printer) The solid plastic is melted and transferred to the nozzle A (A stepper motor moves the nozzle into position) D (The nozzle extrudes the molten plastic) The steps 5 to 6 repeat until the layer is complete B (A fan cools the layer) The steps 4 to 8 are repeated for each subsequent layer 	4
2(b)(ii)	<p>1 mark per bullet point. Max 3 for RAM, max 2 for ROM</p> <p>RAM</p> <ul style="list-style-type: none"> Stores currently running parts of the 3D printer software Stores the data about the layers being printed // contents of buffer Stores current progress of printing Stores the data about the printer, e.g. Plastic levels, nozzle position <p>ROM</p> <ul style="list-style-type: none"> Stores the operating software for the 3D printer // OS for the 3D printer Stores the boot-up/start-up instructions for the 3D printer 	4

Question	Answer	Marks																																													
5(a)	<p>1 mark for each pair of correct answers (shaded)</p> <table><tr><th>A</th><th>B</th><th>C</th><th>Working space</th><th>X</th></tr><tr><td>0</td><td>0</td><td>0</td><td></td><td>0</td></tr><tr><td>0</td><td>0</td><td>1</td><td></td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td><td></td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td><td></td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td><td></td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td><td></td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td><td></td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td><td></td><td>0</td></tr></table>	A	B	C	Working space	X	0	0	0		0	0	0	1		1	0	1	0		1	0	1	1		0	1	0	0		1	1	0	1		1	1	1	0		0	1	1	1		0	4
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Question	Answer	Marks																																		
5(b)	<p>1 mark for name, 1 mark for symbol, 1 mark for truth table</p> <ul style="list-style-type: none"> NAND  <table border="1"> <thead> <tr> <th colspan="2">Input</th><th rowspan="2">Output</th></tr> <tr> <th>A</th><th>B</th></tr> </thead> <tbody> <tr> <td>0</td><td>0</td><td>1</td></tr> <tr> <td>0</td><td>1</td><td>1</td></tr> <tr> <td>1</td><td>0</td><td>1</td></tr> <tr> <td>1</td><td>1</td><td>0</td></tr> </tbody> </table> <ul style="list-style-type: none"> NOR  <table border="1"> <thead> <tr> <th colspan="2">Input</th><th rowspan="2">Output</th></tr> <tr> <th>A</th><th>B</th></tr> </thead> <tbody> <tr> <td>0</td><td>0</td><td>1</td></tr> <tr> <td>0</td><td>1</td><td>0</td></tr> <tr> <td>1</td><td>0</td><td>0</td></tr> <tr> <td>1</td><td>1</td><td>0</td></tr> </tbody> </table>	Input		Output	A	B	0	0	1	0	1	1	1	0	1	1	1	0	Input		Output	A	B	0	0	1	0	1	0	1	0	0	1	1	0	3
Input		Output																																		
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