# Yogendra Rajpoot

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#### **EDUCATION**

#### SHRI MATA VAISHNO DEVI UNI-VERSITY

BTECH IN ELECTRONICS & COMMUNICATION

Aug 2020-24 Katra, J&K, INDIA CGPA: 6.72

#### M.M.D.A.INTER COLLEGE

12th-High school. May 2019 UP, India 78%

### LINKS

Codestudio://yogendra Geeksforgeeks: yogendra Leetcode: yogendra

#### **SKILLS**

#### **PROGRAMMING**

C++ (DSA) • IoT •

#### TOOLS/PLATFORMS

• html css • SQL Git • Microprocessor • VLSI • Verilog

### **ROLES AND**

### RESPONSIBILITY

# FRONT END DEVELOPER GDSC SMVDU

Designed login and sign up pages for website

Responsible for smooth conduct of gdsc session

#### **EXPERIENCE**

# SHRI MATA VAISHNO DEVI UNIVERSITY | VLSI INTERN Exp Cert

July 2022 - August 2022 | SMVDU, JK, India

- Researched and worked on Invertor, AND , OR logic gates etc , and created reports on research activities.
- analysed power consumption and delay of the MOS gates

## **PROJECT**

# IOT-BASED-ULTRASONIC-RADAR-SYSTEM | IOT | AURDINO | ULTRASONIC SENSOR | SERVO MOTOR | VISUAL SOFTWARE SKILLSLINK

- project utilized an ultrasonic sensor to capture real-time distance data, which was then processed to control a servo motor for directional movement.
- The integration of visual software provided a user-friendly interface to monitor and analyze the radar's output.
- Through this project, I demonstrated my proficiency in IoT technology, hardware interfacing, and software integration

# WATER ALARM SYSTEM PCB DESIGN | PCB MANUFACTURING AND DESIGN, |See Project

- conceived and executed a water alarm system, showcasing my expertise in PCB design and manufacturing.
- project involved creating a custom PCB to accommodate water sensors, signal processing components, and an alarm module.
- By seamlessly integrating hardware and design,
- I demonstrated my ability to transform conceptual ideas into practical, functional solutions.

### AWARDS AND ACHIEVEMENTS

2023	1st round	Coding Ninjas CodeKaze Challenge
2023	500+	DSA & CP questions on multiple platform
2023	web developer	Google developer student club