Roll No. Total No. of Questions: 9] [Total No. of Printed Pages: 4 (2102)BCA (CBCS) RUSA IIIrd Semester Examination 3993 COMPUTER ORGANIZATION BCA-303 Time: 3 Hours] [Maximum Marks: 70 Note: - Attempt five questions in all, selecting one question each from Units-I, II, III and IV. Q. No. 1 (Part-A) is compulsory. Part-A (Compulsory Question) 1. (A) Attempt all parts: Decimal equivalent of (2FAOC)₁₆ is (i) Floating Point representation is used to (ii) store numbers. (iii) A binary digit is called a 1,1,1 State whether the statement is True or False: (iv) In computers, subtraction is generally carried out by using 10's Complement.

(1)

C - 765

+

(True/False)

Turn Over

(v)	A group of bits that tells the computer to				(c) Indirect Address Mode
	perform specific task is known as micro- operation. (True/False)				(d) Relative Address Mode
Ans	wer the following MCQs by selecting the			(ix)	The circuit converting binary data into
	appropriate option :				decimal is:
(vi)	Which method/s of representation of				(a) Multiplexer (b) Encoder
	numbers occupies a large amount of				(c) Decoder (d) Code converter
	memory than others ?			(x)	The main virtue for using single Bus
	(a) Sign-magnitude				system is
	(b) 1's complement				(a) Fast data transfers
	(c) 2's complement				(b) Cost effective connectivity and speed
	(d) None of these				(c) Cost effective connectivity and ease
(vii)	are the different type/s of generating control signals.				of attaching peripheral devices
	(a) Micro-programmed				(d) None of the mentioned
	(b) Hardwired		(B)	Ans	wer the following in 25 to 50 words:
	(c) Micro-instruction			(i)	What is common bus system ? Why is it
	(d) Both Micro-programmed and				required ?
	Hardwired	1		(ii)	Perform Y-X using 2's complement, where
(viii)	In which of the following mode effective address is equal to the address part of the				Y = 1000011 and $X = 1010100$.
				(iii)	Discuss logic micro-operations.
	instruction :			(iv)	What do you mean by Control Memory ?
	(a) Indexed Addressing Mode			(v)	Discuss types of Interrupt. $4 \times 5 = 2$
	(b) Direct Address Mode		C-76	5	
5	(2)		0-76	9	(3) Turn Ove

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Part-B

Unit-I

2.	Perform the following Conversions:				
	(i) $(F3A7C2)_{16} = ()_{10} = ()_8 = ()_2$				
	(ii) $(59282)_{10} = (\dots)_{16} = (\dots)_{8} = (\dots)_{2}$	10			
3.	(i) What is BCD? How is it different from conversion from decimal to binary numbers?				
	(ii) Perform the subtraction of unsigned decimal numbers by taking 10's complement of the subtrahend 1753–8640.	5,5			
	Unit-II				
4.	What is Register Transfer? Discuss the basic symbols				
	for Register transfer and also explain control function.	10			
5.	Discuss the working of 4-bit arithmetic circuit.	10			
	Unit-III	*			
6.	What is an Instruction Format? Discuss most common fields present in an instruction format.	10			
7.	Discuss the Timing and Control ? Also explain				
	hardwired and micro-programmed control.	10			
	Unit-IV				
8.	Discuss the General Register Organization? Also give the bus organization for seven CPU Registers.	10			
9.	(i) What do you mean by Zero-Address instruction? Where is it used?				
	(ii) Write a short note on Reverse Polish Notation.	5,5			
C-	-765 (4)				

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