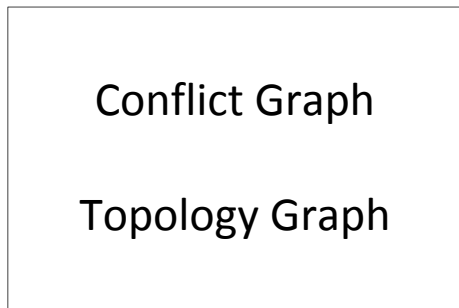


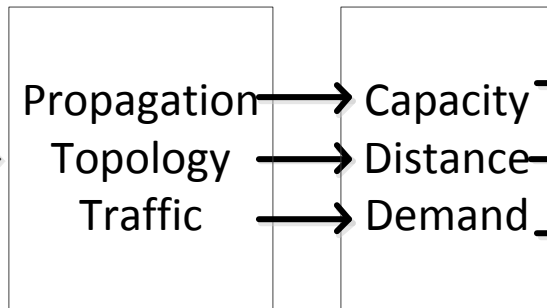
Topology

(Fat-tree/VL2 or Cayley)



Addressing

Measurement (Compress Sensing)



PA-LA



AA-LA

Routing

Scheduling (Logical Addressing)

