

Steve Yoon

INFRASTRUCTURE ENGINEER · GOPHER

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Skills

Programming	Go, Python3, C++, Javascript, Ruby
DevOps	Kubernetes, Terraform, Helm, Ksonnet, EFK, Operator, Docker, Ansible, Prometheus, Grafana, Chef, Sensu
Reliability	Distributed System, Disaster Recovery, Monitoring, Alerting/OnCall
Web	Go, Javascript/Node, Typescript, React/Redux/ReactNative, Tornado/Flask/Django, HTML5/CSS3/SASS
Languages	Korean, English

Experience

Member of Technical Staff

Remote

CHRONOSPHERE

Nov. 2023 - Present

- Rapid Response Team building observability features to get customers signed
- Built observability features responsible for 10s of million dollars in revenue
- GCP Monitoring Integration / Metrics Ingestion Pipeline
- Alert Rule Evaluation
- Public/Internal API (GRPC/HTTP/GraphQL)
- SLO / Monitoring

Senior Site Reliability Engineer

Remote

FULLSTORY

Feb. 2021 - Nov. 2023

- Infrastructure Security Compliance / Customer Checklist Audit and Implementation
- GCP IAM permission management service, permission audit, OSLogin, workload identity
- Metrics / Compute Scaling and Cluster Migration / Upgrade
- Migrate to infrastructure-as-a-code through Terraform / Atlantis / Ansible
- Custom CI/CD solution for Kubernetes and Compute instances

Platform Engineer

Remote

COMPOSE, AN IBM COMPANY

Jan. 2017 - Feb 2021

- Database-as-a-Service orchestration engine for Compose / IBM Cloud Database (PostgreSQL, MongoDB, ElasticSearch, RabbitMQ and more)
- Database administration automation (provision, backup, scaling, resync, recovery etc.)
- Go microservices, Kubernetes operators, CRD, Jobs to manage database replicas
- Monitoring infrastructure through kube-prometheus (prometheus and grafana), ELK, Sensu

Sergeant, Software Engineering

Seoul, S.Korea

REPUBLIC OF KOREA CYBER COMMAND

Feb. 2015 - Nov. 2016

- Cybersecurity web services

ASIC Design Engineer 2

Markham, Canada

ADVANCED MICRO DEVICES INC.

Aug. 2013 - Oct. 2014

- PCIe post-silicon debugging
- PCS IP (PCIe, USB, SATA) design verification
- C++, Perl, System Verilog, Verilog, UVM

Software Engineer (Java)

AVERSAN INC.

- Custom reverse proxy and test automation framework for eHealth Ontario
- Java, Netty, Spring, Hibernate, Maven, Oracle 11g, Selenium

Mississauga, Canada

Nov. 2012 - May. 2013

ASIC Design Engineer (PEY)

ADVANCED MICRO DEVICES INC.

- DfX IP (DFT/DFD) design verification
- IEEE standards (JTAG, P1500)
- C++, Perl, System Verilog, Verilog

Markham, Canada

May. 2010 - Sep. 2011

Education

University of Toronto

B.A.SC. COMPUTER ENGINEERING

- Certificate in Entrepreneurship, Innovation and Small Business

Toronto, Canada

2007 - 2012

Open Source

★ 1178 **GoCelery**, Celery Distributed Task Queue in Go - <https://github.com/gocelery/gocelery>